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Fairchild Semiconductor KSA812YMTF

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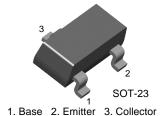




KSA812

Low Frequency Amplifier

- Collector-Base Voltage : V_{CBO}= -60V
- Complement to KSC1623



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

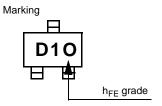
Symbol	Parameter	Ratings	Units
V _{CBO}	Collector-Base Voltage	-60	V
V _{CEO}	Collector-Emitter Voltage	-50	V
V _{EBO}	Emitter-Base Voltage	-5	V
С	Collector Current	-100	mA
P _C	Collector Power Dissipation	150	mW
ТЈ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	V _{CB} = -60V, I _E =0			-0.1	μΑ
I _{EBO}	Emitter Cut-off Current	V _{EB} = -5V, I _C =0			-0.1	μΑ
h _{FE}	DC Current Gain	V_{CE} = -6V, I_{C} = -1mA	90	200	600	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -100mA, I _B = -10mA		-0.18	-0.3	V
V _{BE} (on)	Base-Emitter On Voltage	V_{CE} = -6V, I_{C} = -1mA	-0.55	-0.62	-0.65	V
f _T	Current Gain Bandwidth Product	V _{CE} = -6V, I _C = -10mA		180		MHz
C _{ob}	Output Capacitance	V _{CB} = -10V, I _E =0, f=1MHz		4.5		pF

h_{FF} Classification

• •					
Classification	0	Y	G	L	
h _{FF}	90 ~ 180	135 ~ 270	200 ~ 400	300 ~ 600	



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Typical Characteristics

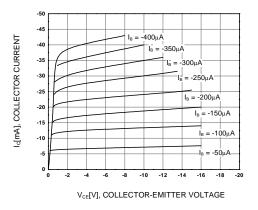


Figure 1. Static Characteristic

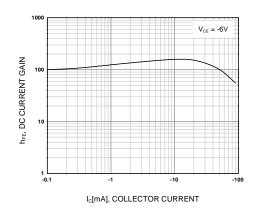


Figure 2. DC current Gain

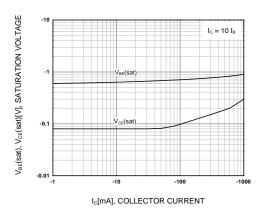


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

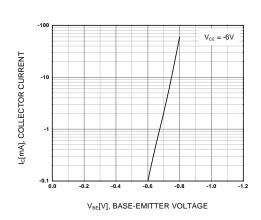


Figure 4. Base-Emitter On Voltage

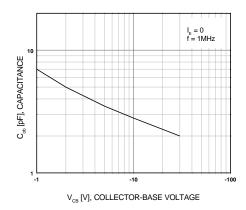


Figure 5. Collector Output Capacitance

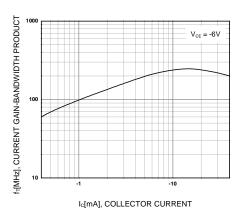


Figure 6. Current Gain Bandwidth Product

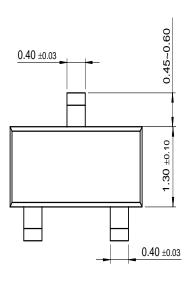
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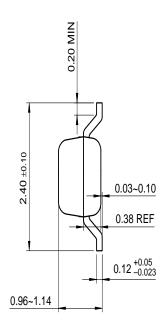
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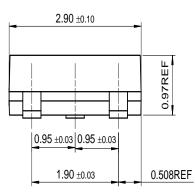


Package Demensions

SOT-23







Dimensions in Millimeters

Distributor of Fairchild Semiconductor: Excellent Integrated System LimitedDatasheet of KSA812YMTF - TRANS PNP 50V 0.1A SOT23

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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