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Fairchild Semiconductor FJV3109RMTF

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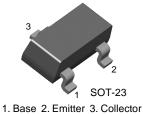




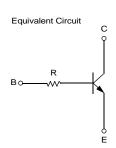
# **FJV3109R**

## Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R=4.7KΩ)
- Complement to FJV4109R







# **NPN Epitaxial Silicon Transistor**

# **Absolute Maximum Ratings** T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CBO</sub>	Collector-Base Voltage	40	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	40	V	
$V_{EBO}$	Emitter-Base Voltage	5	V	
С	Collector Current	100	mA	
Pc	Collector Power Dissipation	200	mW	
T <sub>J</sub>	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature	-55 ~ 150	°C	

# Electrical Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> =100μA, I <sub>E</sub> =0	40			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>E</sub> =1mA, I <sub>B</sub> =0	40			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB}$ =30V, $I_{E}$ =0			0.1	μΑ
h <sub>FE</sub>	DC Current Gain	V <sub>CE</sub> =5V, I <sub>C</sub> =1mA	100		600	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =10mA, I <sub>B</sub> =1mA			0.3	V
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> =10V, I <sub>E</sub> =0 f=1MHz		3.70		pF
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> =10V, I <sub>C</sub> =5mA		250		MHz
R	Input Resistor		3.2	4.7	6.2	ΚΩ

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# **Typical Characteristics**

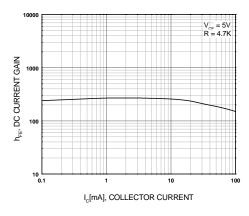


Figure 1. DC current Gain

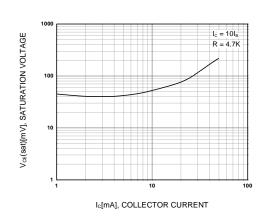


Figure 2. Collector-Emitter Saturation Voltage

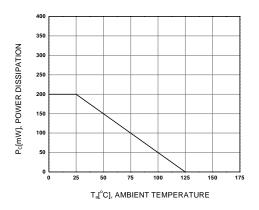
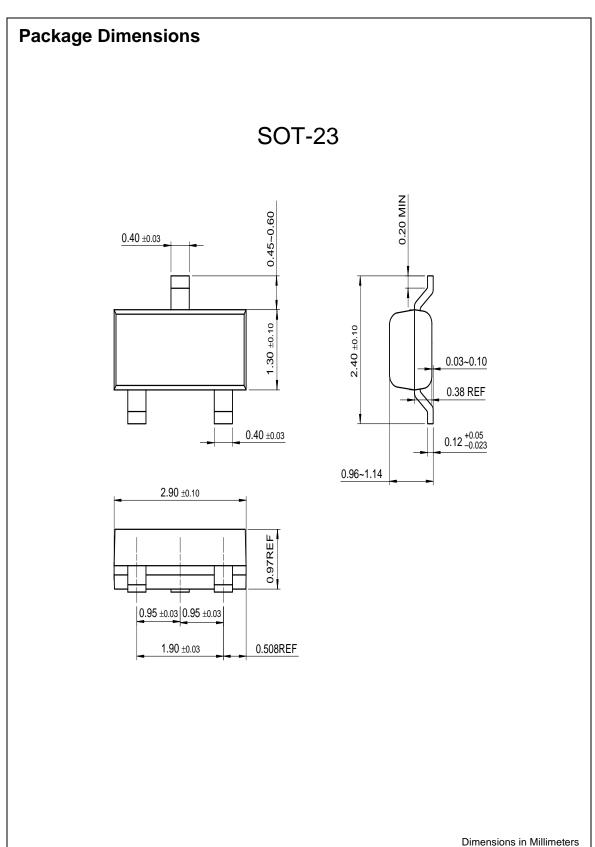


Figure 3. Power Derating

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