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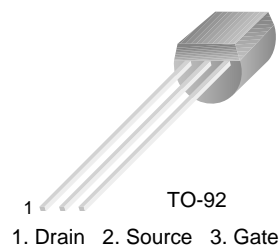


# U1897

## N-Channel JFET Switch

### Features

- This device is designed for low level analog switching, sample and hold circuits and chopper stabilized amplifiers.
- Sourced from Process 51.
- See J111 for characteristics.



### Absolute Maximum Ratings \* T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>DG</sub>	Drain-Gate Voltage	40	V
V <sub>GS</sub>	Gate-Source Voltage	-40	V
I <sub>GF</sub>	Forward Gate Current	50	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

**NOTES:**

- 1) These ratings are based on a maximum junction temperature of 150°C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### Thermal Characteristics\* T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
P <sub>D</sub>	Total Device Dissipation	625	mW
	Derate above 25°C	5.0	mW/°C
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	125	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient	357	°C/W

\* Device mounted on FR-4 PCB 1.6" × 1.6" × 0.06"

**Electrical Characteristics \*** T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Max	Units
<b>Off Characteristics</b>					
V <sub>(BR)GS</sub>	Gate-Source Breakdown Voltage	I <sub>G</sub> = -1.0 μA, V <sub>DS</sub> = 0	-40		V
V <sub>GS(off)</sub>	Gate-Source Cutoff Voltage	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 1.0 nA	-5.0	-10	V
I <sub>DGO</sub>	Drain-Gate Leakage Current	V <sub>DG</sub> = 20 V, I <sub>S</sub> = 0		-200	pA
<b>On Characteristics</b>					
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current *	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0	30		mA
r <sub>DS(on)</sub>	Static Drain-Source On Resistance	I <sub>D</sub> = 1.0 mA, V <sub>GS</sub> = 0		30	Ω
<b>Small Signal Characteristics</b>					
r <sub>ds(on)</sub>	Drain-Source On Resistance	V <sub>DS</sub> = V <sub>GS</sub> = 0, f = 1.0 kHz		30	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 20, V <sub>GS</sub> = 0, f = 1.0 MHz		16	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	V <sub>GS</sub> = -20 V, f = 1.0 MHz		3.5	pF
<b>Switching Characteristics</b>					
t <sub>on</sub>	Turn-On Time	I <sub>D(on)</sub> = 6.6 mA		25	ns
t <sub>off</sub>	Turn-Off Time	V <sub>GS(off)</sub> = 12.0 V		40	ns

\* Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2.0%




**NOTES:**

1. These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
3. These ratings are based on a maximum junction temperature of 150degrees C.



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