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<u>Fairchild Semiconductor</u> <u>TIS75</u>

For any questions, you can email us directly: sales@integrated-circuit.com

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TIS75

N-Channel General Purpose Amplifier

- · This device is designed for low level analog switching, sample and hold circuits and chopper stabilized amplifiers.
- · Sourced from process 54.



Absolute Maximum Ratings * Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	30	V
V _{GS}	Gate-Source Voltage	-30	V
I _{GF}	Forward Gate Current	10	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 ~ +150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

- These ratings are based on a maximum junction temperature of 150 degrees C.
 These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
Off Charac	cteristics	•	•			•
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$	-30			V
GSS	Gate Reverse Current	V _{GS} = 15V, V _{DS} = 0			-2.0	nA
		$V_{GS} = 15V, V_{DS} = 0, T_a = 100^{\circ}C$			-5.0	μΑ
l _D (off)	Drain Cutoff Leakage Current	V _{DS} = 15V, V _{GS} = -10V			-2.0	nA
		$V_{DS} = 15V, V_{GS} = -10V,$			-5.0	μΑ
		T _a = 100°C				
$V_{GS}(off)$	Gate-Source Cutoff Voltage	$V_{DS} = 20V, I_{D} = 4.0nA$	-0.8		-4.0	V
On Charac	cteristics *					
I _{DSS}	Zero-Gate Voltage Drain Current *	V _{DS} = 15V, V _{GS} = 0	8		80	mA
{DS} (on)	Drain-Source On Resistance	$V{DS} \le 0.1 V, V_{GS} = 0$			60	Ω
Small Sigr	nal Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 0$, $V_{GS} = -10V$, $f = 1.0MHz$			18	pF
C _{rss}	Reverse Transfer Capacitance	$V_{DS} = 0$, $V_{GS} = -10V$, $f = 1.0MHz$			8.0	pF
Switching	Characteristics					
t _r	Rise Time	$V_{GS}(off) = -4.0V, V_{GS}(on) = 0,$			10	ns
t _{on}	Turn-On Time	$I_D = 5.0 \text{mA}, V_{DS} = 10 \text{V}$			10	ns
t _{off}	Turn-Off Time				100	ns

^{*} Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 3.0%



Datasheet of TIS75 - JFET N-CH 30V 0.35W TO92 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

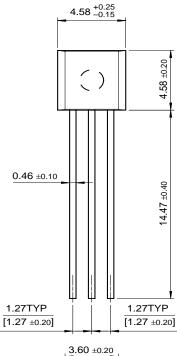
Thermal Characteristics T _a =25°C unless otherwise noted				
Symbol	Parameter	Max.	Units	
P _D	Total Device Dissipation Derate above 25°C	350 2.8	mW mW/ [°] C	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W	

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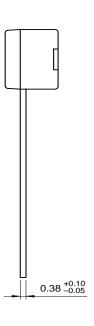




1.02 ±0.10 0.38 ^{+0.10} 0.38 −0.05



(R2.29)



Dimensions in Millimeters

Distributor of Fairchild Semiconductor: Excellent Integrated System Limited Datasheet of TIS75 - JFET N-CH 30V 0.35W TO92

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Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
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