

### KSD1588

## **Low Frequency Power Amplifier**

- Low Speed Switching
- Complement to KSB1097



## **NPN Epitaxial Silicon Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	100	V
V <sub>CEO</sub>	Collector-Emitter Voltage	60	V
V <sub>EBO</sub>	Emitter-Base Voltage	7	V
I <sub>C</sub>	Collector Current (DC)	7	Α
I <sub>CP</sub>	*Collector Current (Pulse)	15	Α
I <sub>B</sub>	Base Current	3.5	Α
P <sub>C</sub>	Collector Dissipation (T <sub>a</sub> =25°C)	2	W
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	30	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 ~ 150	°C

<sup>\*</sup> PW≤300μs, Duty Cycle≤10%

### Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 80V, I_{E} = 0$		10	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$		10	μΑ
h <sub>FE1</sub>	*DC Current Gain	$V_{CE} = 1V, I_{C} = 3A$	40	200	
h <sub>FE2</sub>		$V_{CE} = 1V, I_{C} = 5A$	20		
V <sub>CE</sub> (sat)	*Collector-Emitter Saturation Voltage	$I_C = 5A, I_B = 0.5A$		0.5	V
V <sub>BE</sub> (sat)	*Base-Emitter Saturation Voltage	$I_C = 5A, I_B = 0.5A$		1.5	V

<sup>\*</sup> Pulse Test: PW≤350μs, Duty Cycle≤2%

## **h**<sub>FE1</sub> Classification

Classification	R	0	Υ
h <sub>FE1</sub>	40 ~ 80	80 ~ 120	100 ~ 200

# **Typical Characteristics**

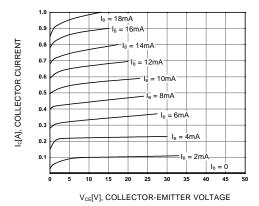


Figure 1. Static Characteristic

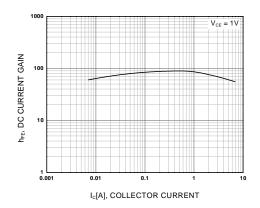


Figure 2. DC current Gain

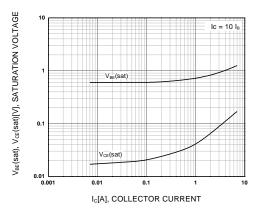


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

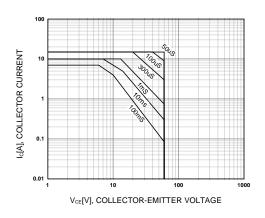


Figure 4. Safe OPerating Area

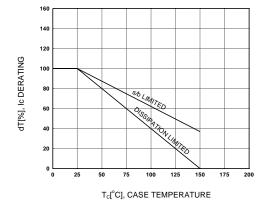


Figure 5. Derating Curve Safe Operating Area

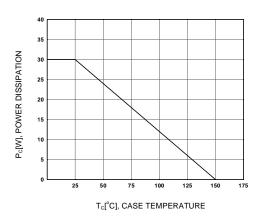
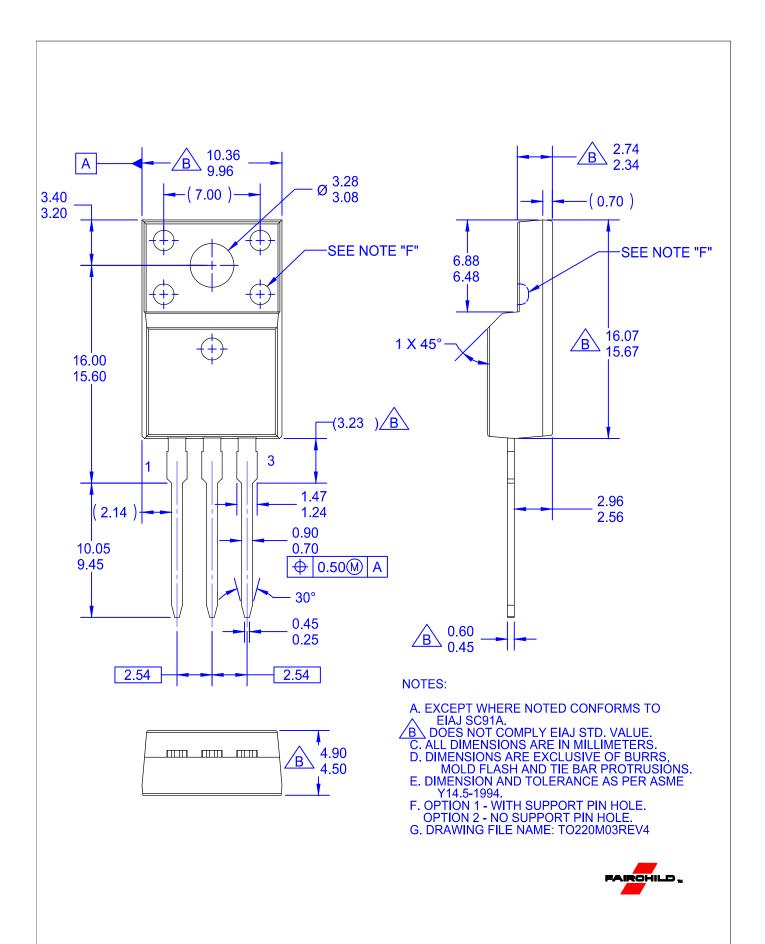


Figure 6. Power Derating

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Rev. 177

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