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Avago Technologies US, Inc. 5082-0012

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Distributor of Avago Technologies US, Inc.: Excellent Integrated System Limited Datasheet of 5082-0012 - DIODE PIN CHIP MIC SW/ATTENUATOR Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

5082-0012 PIN Diode Chip for Hybrid MIC Switches/Attenuators

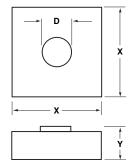


Data Sheet

Description

This PIN diode chip is silicon dioxide or nitride passivated. The 5082-0012 has a planar construction. The fabrication processes are optimized for long term reliability and tightly controlled for uniformity in electrical performance.

Outline



DIMENSIONS				
D	0.10			
±0.03 (1)	(4)			
Х	0.38			
±0.05 (2)	(15)			
Y	0.23			
±0.03 (1)	(9.0)			
Top Contact	Au. Cathode			
Bottom Contact	Au. Anode			

Dimensions in millimeters (1/1000 inch)

Features

• Low Series Resistance: 1.0 Ω Typical

Applications

This general purpose PIN diode is intended for low power switching applications such as duplexers, antenna switching matrices, digital phase shifters, time multiplex filters, TR switches, pulse and amplitude modulators, limiters, leveling circuits, and attenuators.

Maximum Ratings

Typical Parameters

Electrical Specifications at $T_A = 25^{\circ}C$

A Provide A Prov						
Chip Part Number 5082-	Nearest Equivalent Packaged Part No. 5082-	Minimum Breakdown Voltage V _{BR} (V)	Maximum Junction Capacitance C _j (pF)	Typical Series Resistance R _S (Ω)	Typical Lifetime τ (ns)	Typical Reverse Recovery Time t _{rr} (ns)
0012	3001	150	0.12	1.0	400	100
Test Conditions		$\label{eq:VR} \begin{split} V_{\rm R} &= V_{\rm BR} \\ Measure \\ I_{\rm R} &\leq 10 \ \rm mA \end{split}$	$V_{\rm R} = 50 \text{ V}$ $f = 1 \text{ MHz}$	$I_{\rm F} = 100 \text{ mA}$ $f = 100 \text{ MHz}$	$I_{\rm F} = 50 \text{ mA}$ $I_{\rm R} = 250 \text{ mA}$	$I_{\rm F} = 20 \text{ mA}$ $V_{\rm R} = 10 \text{ V}$ 90% Recovery



Assembly and Handling Procedures for PIN Chips

1. Storage

Devices should be stored in a dry nitrogen purged dessicator or equivalent.

2. Cleaning

If required, surface contamination may be removed with electronic grade solvents. Typical solvents, such as freon (T.F. or T.M.C.), acetone, deionized water, and methanol, or their locally approved equivalents, can be used singularly or in combinations. Typical cleaning times per solvent are one to three minutes. DI water and methanol should be used (in that order) in the final cleans. Final drying can be accomplished by placing the cleaned dice on clean filter paper and drying with an infrared lamp for 5-10 minutes. Acids such as hydrofluoric (HF), nitric (HNO₃) and hydrochloric (HCl) should not be used.

The effects of cleaning methods/solutions should be verified on small samples prior to submitting the entire lot.

Following cleaning, dice should be either used in assembly (typically within a few hours) or stored in clean containers in a reducing atmosphere or a vacuum chamber.

3. Die Attach

a. Eutectic

AuSn preform with stage temperature of 310° C for one minute max. AuGe preform with stage temperature of 390° C for one minute max.

b. Epoxy

For epoxy die-attach, conductive silver-filled or goldfilled epoxies are recommended. This method can be used for all Avago PIN chips.

4. Wire Bonding

Either ultrasonic or thermocompression bonding techniques can be employed. Suggested wire is pure gold, 0.7 to 1.5 mil diameter.

For product information and a complete list of distributors, please go to our web site: www.avagotech.com

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