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ON Semiconductor BC856BDW1T1

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Distributor of ON Semiconductor: Excellent Integrated System Limited Datasheet of BC856BDW1T1 - TRANS 2PNP 65V 0.1A SOT363 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

BC856BDW1T1, BC857BDW1T1 Series, BC858CDW1T1 Series

Preferred Devices

Dual General Purpose Transistors

PNP Duals

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-363/SC-88 which is designed for low power surface mount applications.

Features

• Pb–Free Packages are Available

MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Collector – Emitter Voltage	BC856 BC857 BC858	V _{CEO}	-65 -45 -30	V
Collector-Base Voltage	BC856 BC857 BC858	V _{CBO}	-80 -50 -30	V
Emitter-Base Voltage		V _{EBO}	-5.0	V
Collector Current –Continuous		Ι _C	-100	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation Per Device FR-5 Board (Note 1) $T_A = 25^{\circ}C$ Derate Above 25°C	P _D	380 250 3.0	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	328	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

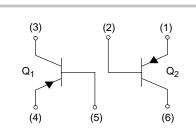
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in



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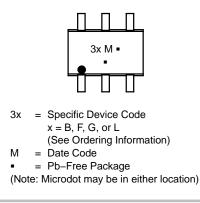
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SOT-363/SC-88 CASE 419B STYLE 1

MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.



BC856BDW1T1, BC857BDW1T1 Series, BC858CDW1T1 Series

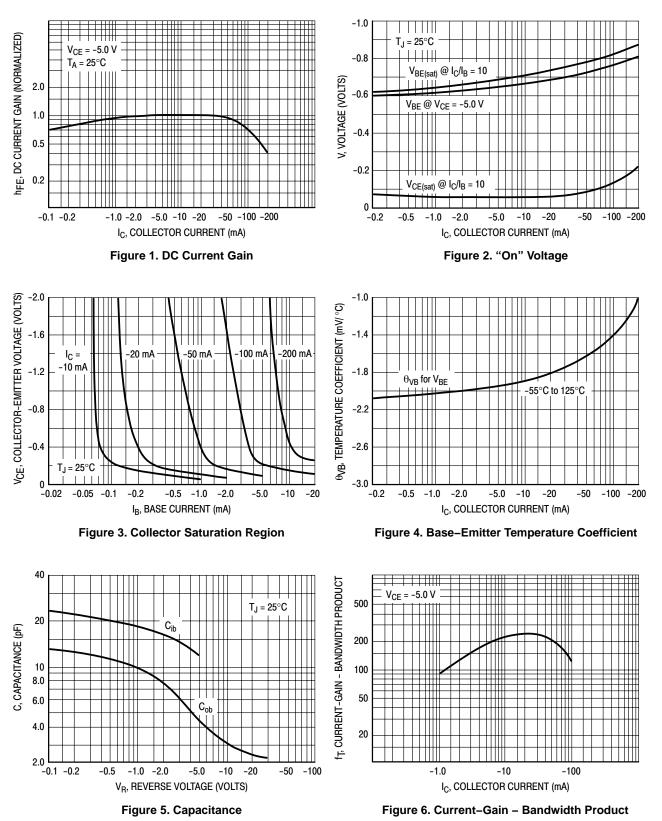
ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I _C = –10 mA) BC856 Series BC857 Series BC858 Series	V _{(BR)CEO}	-65 -45 -30			V
	V _{(BR)CES}	-80 -50 -30		- - -	V
	V _{(BR)CBO}	80 50 30			V
Emitter – Base Breakdown Voltage (I _E = –1.0 µA) BC856 Series BC857 Series BC858 Series	V _{(BR)EBO}	-5.0 -5.0 -5.0			V
Collector Cutoff Current (V _{CB} = -30 V) (V _{CB} = -30 V, T _A = 150° C)	І _{СВО}			-15 -4.0	nA μA
ON CHARACTERISTICS		-			
DC Current Gain $(I_C = -10 \ \mu\text{A}, \ V_{CE} = -5.0 \ \text{V})$ BC856B, BC857B BC857C, BC858C	h _{FE}		150 270		-
(I _C = -2.0 mA, V _{CE} = -5.0 V) BC856B, BC857B BC857C, BC858C		220 420	290 520	475 800	
Collector – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)	V _{CE(sat)}			-0.3 -0.65	V
Base – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)	V _{BE(sat)}		-0.7 -0.9		V
Base – Emitter On Voltage ($I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$) ($I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ V}$)	V _{BE(on)}	-0.6 -		-0.75 -0.82	V
SMALL-SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product ($I_c = -10$ mA, $V_{CE} = -5.0$ Vdc, f = 100 MHz)	f _T	100	_	-	MHz
Output Capacitance ($V_{CB} = -10 \text{ V}, \text{ f} = 1.0 \text{ MHz}$)	C _{ob}	-	-	4.5	pF
Noise Figure (I _C = -0.2 mA, V _{CE} = -5.0 Vdc, R _S = 2.0 k Ω , f = 1.0 kHz, BW = 200 Hz)	NF	-	-	10	dB



BC856BDW1T1, BC857BDW1T1 Series, BC858CDW1T1 Series

TYPICAL CHARACTERISTICS – BC856

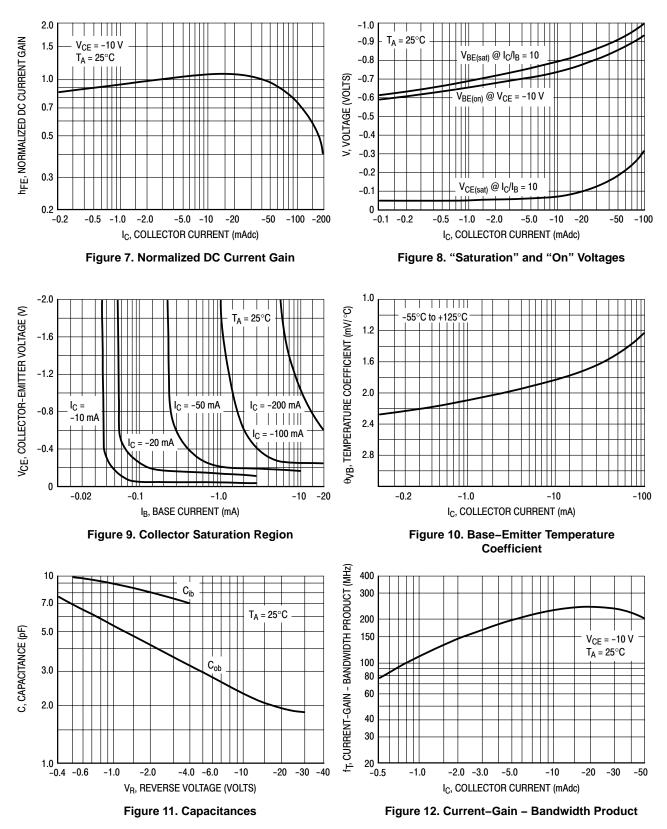




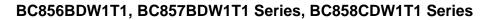
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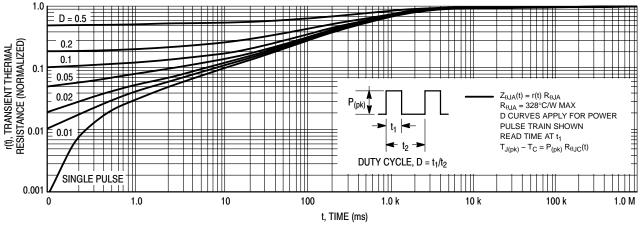
BC856BDW1T1, BC857BDW1T1 Series, BC858CDW1T1 Series

TYPICAL CHARACTERISTICS – BC857/BC858











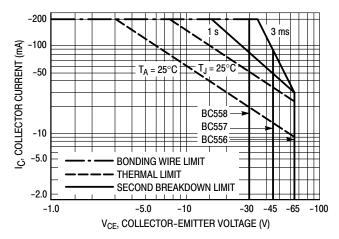


Figure 14. Active Region Safe Operating Area

The safe operating area curves indicate I_C-V_{CE} limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 14 is based upon $T_{J(pk)} = 150^{\circ}$ C; T_{C} or T_{A} is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}$ C. $T_{J(pk)}$ may be calculated from the data in Figure 13. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

Device	Device Marking	Package	Shipping [†]	
BC856BDW1T1	3В	SOT-363		
BC856BDW1T1G	3В	SOT-363 (Pb-Free)	3,000 / Tape & Reel	
BC856BDW1T3	3В	SOT-363		
BC856BDW1T3G	3В	SOT-363 (Pb-Free)	10,000 / Tape & Reel	
BC857BDW1T1	3F	SOT-363	3,000 / Tape & Reel	
BC857BDW1T1G	3F	SOT-363 (Pb-Free)		
BC857CDW1T1	3G	SOT-363	3,000 / Tape & Reel	
BC857CDW1T1G	3G	SOT-363 (Pb-Free)		
BC858CDW1T1	3L	SOT-363	3,000 / Tape & Reel	
BC858CDW1T1G	3L	SOT-363 (Pb-Free)		

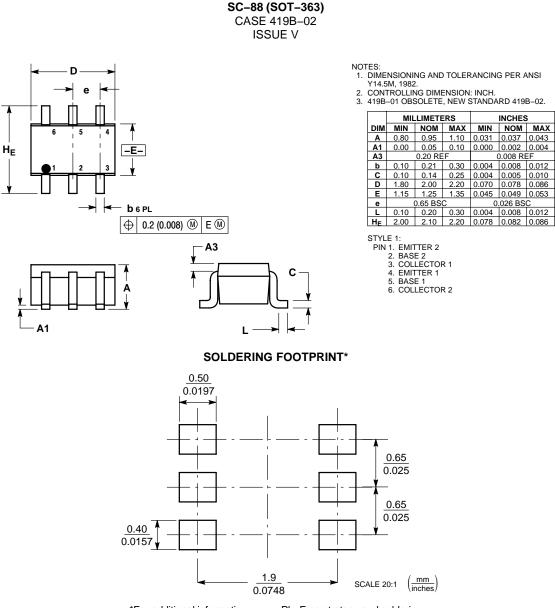
ORDERING INFORMATION

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



BC856BDW1T1, BC857BDW1T1 Series, BC858CDW1T1 Series

PACKAGE DIMENSIONS



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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