

# **Excellent Integrated System Limited**

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ON Semiconductor MMDL6050T1

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



Distributor of ON Semiconductor: Excellent Integrated System Limited Datasheet of MMDL6050T1 - DIODE GEN PURP 70V 200MA SOD323 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

## MMDL6050T1G, SMMDL6050T1G

# **Switching Diode**

#### Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant\*

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V <sub>R</sub>	70	Vdc
Peak Forward Current	١ <sub>F</sub>	200	mAdc
Peak Forward Surge Current	I <sub>FM(surge)</sub>	500	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) @T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	200 1.57	mW mW/°C
Thermal Resistance, Junction-to-Ambient	RθJA	635	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	–55 to 150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-4 Minimum Pad.

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

LLCTRICAL CHARACTERISTICS (TA = 25 C unless otherwise noted)					
Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Reverse Breakdown Voltage (I <sub>(BR)</sub> = 100 μAdc)	V <sub>(BR)</sub>	70	-	Vdc	
Reverse Voltage Leakage Current (V <sub>R</sub> = 50 Vdc)	۱ <sub>R</sub>	-	0.1	μAdc	
Forward Voltage (I <sub>F</sub> = 1.0 mAdc) (I <sub>F</sub> = 100 mAdc)	V <sub>F</sub>	0.55 0.85	0.7 1.1	Vdc	
Reverse Recovery Time (I <sub>F</sub> = I <sub>R</sub> = 10 mAdc, I <sub>R(REC)</sub> = 1.0 mAdc) (Figure 1)	t <sub>rr</sub>	-	4.0	ns	
Capacitance (V <sub>R</sub> = 0 V)	С	-	2.5	pF	



### **ON Semiconductor**<sup>®</sup>

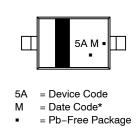
http://onsemi.com







#### MARKING DIAGRAM



(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MMDL6050T1G	SOD-323 (Pb-Free)	3,000 / Tape & Reel
SMMDL6050T1G	SOD-323 (Pb-Free)	3,000 / Tape & Reel

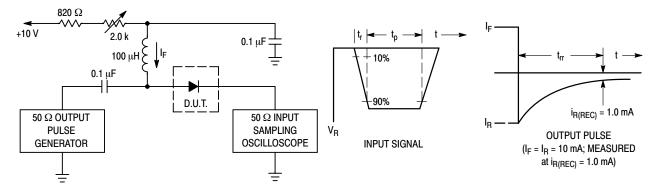
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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## MMDL6050T1G, SMMDL6050T1G



Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (I\_F) of 10 mA.

2. Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10 mA. 3.  $t_p \mathrel{\scriptstyle >} t_{rr}$ 

Figure 1. Recovery Time Equivalent Test Circuit

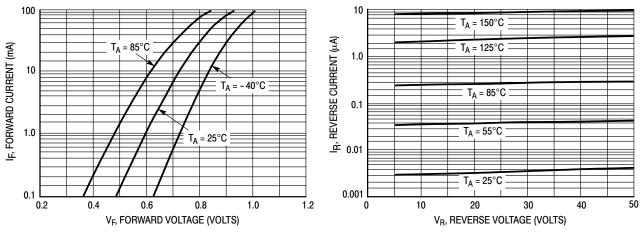
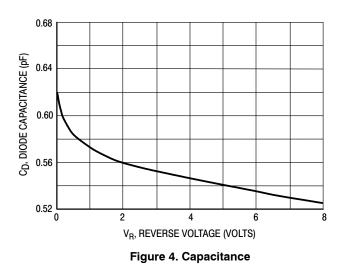




Figure 3. Leakage Current

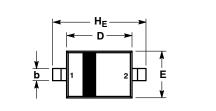


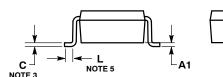


## MMDL6050T1G, SMMDL6050T1G

### PACKAGE DIMENSIONS

SOD-323 CASE 477-02 **ISSUE H** 





NOTES

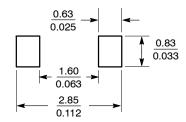
- VOTES:
  DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  CONTROLLING DIMENSION: MILLIMETERS.
  LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.
  DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
  DIMENSIONS A MOLE DO NOT AND A DO NOT INCLUDE MOLD
- 5. DIMENSION L IS MEASURED FROM END OF RADIUS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.031	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.15 REF			0.006 REF		
b	0.25	0.32	0.4	0.010	0.012	0.016
С	0.089	0.12	0.177	0.003	0.005	0.007
D	1.60	1.70	1.80	0.062	0.066	0.070
Е	1.15	1.25	1.35	0.045	0.049	0.053
L	0.08			0.003		
	0.00	0.50	0.70	0.000	0.000	0.405

H<sub>F</sub> 2.30 2.50 2.70 0.090 0.098 0.105 STYLE 1: PIN 1. CATHODE (POLARITY BAND)

2. ANODE

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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