

# **Excellent Integrated System Limited**

Stocking Distributor

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Vishay/Siliconix SI3867DV-T1-E3

For any questions, you can email us directly: sales@integrated-circuit.com





**Si3867DV** 

Vishay Siliconix

# P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)			
- 20	0.051 at V <sub>GS</sub> = - 4.5 V	- 5.1			
	0.067 at V <sub>GS</sub> = - 3.3 V	- 4.5			
	0.100 at V <sub>GS</sub> = - 2.5 V	- 3.7			

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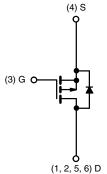
### **FEATURES**

- Halogen-free According to IEC 61249-2-21
- TrenchFET® Power MOSFET
- PWM Optimized
- Compliant to RoHS Directive 2002/95/EC

# COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- DC/DC
  - HDD
  - Power Supplies
- Portable Devices Such As Cell Phones, PDA, DSC, and DVC



P-Channel MOSFET

		TSOP- Top Vie		
T		1	6	
3 mm	Ш	2	5	Ш
		3	4	
	<b>-</b>			

Ordering Information: Si3867DV-T1-E3 (Lead (Pb)-free)

Si3867DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		$V_{DS}$	- 20		V	
Gate-Source Voltage		V <sub>GS</sub>	± 12			
0	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 5.1	- 3.9		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		- 3.7	- 2.8		
Pulsed Drain Current		I <sub>DM</sub>	- 20		Α	
Continuous Diode Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.7	- 0.9		
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	2.0	1.1	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C		1.0	0.6		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	R <sub>thJA</sub>	45	62.5		
	Steady State		90	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	25	30		

a. Surface Mounted on 1" x 1" FR4 board.

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Datasheet of SI3867DV-T1-E3 - MOSFET P-CH 20V 3.9A 6-TSOP

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### Si3867DV

# Vishay Siliconix



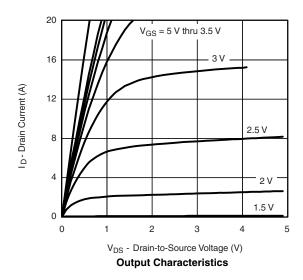
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{GS(th)}$ $V_{DS} = V_{GS}, I_{D} = -250 \mu A$			- 1.4	V	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 12 V			± 100	nA	
Zero Gate Voltage Drain Current		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V	-1		- 1	μΑ	
	I <sub>DSS</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	- 5		- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -5.1 \text{ A}$		0.041	0.051	Ω	
		$V_{GS} = -3.3 \text{ V}, I_D = -4.5 \text{ A}$		0.054	0.067		
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 2 A		0.081	0.100		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 5.1 A		11		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V		- 0.7	- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			7	11		
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5.1 A		2.3		nC	
Gate-Drain Charge	$Q_{gd}$			1.6		1	
Turn-On Delay Time	t <sub>d(on)</sub>			17	30		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$		31	50		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$		32	50	ns	
Fall Time	t <sub>f</sub>			30	50		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/μs		25	50		

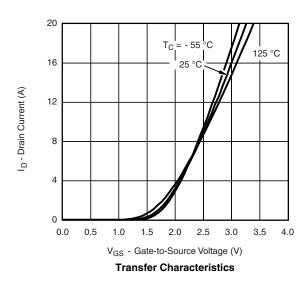
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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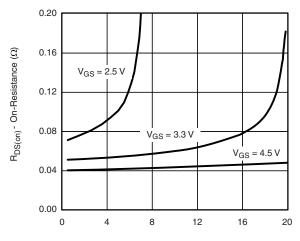
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 $V_{DS} = 10 \text{ V}$  $I_{D} = 5.1 \text{ A}$ 

### **Si3867DV**

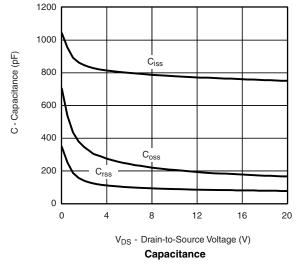
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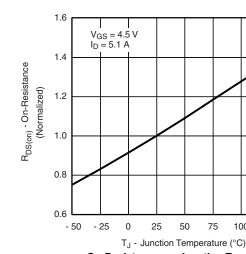
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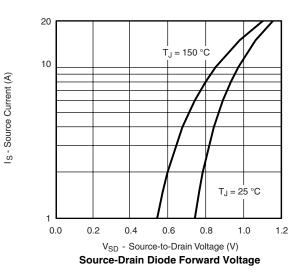
I<sub>D</sub> - Drain Current (A)

On-Resistance vs. Drain Current





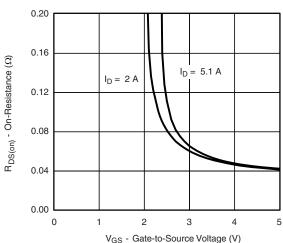
V<sub>GS</sub> - Gate-to-Source Voltage (V) 3 2 0 8 0  $\mathbf{Q}_{g}$  - Total Gate Charge (nC) **Gate Charge** 





75

100



On-Resistance vs. Gate-to-Source Voltage

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125



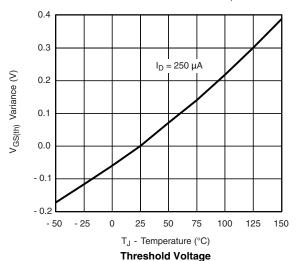
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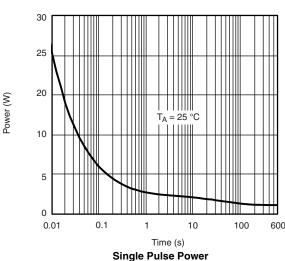
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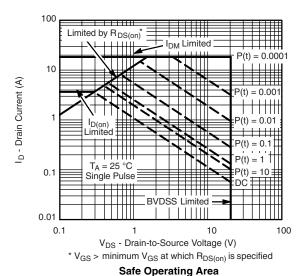
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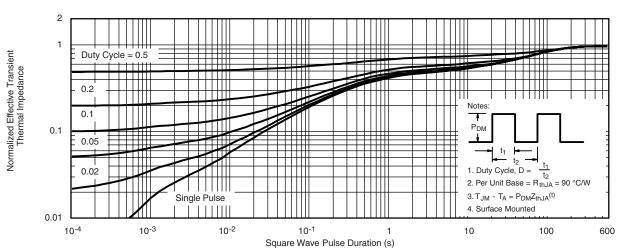
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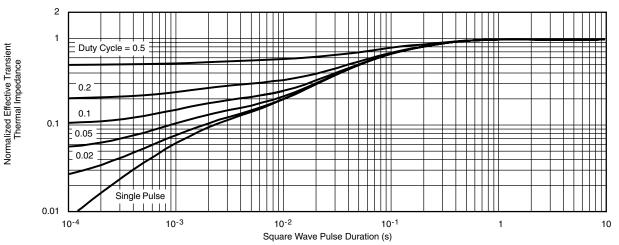
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Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?72068">www.vishay.com/ppg?72068</a>.

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