

# **Excellent Integrated System Limited**

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

Vishay/Siliconix SI9926BDY-T1-E3

For any questions, you can email us directly: sales@integrated-circuit.com





# Si9926BDY

Vishay Siliconix

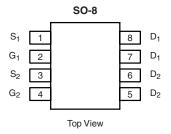
# **Dual N-Channel 2.5-V (G-S) MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
20	0.020 at V <sub>GS</sub> = 4.5 V	8.2		
	0.030 at V <sub>GS</sub> = 2.5 V	6.7		

#### **FEATURES**

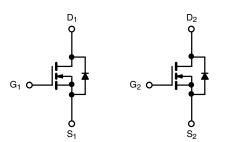
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFETS
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si9926BDY-T1-E3 (Lead (Pb)-free)

Si9926BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted					
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	20		V
Gate-Source Voltage		V <sub>GS</sub>	± 12		
O-ation - David One - 17	T <sub>A</sub> = 25 °C	I <sub>D</sub>	8.2	6.2	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		6.5	4.9	
Pulsed Drain Current		I <sub>DM</sub>	30		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.7	0.95	
2	T <sub>A</sub> = 25 °C	В	2.0	1.14	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	- P <sub>D</sub>	1.3	0.72	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian la Marrian de Analai and	t ≤ 10 s	- R <sub>thJA</sub>	52	62.5		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		1 thJA 90	90	110	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	32	40		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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# Distributor of Vishay/Siliconix: Excellent Integrated System Limited

Datasheet of SI9926BDY-T1-E3 - MOSFET 2N-CH 20V 6.2A 8-SOIC

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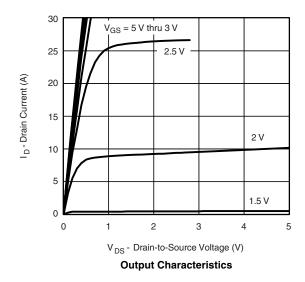
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.6		1.5	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	int loco	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1	μΑ	
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	30			Α	
Drain-Source On-State Resistance <sup>a</sup>	D	$V_{GS} = 4.5 \text{ V}, I_D = 8.2 \text{ A}$		0.016	0.020	0	
	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 3.3 \text{ A}$		0.024	0.030	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 8.2 A		29		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 1.7 A, V <sub>GS</sub> = 0 V		0.8	1.2	٧	
Dynamic <sup>b</sup>			•				
Total Gate Charge	$Q_g$			11	20		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 8.2 \text{ A}$		2.5		nC	
Gate-Drain Charge	Q <sub>gd</sub>			3.2			
Turn-On Delay Time	t <sub>d(on)</sub>			35	55		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 10 $\Omega$		50	75		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ 1 A, $V_{GEN}$ = 4.5 V, $R_g$ = 6 $\Omega$		31	50	ns	
Fall Time	t <sub>f</sub>			15	25		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.7 A, dI/dt = 100 A/μs		30	60		

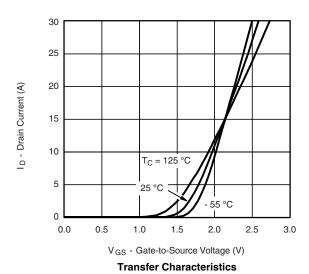
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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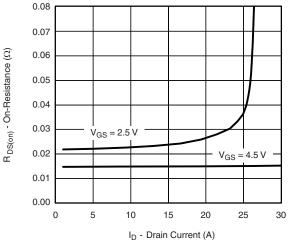




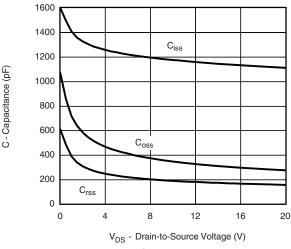
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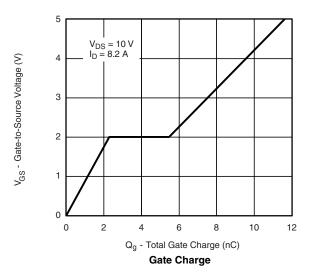
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#### On-Resistance vs. Drain Current

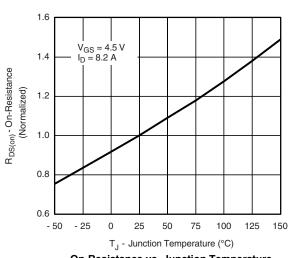


Capacitance

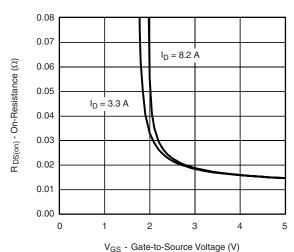


30 T<sub>J</sub> = 150 °C I<sub>S</sub> - Source Current (A) 10 T<sub>J</sub> = 25 °C 0 0 0.3 0.6 1.5 V<sub>SD</sub> - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage



On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

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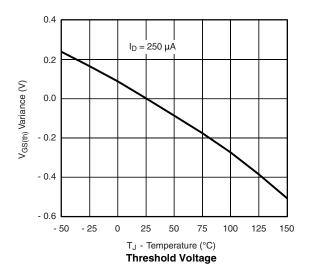
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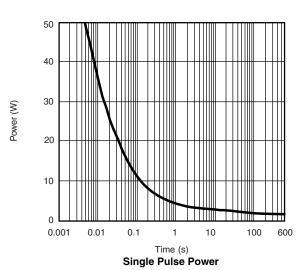
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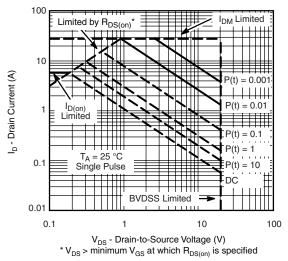
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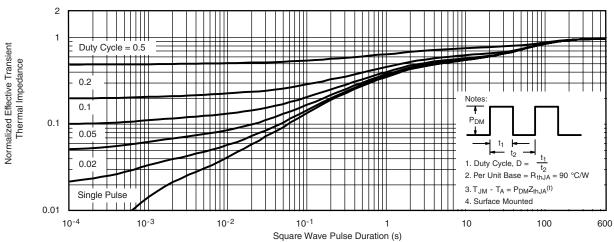








#### Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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Datasheet of SI9926BDY-T1-E3 - MOSFET 2N-CH 20V 6.2A 8-SOIC

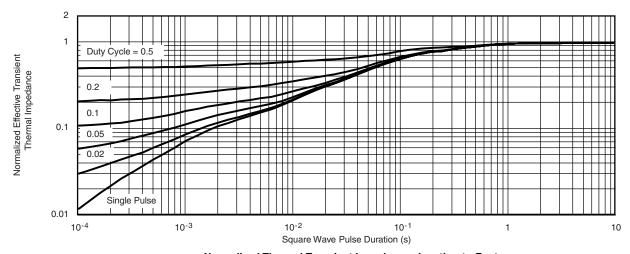
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72278.

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