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Vishay/Siliconix SI6463BDQ-T1-E3

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Vishay Siliconix

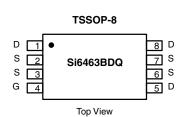
P-Channel 1.8 V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 20	0.015 at V _{GS} = - 4.5 V	- 7.4		
	0.020 at V _{GS} = - 2.5 V	- 6.3		
	0.027 at V _{GS} = - 1.8 V	- 5.5		

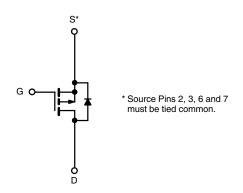
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si6463BDQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)							
Parameter		Symbol	10 s	Steady State	Unit		
Drain-Source Voltage		V _{DS}	- 20		V		
Gate-Source Voltage		V _{GS}	± 8				
O-atimos Paris Osamos /T 450 00\8	T _A = 25 °C	I _D	- 7.4	- 6.2			
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 5.9	- 4.9	٨		
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	- 30		Α		
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.35	- 0.95			
	T _A = 25 °C	P _D	1.5	1.05	W		
Maximum Power Dissipation ^a	T _A = 70 °C		1.0	0.67			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian de Angliant	t ≤ 10 s	R _{thJA}	65	83	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		100	120		
Maximum Junction-to-Foot	Steady State		46	56		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

Document Number: 72018 S10-2138-Rev. C, 20-Sep-10



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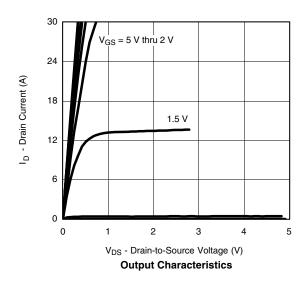
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			"	•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.45		- 0.8	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	4	
		V_{DS} = - 20 V, V_{GS} = 0 V, T_J = 70 °C			- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 20			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -7.4 \text{ A}$		0.011	0.015	0.015 0.020 Ω 0.027	
		$V_{GS} = -2.5 \text{ V}, I_D = -6.3 \text{ A}$		0.015	0.020		
		V _{GS} = - 1.8 V, I _D = - 5.5 A		0.020	0.027		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 7.4 A		34		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.3 A, V _{GS} = 0 V		- 0.64	- 1.1	V	
Dynamic ^b							
Total Gate Charge	Q_g			40	60	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -7.4 \text{ A}$		5.2			
Gate-Drain Charge	Q_{gd}			8			
Turn-On Delay Time	t _{d(on)}			35	55		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 15 Ω		40	60	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, $V_{GEN}=$ - 4.5 V, $R_g=$ 6 Ω		190	300		
Fall Time	t _f			90	150		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.3 A, dI/dt = 100 A/μs		75	120		

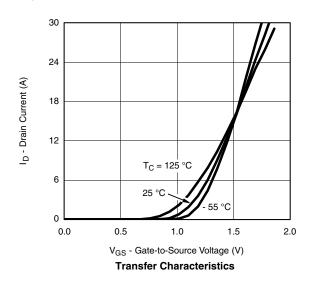
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



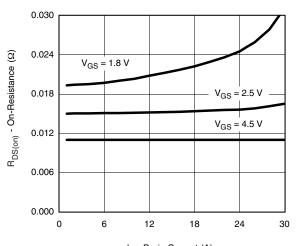




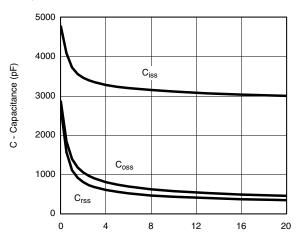


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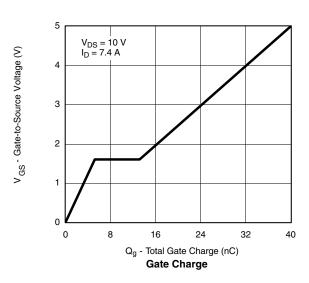
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



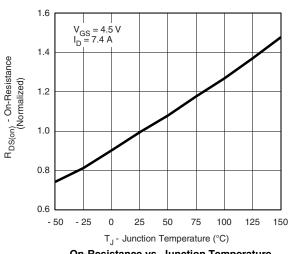
I_D - Drain Current (A) On-Resistance vs. Drain Current



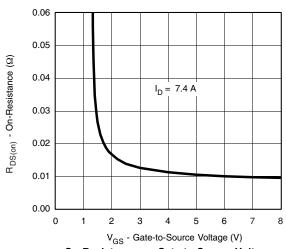
V_{DS} - Drain-to-Source Voltage (V) Capacitance



30 = 150 °C 10 $T_J = 25 \, ^{\circ}C$ 0.0 0.2 0.4 1.0 1.2 0.8 V_{SD} - Source-to-Drain Voltage (V) Source-Drain Diode Forward Voltage



On-Resistance vs. Junction Temperature



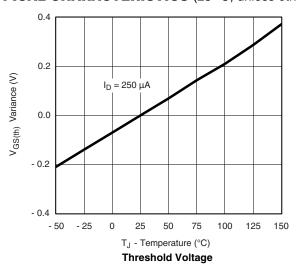
S - Source Current (A)

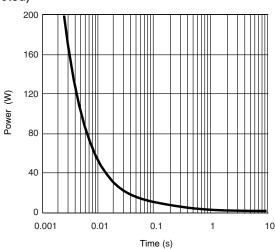


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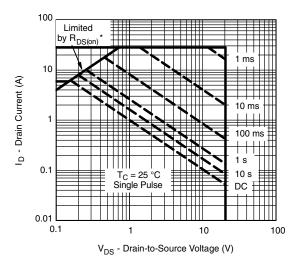
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

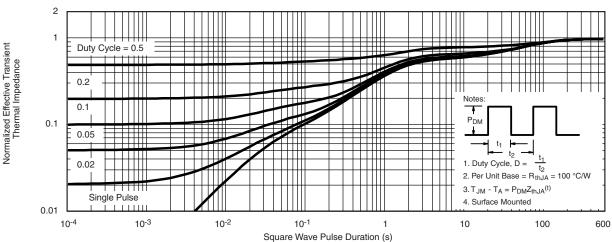




Single Pulse Power, Junction-to-Ambient



 * V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified **Safe Operating Area, Junction-to-Case**



Normalized Thermal Transient Impedance, Junction-to-Ambient

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Datasheet of SI6463BDQ-T1-E3 - MOSFET P-CH 20V 6.2A 8-TSSOP

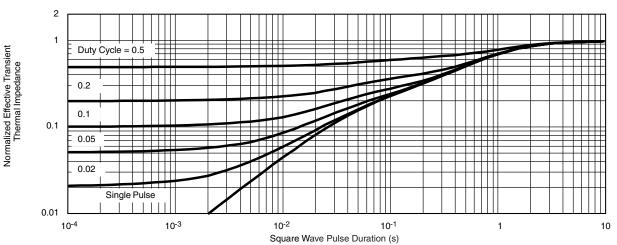
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Si6463BDQ

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72018.

Document Number: 72018 S10-2138-Rev. C, 20-Sep-10



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Datasheet of SI6463BDQ-T1-E3 - MOSFET P-CH 20V 6.2A 8-TSSOP

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Revision: 13-Jun-16 1 Document Number: 91000