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Diodes Incorporated DMP2004DWK-7

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Datasheet of DMP2004DWK-7 - MOSFET 2P-CH 20V 0.43A SOT-363

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DMP2004DWK

#### **DUAL P-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> max	<b>Ι<sub>D</sub></b> Τ <sub>A</sub> = +25°C
20V	$0.55\Omega$ @ $V_{GS} = 4.5V$	540mA

### **Description**

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(on)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

Load Switch

#### **Features**

- **Dual P-Channel MOSFET**
- Low On-Resistance
- Low Gate Threshold Voltage V<sub>GS(TH)</sub> <1V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.006 grams (approximate)

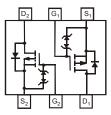
**SOT363** 







Top View



Top View Internal Schematic

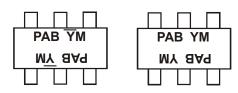
#### Ordering Information (Note 3)

Part Number	Case	Packaging
DMP2004DWK-7	SOT363	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

### **Marking Information**



PAB = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or  $\overline{Y}$  = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Year	2007	2008	20	09	2010	2011	2012	2013	20	14	2015	2016
Code	U	V	V	V	Χ	Υ	Z	Α	E	3	С	D
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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DMP2004DWK

#### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	$V_{DSS}$	-20	V	
Gate-Source Voltage		$V_{GSS}$	±8	V
Drain Current (Note 4)	T <sub>A</sub> = +25°C T <sub>A</sub> = +85°C	I <sub>D</sub>	-430 -310	mA

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

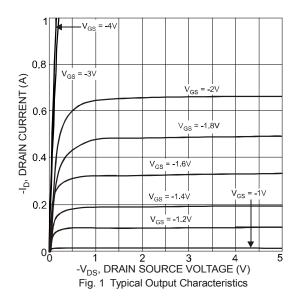
Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 4)	$P_{D}$	250	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	500	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

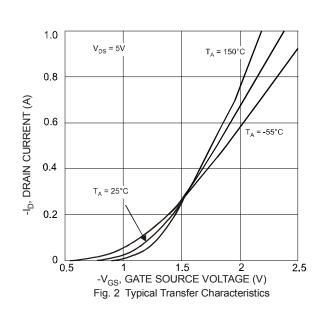
#### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-1.0	μΑ	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±1.0	μA	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.5		-1.0	V	$V_{DS} = V_{GS}$ , $I_{D} = -250 \mu A$	
			0.7	0.9		$V_{GS} = -4.5V$ , $I_D = -430mA$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>	_	1.1	1.4	Ω	$V_{GS} = -2.5V$ , $I_D = -300mA$	
			1.7	2.0		$V_{GS} = -1.8V, I_D = -150mA$	
Forward Transfer Admittance	Y <sub>fs</sub>	200	_	_	ms	$V_{DS} = 10V, I_D = 0.2A$	
Diode Forward Voltage (Note 5)	V <sub>SD</sub>	-0.5	_	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 115mA	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C <sub>iss</sub>	_	_	175	pF		
Output Capacitance	Coss	_	_	30	pF	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V -f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>			20	pF	71 - 1.0IVIDZ	

Notes:

- 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 5. Short duration pulse test used to minimize self-heating effect.





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# **DIODES**

#### 1.0 0.9 - $V_{GS(th)}$ , GATE THRESHOLD VOLTAGE (V) 0.8 0.7 $V_{DS} = V_{GS}$ $I_{D} = -1 \text{mA}$ 0.6 0.5 0.4 0.3 0.2 0.1 -50 -25 0 25 50 75 100 125 AMBIENT TEMPERATURE (°C) Fig. 3 Gate Threshold Voltage vs. Ambient Temperature

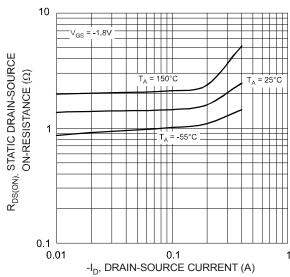
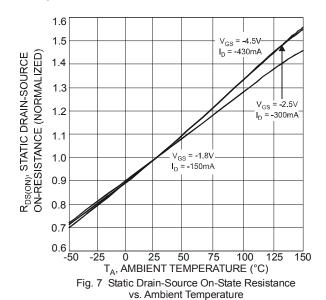
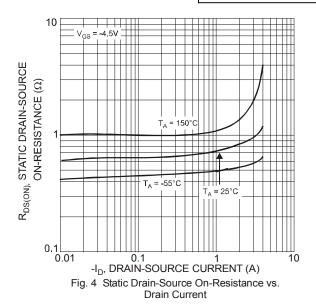
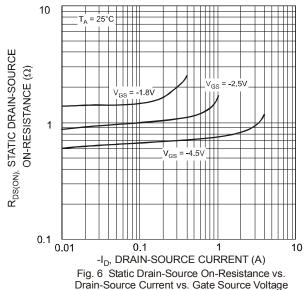


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current



#### DMP2004DWK





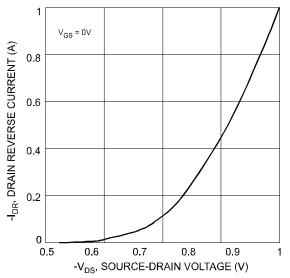
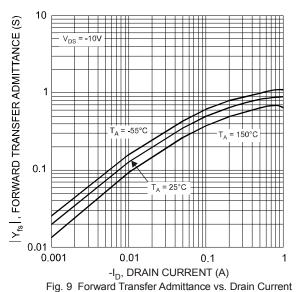


Fig. 8 Drain Reverse Current vs. Source-Drain Voltage

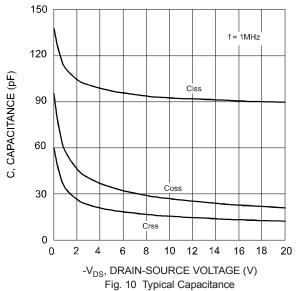
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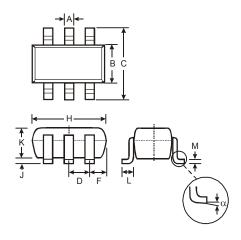




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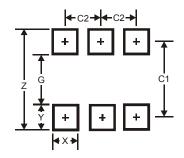


## **Package Outline Dimensions**



SOT363							
Dim	Min	Max	Тур				
Α	0.10	0.30	0.25				
В	1.15	1.35	1.30				
C	2.00	2.20	2.10				
ם		0.65 Ty	р				
F	0.40	0.45	0.425				
Н	1.80	2.20	2.15				
J	0	0.10	0.05				
K	0.90	1.00	1.00				
L	0.25	0.40	0.30				
М	0.10	0.22	0.11				
α	0°	8°	-				
All Dimensions in mm							

### **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Υ	0.6
C1	1.9
C2	0.65



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