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AON6410

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ALPHA & OMEGA SEMICONDUCTOR

AON6410

30V N-Channel MOSFET

General Description

The AON6410 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge.This device is suitable for use as a high side switch in SMPS and general purpose applications.

Product Summary

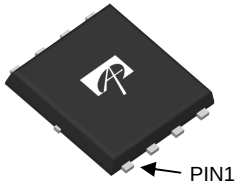
V_{DS} (V) = 30V
I_D = 24A (V_{GS} = 10V)
$R_{DS(ON)}$ < 12mΩ (V_{GS} = 10V)
$R_{DS(ON)}$ < 14mΩ (V_{GS} = 4.5V)

100% UIS Tested
100% Rg Tested

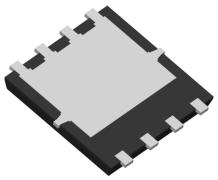
Green Product

DFN5X6

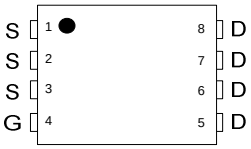
Top View

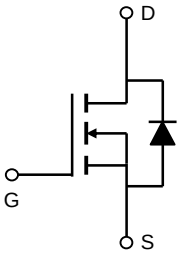


Bottom View



Top View





Absolute Maximum Ratings T_A=25℃ unless otherwise noted

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current [BJ]	T_C=25℃	I_D	24	A
	T_C=100℃		19	
Pulsed Drain Current		I_{DM}	120	
Continuous Drain Current [H]	T_A=25℃	I_{DSM}	10	A
	T_A=70℃		8	
Avalanche Current [C]		I_{AR}	30	A
Repetitive avalanche energy L=0.3mH [C]		E_{AR}	135	mJ
Power Dissipation [B]	T_C=25℃	P_D	35	W
	T_C=100℃		14	
Power Dissipation [A]	T_A=25℃	P_{DSM}	2	W
	T_A=70℃		1.3	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	℃

Thermal Characteristics

Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient [A]	t ≤ 10s	$R_{\theta JA}$	24	30	℃/W
Maximum Junction-to-Ambient [A]	Steady-State		53	64	℃/W
Maximum Junction-to-Case [C]	Steady-State	$R_{\theta JC}$	2.6	3.5	℃/W

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Electrical Characteristics (T_J=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	I_D=250µA, V_{GS}=0V	30			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=30V, V_{GS}=0V			1	µA
		T_J=55℃			5	
I_{GSS}	Gate-Body leakage current	V_{DS}=0V, V_{GS}= ±12V			100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS} I_D=250µA	1	1.5	2.5	V
$I_{D(ON)}$	On state drain current	V_{GS}=10V, V_{DS}=5V	120			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V, I_D=20A		10	12	mΩ
		T_J=125℃		16	19	
		V_{GS}=4.5V, I_D=10A		11.5	14	mΩ
g_{FS}	Forward Transconductance	V_{DS}=5V, I_D=20A		49		S
V_{SD}	Diode Forward Voltage	I_S=1A,V_{GS}=0V		0.72	1.0	V
I_S	Maximum Body-Diode Continuous Current				35	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=15V, f=1MHz		1210	1452	pF
C_{oss}	Output Capacitance			330		pF
C_{rss}	Reverse Transfer Capacitance			85		pF
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz		1.1	1.6	Ω
SWITCHING PARAMETERS						
Q_g(10V)	Total Gate Charge	V_{GS}=10V, V_{DS}=15V, I_D=20A		22	28	nC
Q_g(4.5V)	Total Gate Charge			10	13	nC
Q_{gs}	Gate Source Charge			3.7		nC
Q_{gd}	Gate Drain Charge			2.7		nC
$t_{D(on)}$	Turn-On DelayTime	V_{GS}=10V, V_{DS}=15V, R_L=0.75Ω, R_{GEN}=3Ω		10		ns
t_r	Turn-On Rise Time			6.3		ns
$t_{D(off)}$	Turn-Off DelayTime			21		ns
t_f	Turn-Off Fall Time			2.8		ns
t_{rr}	Body Diode Reverse Recovery Time	I_F=20A, dI/dt=100A/µs		36	45	ns
Q_{rr}	Body Diode Reverse Recovery Charge	I_F=20A, dI/dt=100A/µs		47		nC

A: The value of $R_{\theta JA}$ is measured with the device in a still air environment with T_A =25℃.
B. The power dissipation P_D is based on $T_{J(MAX)}$=150℃, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
C: Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150℃.
D. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to case $R_{\theta JC}$ and case to ambient.
E. The static characteristics in Figures 1 to 6 are obtained using <300 µs pulses, duty cycle 0.5% max.
F. These curves are based on the junction-to-case thermal impedence which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)}$=150℃.
G. These tests are performed with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25℃. The SOA curve provides a single pulse rating.
H. Surface mounted on a 1 in 2 FR-4 board with 2oz. Copper.
J. Maximum current is limited by bonding wire.
Rev8: March 2011

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

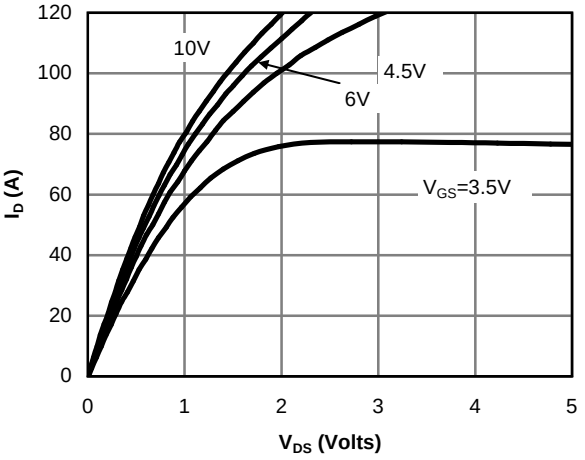

Fig 1: On-Region Characteristics

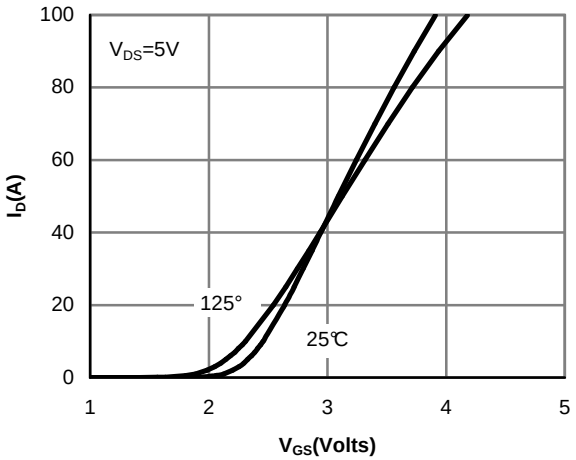

Figure 2: Transfer Characteristics

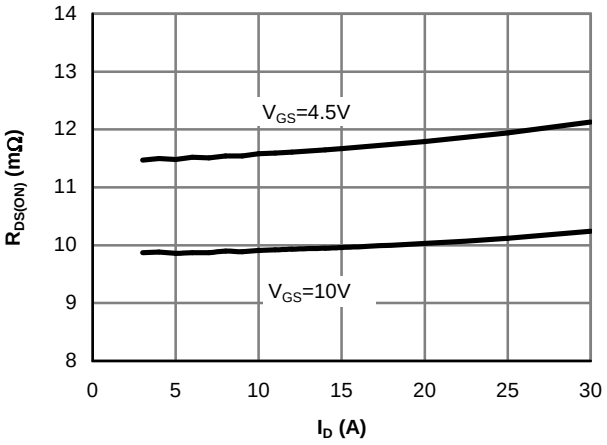

Figure 3: On-Resistance vs. Drain Current and Gate Voltage

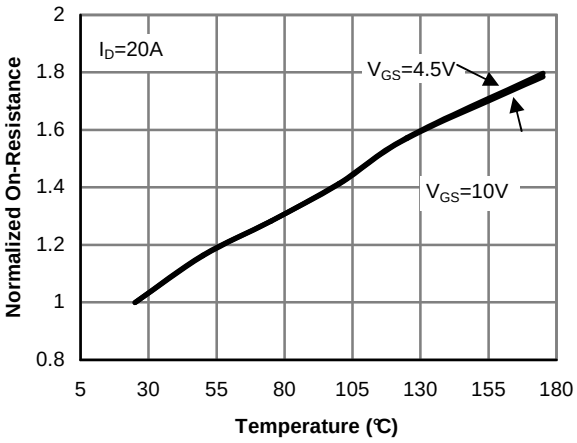

Figure 4: On-Resistance vs. Junction Temperature

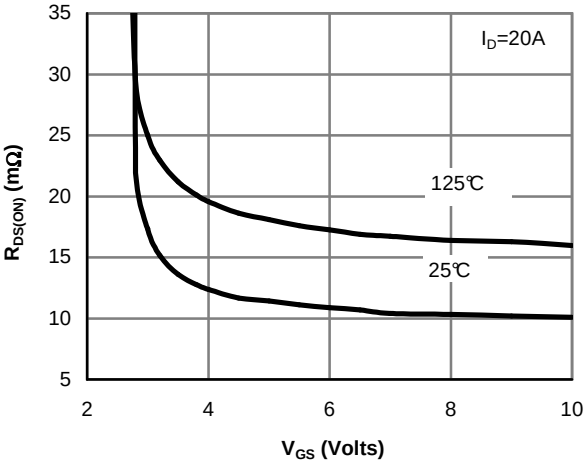

Figure 5: On-Resistance vs. Gate-Source Voltage

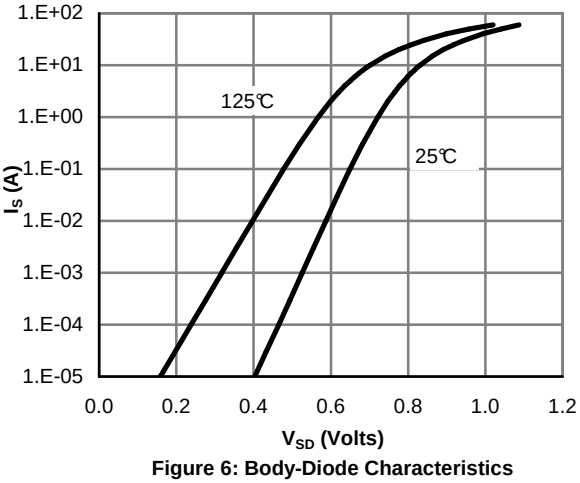

Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

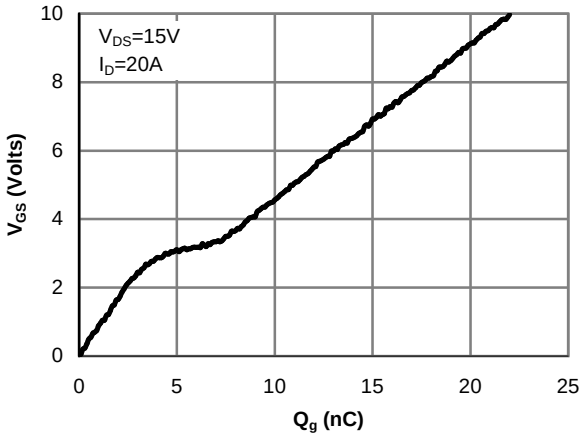

Figure 7: Gate-Charge Characteristics

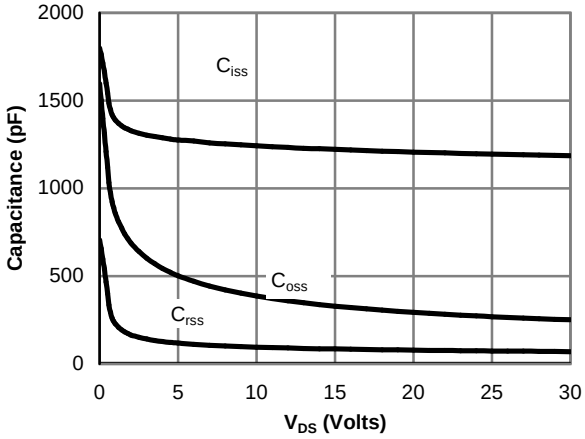

Figure 8: Capacitance Characteristics

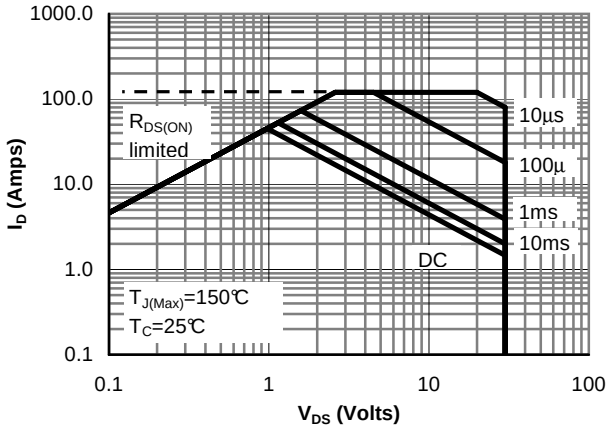

Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

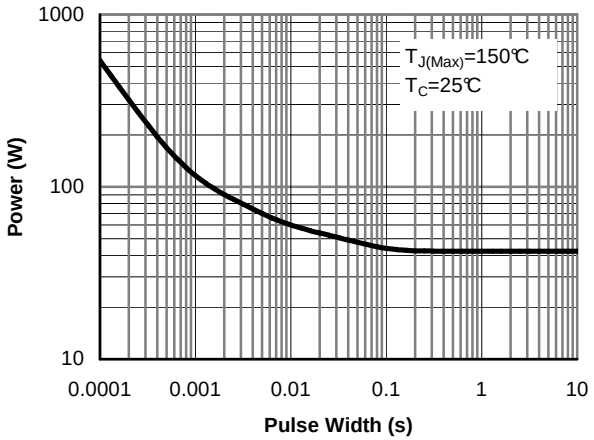

Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

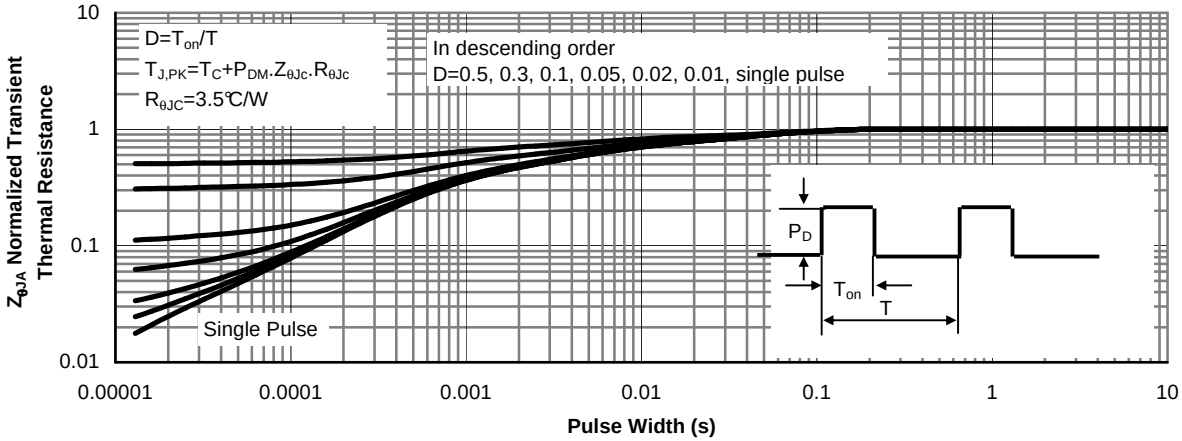

Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

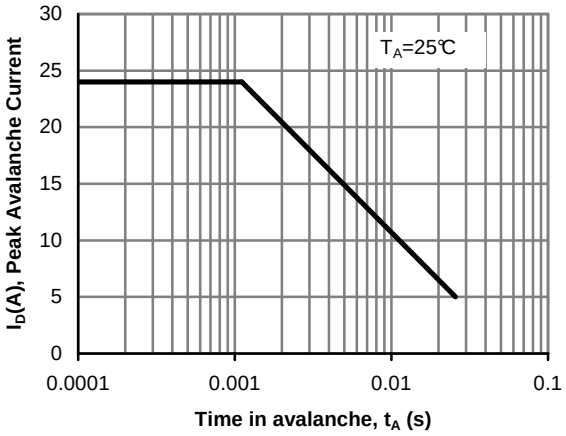

Figure 12: Single Pulse Avalanche capability

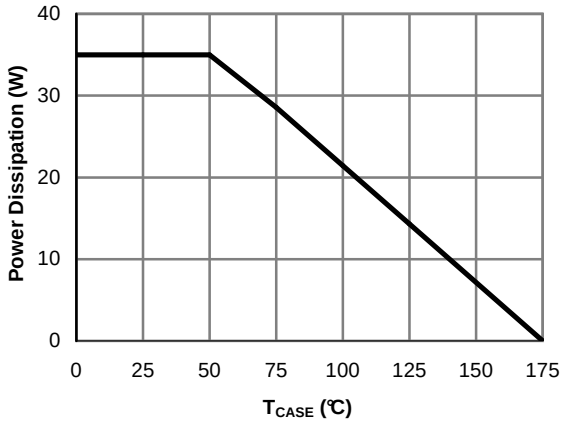

Figure 13: Power De-rating (Note B)

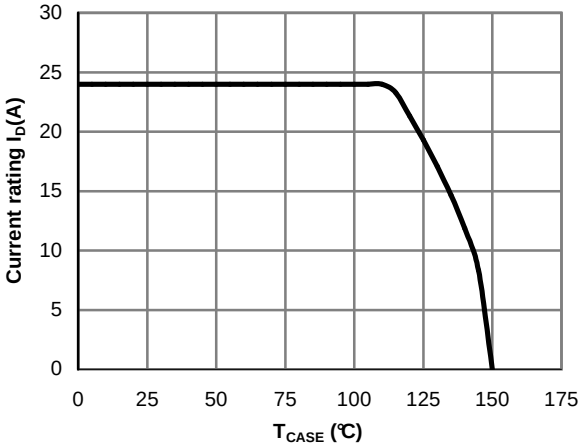

Figure 14: Current De-rating (Note B)

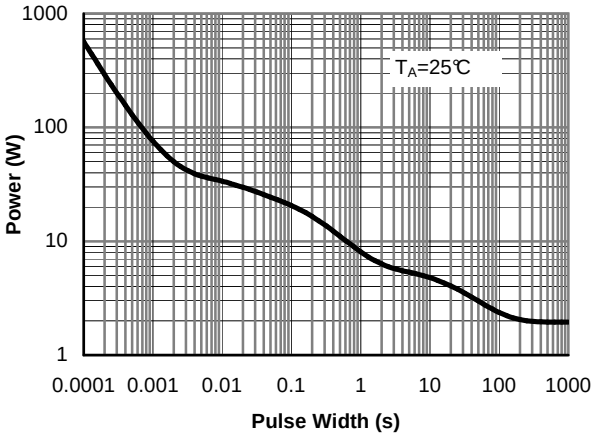

Figure 15: Single Pulse Power Rating Junction-to-Ambient (Note G)

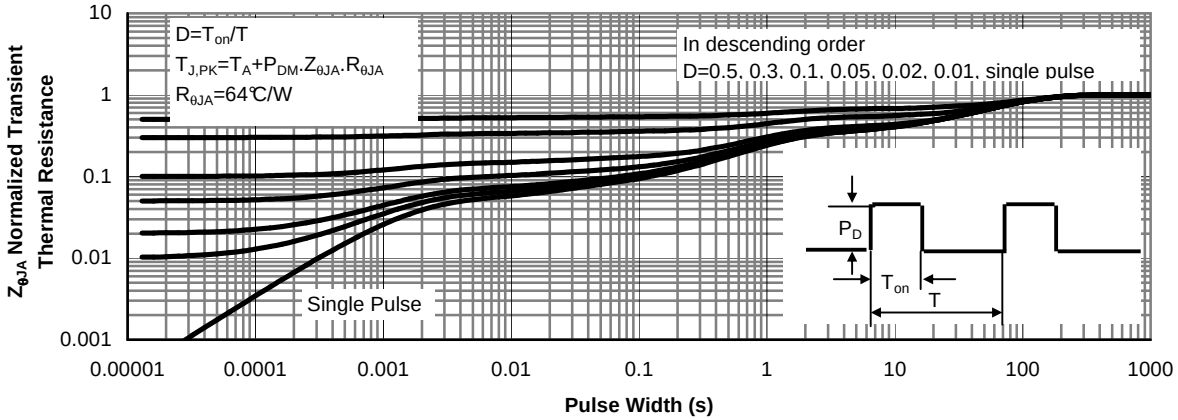

Figure 16: Normalized Maximum Transient Thermal Impedance (Note G)

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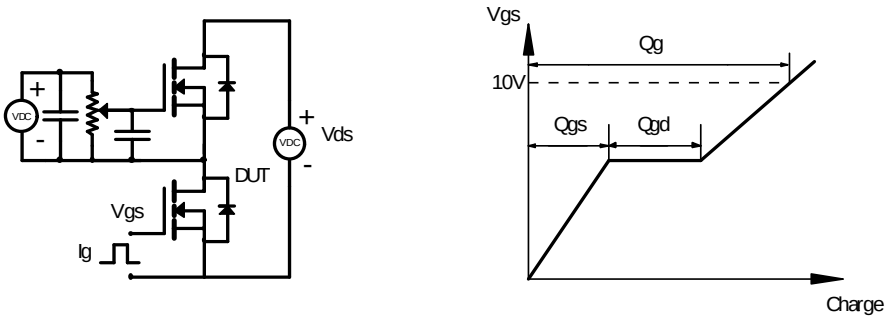
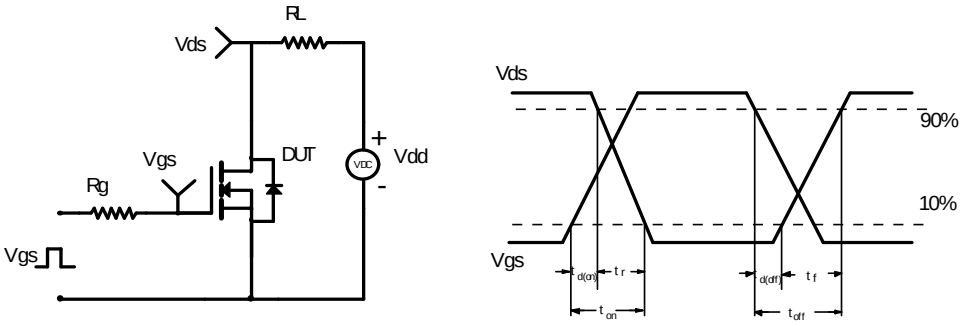
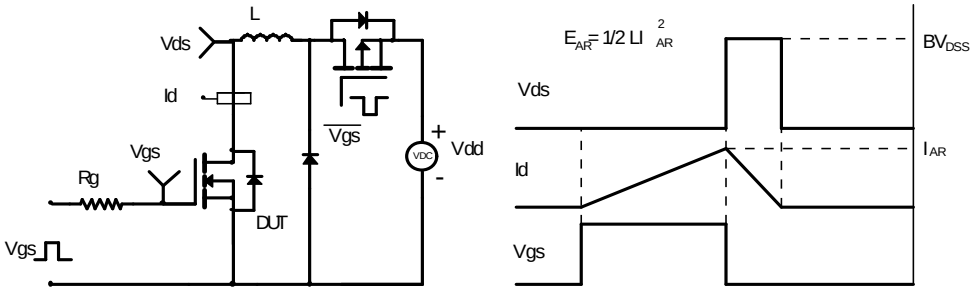
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



#DIV/0! #DIV/0!
#DIV/0! #DIV/0!

Diode Recovery Test Circuit & Waveforms