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<u>Diodes Incorporated</u> <u>DVRN6056-7-F</u>

For any questions, you can email us directly: sales@integrated-circuit.com



Datasheet of DVRN6056-7-F - TRANS NPN 40V 0.6A SOT-26

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DVRN6056

VOLTAGE REFERENCE ARRAY

Features

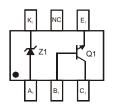
- Epitaxial Planar Die Construction
- Ideally Suited for Automated Assembly Processes
- Lead Free/RoHS Compliant Version (Notes 2 & 3)
- "Green" Device (Note 3)

Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic, "Green" Molding Compound (Note 3) UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Matte Tin Finish annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4 Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)







Device Schematic

Maximum Ratings, NPN Transistor Element (Q1) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	60	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Collector Current - Continuous (Note 1)	Ι _C	600	mA

Maximum Ratings, Zener Element (Z1) @TA = 25°C unless otherwise specified

	Characteristic	Symbol	Value	Unit
Forward Voltage	@ I _F = 10mA	V _F	0.9	V

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 1)	P_{D}	300	mW
Thermal Resistance, Junction to Ambient	(Note 1)	$R_{ hetaJA}$	417	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes:

- 1. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 2. No purposefully added lead.
- 3. Product manufactured with date code WN (Week 45, 2009) and newer are built with Green Molding Compound and Lead-free plating. Product manufactured prior to date code WO are built with Tin-Lead plating, Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.

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Electrical Characteristics, NPN Transistor Element (Q1) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)					
Collector-Base Breakdown Voltage	V _{(BR)CBO}	60	_	٧	$I_C = 100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	40	_	V	$I_C = 1.0 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	6.0	_	V	$I_E = 100 \mu A, I_C = 0$
Collector Cutoff Current	I _{CEX}		100	nA	$V_{CE} = 35V$, $V_{EB(OFF)} = 0.4V$
Base Cutoff Current	I _{BL}		100	nA	$V_{CE} = 35V, V_{EB(OFF)} = 0.4V$
ON CHARACTERISTICS (Note 4)					
		20	_		$I_C = 100 \mu A, V_{CE} = 1.0 V$
		40	_		$I_C = 1.0 \text{mA}, V_{CE} = 1.0 \text{V}$
DC Current Gain	h _{FE}	80	_	_	$I_C = 10 \text{mA}, V_{CE} = 1.0 \text{V}$
		100	300		$I_C = 150 \text{mA}, V_{CE} = 1.0 \text{V}$
		40			$I_C = 500 \text{mA}, V_{CE} = 2.0 \text{V}$
Collector-Emitter Saturation Voltage	V05(045)		0.40	V	$I_C = 150 \text{mA}, I_B = 15 \text{mA}$
Conector-Emitter Saturation Voltage	V _{CE(SAT)}	_	0.75	V	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$
Base-Emitter Saturation Voltage	V	0.75	0.95	V	$I_C = 150 \text{mA}, I_B = 15 \text{mA}$
base-Emilier Saturation Voltage	$V_{BE(SAT)}$	_	1.2	V	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$
SMALL SIGNAL CHARACTERISTICS				1	
Output Capacitance	C _{cb}	_	6.5	pF	$V_{CB} = 5.0V$, $f = 1.0MHz$, $I_E = 0$
Input Capacitance	C_{eb}	_	30	pF	$V_{EB} = 0.5V$, $f = 1.0MHz$, $I_{C} = 0$
Input Impedance	h _{ie}	1.0	15	kΩ	
Voltage Feedback Ratio	h _{re}	0.1	8.0	x 10 ⁻⁴	V _{CE} = 10V, I _C = 1.0mA, f = 1.0kHz
Small Signal Current Gain	h _{fe}	40	500	_	VCE = 10V, $IC = 1.0IIIA$, $I = 1.0KI12$
Output Admittance	h _{oe}	1.0	30	μS	
Current Gain-Bandwidth Product	f _T	250	_	MHz	$V_{CE} = 10V, I_{C} = 20mA, f = 100MHz$
SWITCHING CHARACTERISTICS					
Delay Time	t_d	_	15	ns	$V_{CC} = 30V, I_C = 150mA,$
Rise Time	t _r	_	20	ns	$V_{BE(off)} = 2.0V, I_{B1} = 15mA$
Storage Time	ts	_	225	ns	$V_{CC} = 30V, I_C = 150mA,$
Fall Time	t _f	_	30	ns	$I_{B1} = I_{B2} = 15 \text{mA}$

Electrical Characteristics, Zener Element (Z1) @TA = 25°C unless otherwise specified

Z	Zener Voltage Range (Note 4) Maximum Zener Impedance						n Reverse rent (Note 4)
	Vz @ IzT		I _{ZT}	$Z_{ZT} @ I_{ZT}$ $Z_{ZK} @ I_{ZK} = 0.5mA$		I _R	@ V _R
Nom (V)	Min (V)	Max (V)	mA	Ω		μΑ	V
5.6	5.49	5.73	5	60 200		1.0	2.5

Notes: 4. Short duration pulse test used to minimize self-heating effect.

NPN Transistor Section (Q1)

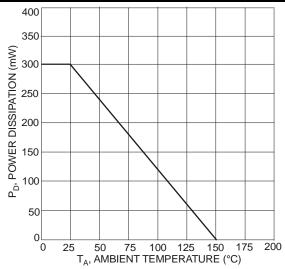


Fig. 1 Power Dissipation vs. Ambient Temperature (Total Device)

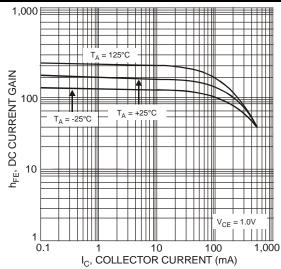


Fig. 2 Typical DC Current Gain vs. Collector Current

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NPN Transistor Section (Q1)

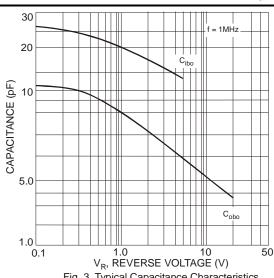


Fig. 3 Typical Capacitance Characteristics

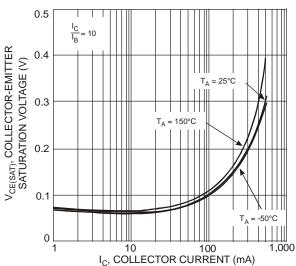


Fig. 5 Typical Collector-Emitter Saturation Voltage vs. Collector Current

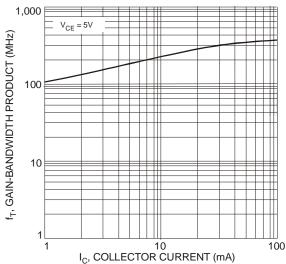


Fig. 7 Typical Gain-Bandwidth Product vs. Collector Current

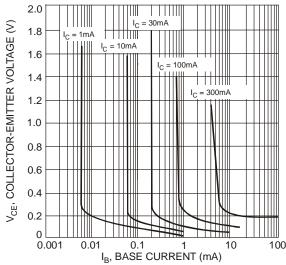


Fig. 4 Typical Collector Saturation Region

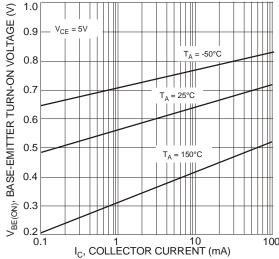


Fig. 6 Typical Base-Emitter Turn-On Voltage vs. Collector Current

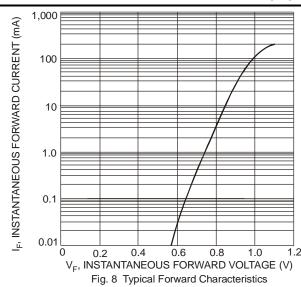
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Zener Section (Z1)

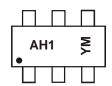


Ordering Information (Note 5)

Part Number	Case	Packaging
DVRN6056-7-F	SOT-26	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

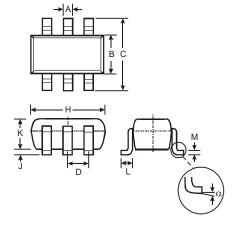


AH1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: P = 2003) M = Month (ex: 9 = September)

Date Code Key

	,												
Year	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
Code	Р	R	S	T	U	V	W	Х	Υ	Z	Α	В	С
Month	Jan	Feb	Mar	Apr	May	y Ju	un	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	(ô .	7	8	9	0	N	D

Package Outline Dimensions



	SOT-26						
Dim	Min	Max	Тур				
Α	0.35	0.50	0.38				
В	1.50	1.70	1.60				
С	2.70	3.00	2.80				
D		_	0.95				
Н	2.90	3.10	3.00				
J	0.013	0.10	0.05				
K	1.00	1.30	1.10				
L	0.35	0.55	0.40				
М	0.10	0.20	0.15				
α 0° 8° —							
All D	All Dimensions in mm						

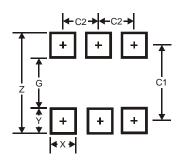
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Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Υ	0.80
C1	2.40
C2	0.95

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