# NST3904DP6T5G

# **Dual General Purpose** Transistor

The NST3904DP6T5G device is a spin-off of our popular SOT-23/SOT-323/SOT-563 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-963 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

## **Features**

- h<sub>FE</sub>, 100–300
- Low  $V_{CE(sat)} \le 0.4 \text{ V}$
- Reduces Board Space and Component Count
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Collector-Emitter Voltage		V <sub>CEO</sub>	40	Vdc
Collector-Base Voltage		V <sub>CBO</sub>	60	Vdc
Emitter-Base Voltage		V <sub>EBO</sub>	6.0	Vdc
Collector Current – Continuous		Ι <sub>C</sub>	200	mAdc
Electrostatic Discharge	HBM MM	ESD Class	2 B	

#### THERMAL CHARACTERISTICS

Characteristic (Single Heated)	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C (Note 1)	PD	240 1.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	520	°C/W
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C (Note 2)	P <sub>D</sub>	280 2.2	m₩ m₩/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\thetaJA}$	446	°C/W
Characteristic (Dual Heated) (Note 3)	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C (Note 1)	PD	350 2.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	357	°C/W
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C (Note 2)	P <sub>D</sub>	420 3.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	297	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	−55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

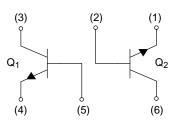
FR-4 @ 100 mm<sup>2</sup>, 1 oz. copper traces, still air.
 FR-4 @ 500 mm<sup>2</sup>, 1 oz. copper traces, still air.

3. Dual heated values assume total power is sum of two equally powered channels.



## **ON Semiconductor®**

#### http://onsemi.com



NST3904DP6T5G



CASE 527AD

## MARKING DIAGRAM





Μ = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NST3904DP6T5G	SOT–963 (Pb–Free)	8000/Tape & Reel
NSVT3904DP6T5G	SOT-963 (Pb-Free)	8000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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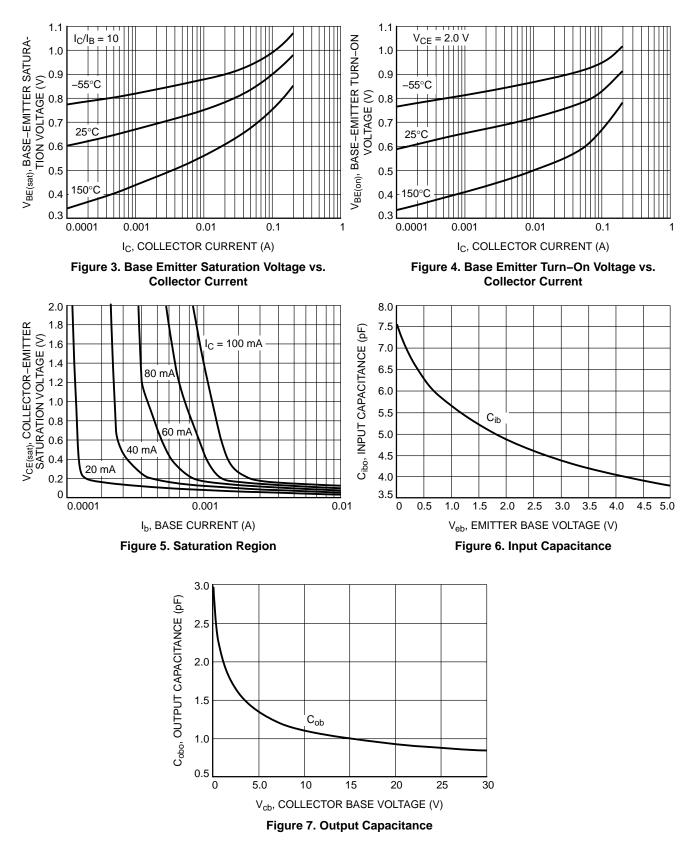
#### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit	
OFF CHARACTERISTICS	;				•	
Collector – Emitter Breakd	own Voltage (Note 4) ( $I_C = 1.0 \text{ mAdc}, I_B = 0$ )	V <sub>(BR)CEO</sub>	40	_	Vdc	
Collector - Base Breakdov	vn Voltage (I <sub>C</sub> = 10 $\mu$ Adc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	60	-	Vdc	
Emitter-Base Breakdown	Voltage ( $I_E = 10 \ \mu Adc$ , $I_C = 0$ )	V <sub>(BR)EBO</sub>	6.0	-	Vdc	
Collector Cutoff Current ( $V_{CE} = 30$ Vdc, $V_{EB} = 3.0$ Vdc)		I <sub>CEX</sub>	-	50	nAdc	
ON CHARACTERISTICS	(Note 4)				•	
DC Current Gain ( $I_C = 0.1 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$ ) ( $I_C = 1.0 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$ ) ( $I_C = 10 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$ ) ( $I_C = 50 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$ ) ( $I_C = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$ )		h <sub>FE</sub>	40 70 100 60 30	- - 300 - -	-	
Collector – Emitter Saturat ( $I_C = 10 \text{ mAdc}$ , $I_B = 1.0$ ( $I_C = 50 \text{ mAdc}$ , $I_B = 5.0$	) mAdc)	V <sub>CE(sat)</sub>		0.2 0.3	Vdc	
Base – Emitter Saturation Voltage $(I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc})$ $(I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc})$		V <sub>BE(sat)</sub>	0.65 -	0.85 0.95	Vdc	
SMALL-SIGNAL CHARA	CTERISTICS		L			
Current-Gain – Bandwidth Product (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 MHz)		f <sub>T</sub>	200	-	MHz	
Output Capacitance ( $V_{CB}$ = 5.0 Vdc, $I_E$ = 0, f = 1.0 MHz)		C <sub>obo</sub>	-	4.0	pF	
Input Capacitance ( $V_{EB} = 0.5$ Vdc, $I_C = 0$ , f = 1.0 MHz)		C <sub>ibo</sub>	-	8.0	pF	
Noise Figure (V <sub>CE</sub> = 5.0 Vdc, I <sub>C</sub> = 100 $\mu$ Adc, R <sub>S</sub> = 1.0 k $\Omega$ , f = 1.0 kHz)		NF	-	5.0	dB	
SWITCHING CHARACTE	RISTICS				•	
Delay Time	$(V_{CC} = 3.0 \text{ Vdc}, V_{BE} = -0.5 \text{ Vdc})$	t <sub>d</sub>	-	35		
Rise Time	(I <sub>C</sub> = 10 mAdc, I <sub>B1</sub> = 1.0 mAdc)	t <sub>r</sub>	-	35	ns	
Storage Time	$(V_{CC} = 3.0 \text{ Vdc}, I_{C} = 10 \text{ mAdc})$	t <sub>s</sub>	-	275		
Fall Time	$(I_{B1} = I_{B2} = 1.0 \text{ mAdc})$	t <sub>f</sub>	_	50	ns	

0.28 400  $I_{\rm C}/I_{\rm B} = 10$ 150°C (5.0 V)  $V_{CE(sat)} = 150^{\circ}C$ 350 h<sub>FE</sub>, DC CURRENT GAIN (V) 300 150°C (1.0 V) | | | ||||| 25°C (5.0 V) 250 200 25°C (1.0 V) -55°C 1 1 1 1 1 1 1 1 1 25°C 150 -55°C (5.0 V) -55°C (1.0 V) 100 50 0 0.03 0.001 0.0001 0.01 0.1 1 0.0001 0.001 0.01 0.1 I<sub>C</sub>, COLLECTOR CURRENT (A) I<sub>C</sub>, COLLECTOR CURRENT (A) Figure 2. DC Current Gain vs. Collector Current Figure 1. Collector Emitter Saturation Voltage vs. **Collector Current** 

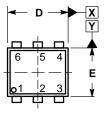
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## NST3904DP6T5G

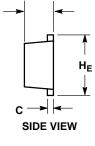


#### PACKAGE DIMENSIONS

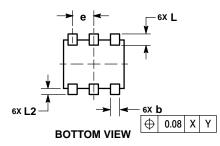
SOT-963 CASE 527AD ISSUE E







Α



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
   CONTROLLING DIMENSION: MILLIMETERS
- CONTROLLING DIMENSION: MILLIMETERS
  MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD
- FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.



	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	0.34	0.37	0.40	
b	0.10	0.15	0.20	
С	0.07	0.12	0.17	
D	0.95	1.00	1.05	
Е	0.75	0.80	0.85	
е	0.35 BSC			
ΗE	0.95	1.00	1.05	
L	0.19 REF			
L2	0.05	0.10	0.15	

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