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[DMN3007LSS-13](#)

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**DMN3007LSS**

**SINGLE N-CHANNEL ENHANCEMENT MODE MOSFET**

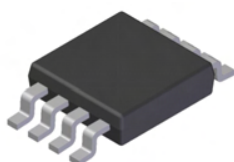
**Features**

- Low On-Resistance
  - 7mΩ @ V<sub>GS</sub> = 10V
  - 10mΩ @ V<sub>GS</sub> = 4.5V
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **"Green" Device (Note 4)**
- **Qualified to AEC-Q101 Standards for High Reliability**

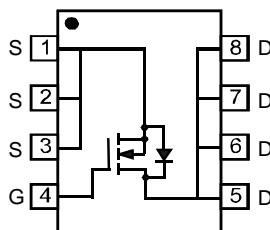
**Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.072g (approximate)

SO-8



Top View



Top View  
Internal Schematic

**Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Drain Current (Note 1)	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	16	A
		T <sub>A</sub> = 70°C		13	
Pulsed Drain Current (Note 3)			I <sub>DM</sub>	64	A

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P <sub>D</sub>	2.5	W
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	50	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

- Notes:
1. Device mounted on 2 oz. Copper pads on FR-4 PCB, with R<sub>θJA</sub> = 50°C
  2. No purposefully added lead.
  3. Pulse width ≤10μs, Duty Cycle ≤1%.
  4. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).



DMN3007LSS

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 5)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 5)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.3	—	2.1	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	5 7.9	7 10	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 13A
Forward Transconductance	g <sub>fs</sub>	—	16.4	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 15A
Diode Forward Voltage (Note 5)	V <sub>SD</sub>	—	0.67	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2.3A
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	C <sub>iss</sub>	—	2714	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	436	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	380	—	pF	
Gate Resistance	R <sub>G</sub>	—	0.7	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge	Q <sub>g</sub>	—	31.2 64.2	—	nC	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 16A V <sub>DS</sub> = 15V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 16A V <sub>DS</sub> = 15V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 16A
Gate-Source Charge	Q <sub>gs</sub>	—	7.1	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	17.1	—		
Turn-On Delay Time	t <sub>d(on)</sub>	—	10.3	—	ns	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A, R <sub>G</sub> = 6.0Ω
Rise Time	t <sub>r</sub>	—	14.8	—		
Turn-Off Delay Time	t <sub>d(off)</sub>	—	85.1	—		
Fall Time	t <sub>f</sub>	—	43.6	—		

Notes: 5. Short duration pulse test used to minimize self-heating effect.

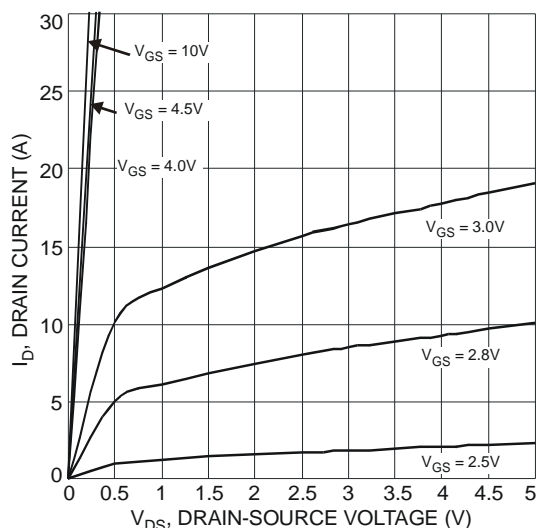


Fig. 1 Typical Output Characteristic

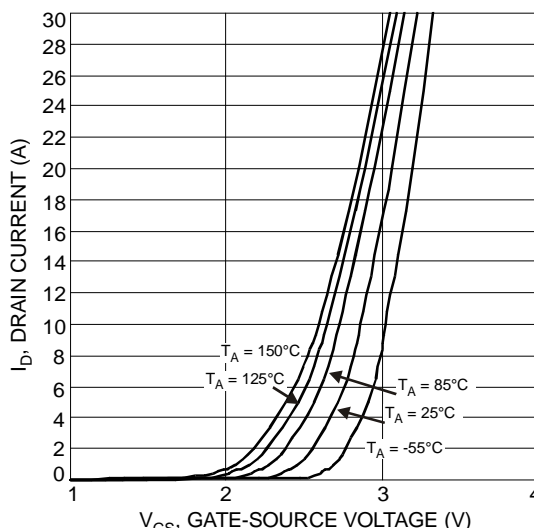


Fig. 2 Typical Transfer Characteristic



**DMN3007LSS**

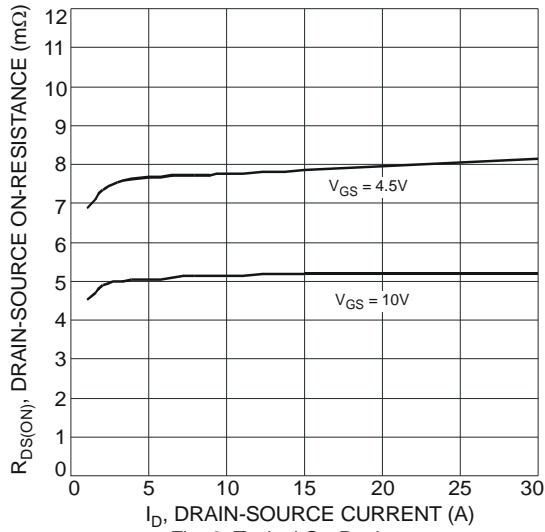


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

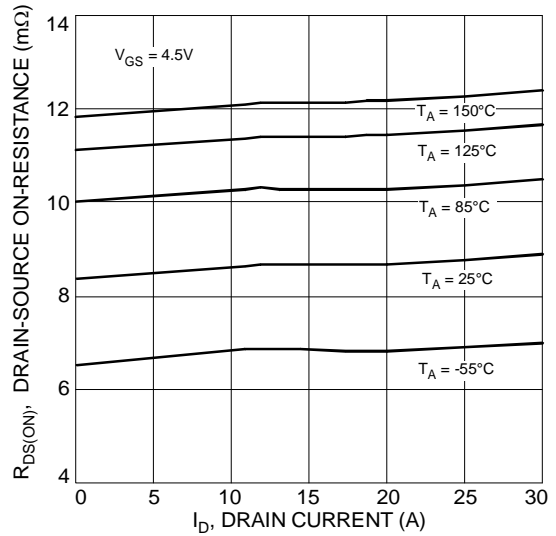


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

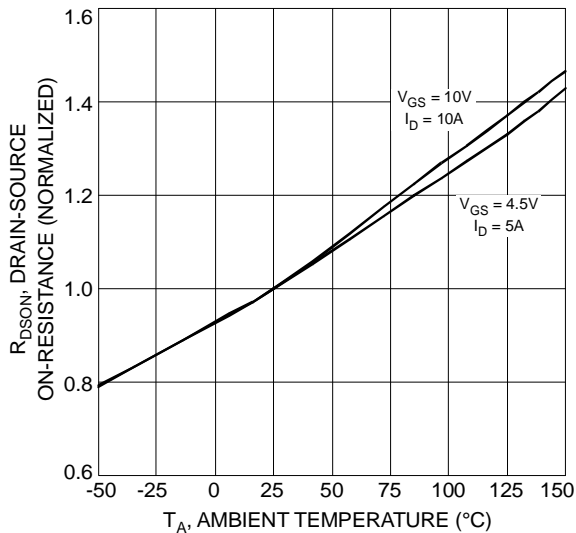


Fig. 5 On-Resistance Variation with Temperature

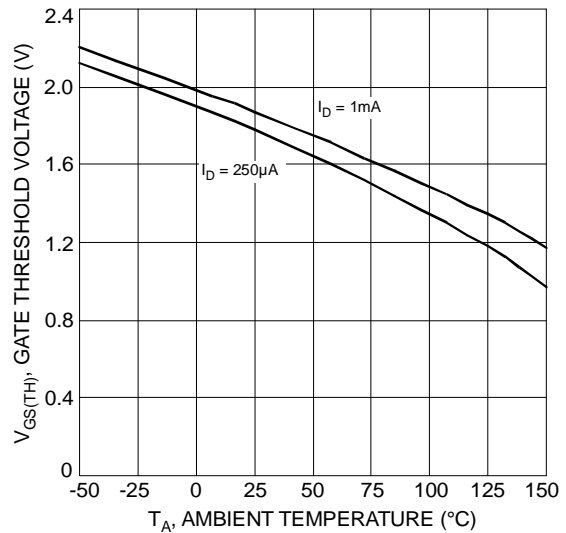


Fig. 6 Gate Threshold Variation vs. Ambient Temperature

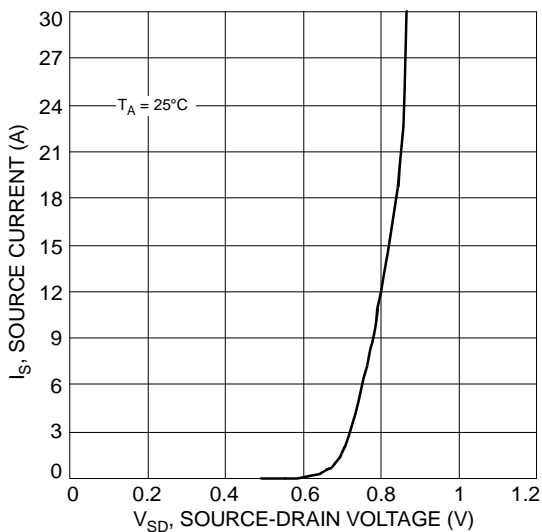


Fig. 7 Diode Forward Voltage vs. Current

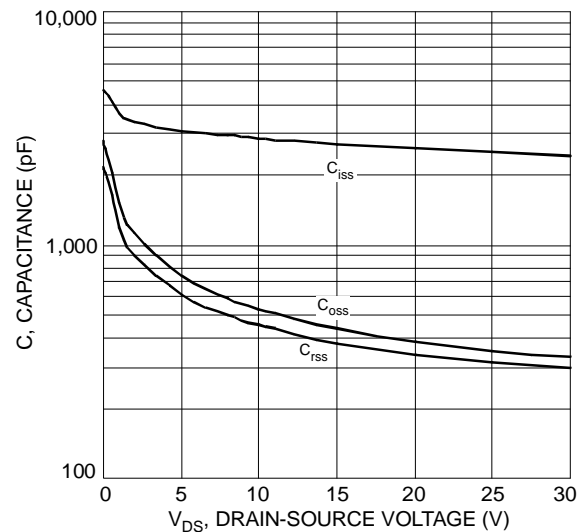


Fig. 8 Typical Total Capacitance

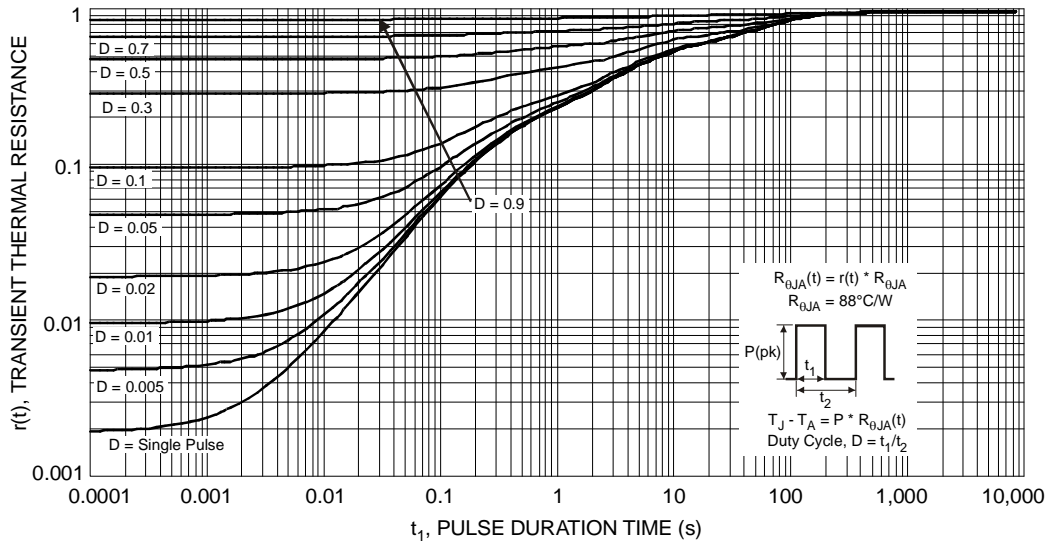


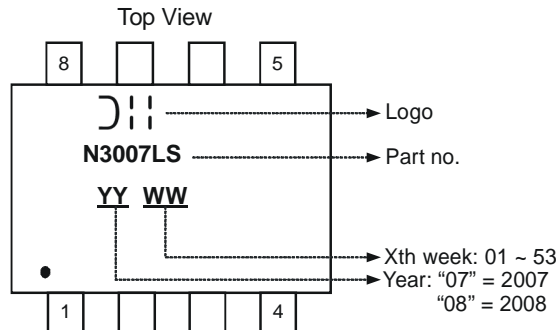
Fig. 9 Transient Thermal Response

**Ordering Information** (Note 6)

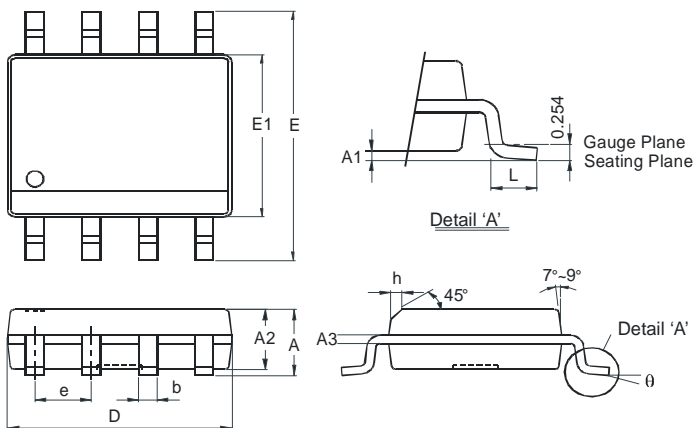
Part Number	Case	Packaging
DMN3007LSS-13	SO-8	2500/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

**Marking Information**

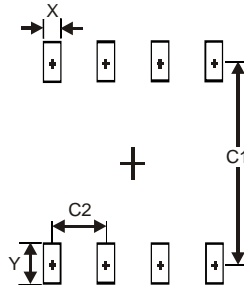


**Package Outline Dimensions**



SO-8		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

## Suggested Pad Layout



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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