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Diodes Incorporated DMP58D0SV-7

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DMP58D0SV

DUAL P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- ESD Protected Gate to 500V
- Low Input Capacitance
- Fast Switching Speed
- Lead Free By Design/RoHS Compliant (Note 3)
- "Green" Device (Note 4)
- Qualified to AEC-Q 101 Standards for High Reliability

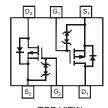
Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.006 grams (approximate)

SOT-563



ESD protected to 500V



TOP VIEW Internal Schematic

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	;	Symbol	Value	Units
Drain-Source Voltage		V _{DSS}	-50	V
Drain-Gate Voltage (Note 1)		V _{DGR}	-50	V
Gate-Source Voltage	Continuous	V _{GSS}	±20	V
Drain Current (Note 2)	Continuous	ID	-160	mA

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 2)	PD	400	mW
Thermal Resistance, Junction to Ambient (Note 2)	$R_{ heta JA}$	313	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV _{DSS}	-50			V	$V_{GS} = 0V, I_D = -250 \mu A$
Zero Gate Voltage Drain Current	IDSS			-1	μΑ	$V_{DS} = -50V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}			±5	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V _{GS(th)}	-0.8		-2.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	R _{DS} (ON)		6	8	Ω	V _{GS} = -5V, I _D = -0.100A
Forward Transconductance	g fs	0.05			S	V _{DS} = -25V, I _D = -0.1A
DYNAMIC CHARACTERISTICS		_	_	_	_	
Input Capacitance	C _{iss}	_	27		рF	
Output Capacitance	Coss		4		pF	$V_{DS} = -25V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance	C _{rss}		1.4		pF	

Notes: 1. $R_{GS} \leq 20 K \Omega$.

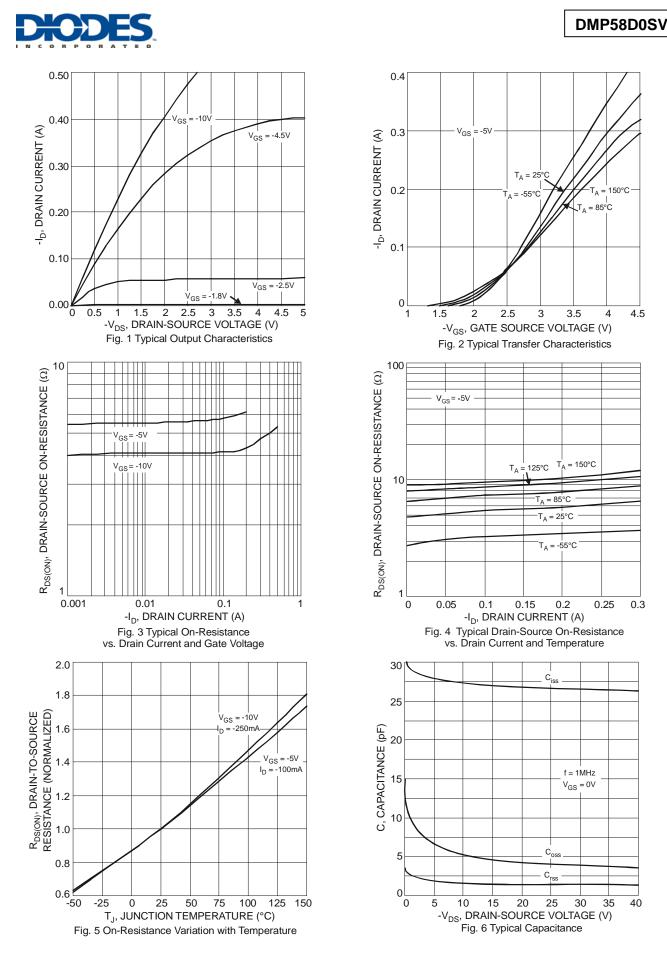
 Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

3. No purposefully added lead.

4. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

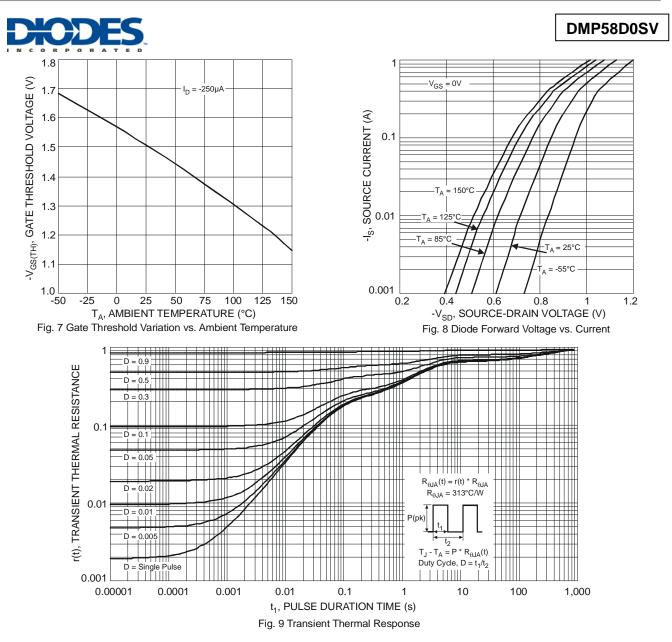
5. Short duration pulse test used to minimize self-heating effect.





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Ordering Information (Note 6)

Part Number	Case	Packaging
DMP58D0SV -7	SOT-563	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information (Note 7)

				MPA	YM I	YM = Date	Code Ma					
			L			Y = Year (e M = Month		September)				
e Code Kev												
e Code Key Year	2007	20	08	2009		201	1	2012	2013	20)14	2015
,	1	20		2009 W	2010 X	201 Y	1	2012 Z	2013 A		9 14 B	2015 C
	1	-				201 Y Jun	Jul					

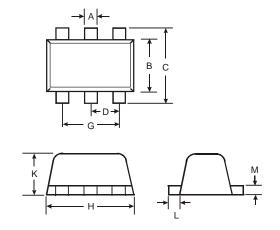
Notes: 7. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).





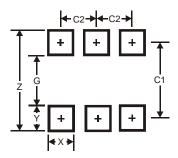
DMP58D0SV

Package Outline Dimensions



	SOT-563					
Dim	Min	Max	Тур			
Α	0.15	0.30	0.20			
В	1.10	1.25	1.20			
С	1.55	1.70	1.60			
D	-	-	0.50			
G	0.90	1.10	1.00			
н	1.50	1.70	1.60			
Κ	0.55	0.60	0.60			
L	0.10	0.30	0.20			
М	0.10	0.18	0.11			
All	Dimens	sions in	mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5





DMP58D0SV

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