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[Bel Fuse Inc.](#)
[S553-6500-C4-F](#)

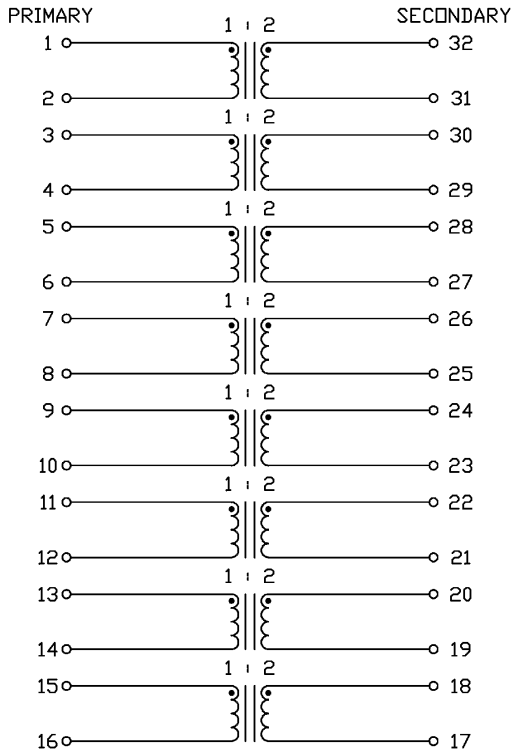
For any questions, you can email us directly:
sales@integrated-circuit.com



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SCHEMATIC

ELECTRICAL CHARACTERISTICS @25°C



TURNS RATIO PRIMARY : SECONDARY	1 : 2
PHASE	PER DOT CONVENTION
INDUCTANCE PRIMARY AT -40°C	1.5mH MIN. @10kHz, 20mV 640µH MIN.
LEAKAGE INDUCTANCE (Le) PRIMARY (SECONDARY SHORTED)	0.30µH MAX. @1MHz, 20mV
INTERWINDING CAPACITANCE (Cw/W) PRIMARY TO SECONDARY	35pF MAX. @1MHz, 20mV
DC RESISTANCE PRIMARY SECONDARY	0.60Ω MAX. 1.20Ω MAX.
HIPOT	PER HAND-WORK-03

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH	<input type="checkbox"/> METRIC DIM. AS REFERENCE
CHAN CH	10-02-07	ELECTRICAL SPECIFICATION S553-6500-C4-F	X5536500C4-F	.X	UNIT : INCH [mm] REV. : A
DRAWN BY	DATE		FILE NAME	.XX	SCALE : N/A SIZE : A4
DQ Chen	10-02-07		X5536500C4FA.DWG	.XXX	PAGE : 2



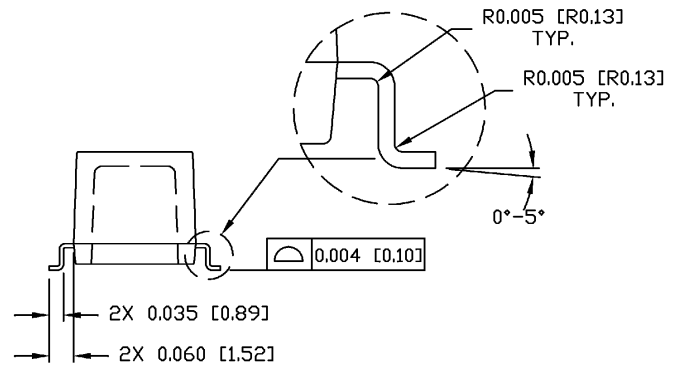
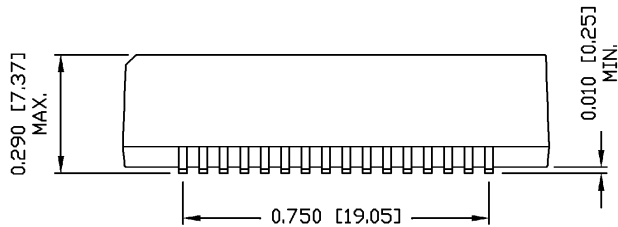
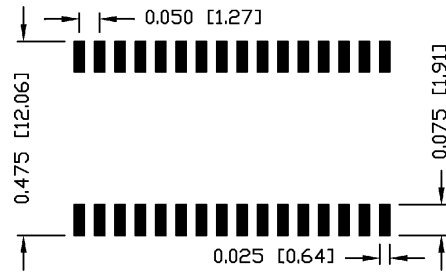
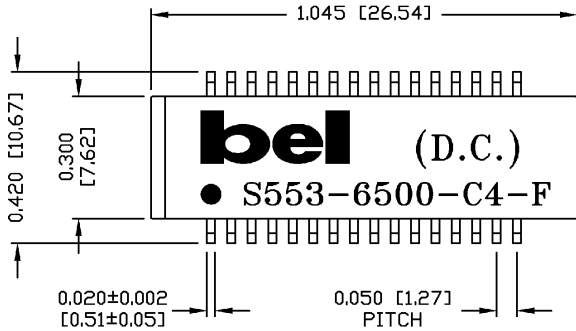
DC002(2)032305

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SUGGESTED PCB PAD LAYOUT



NOTES:

- STANDARD MARKING REFER TO DOC. HAND-WORK-04.
- PACKAGE CODE: 'RDS001'.

ORIGINATED BY Lawrence Tsang	DATE 10-02-07	TITLE MECHANICAL OUTLINE S553-6500-C4-F	PART NO. / DRAWING NO. X5536500C4-F	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE	REV. : A	SIZE : A4	PAGE : 3	
DRAWN BY DQ Chen	DATE 10-02-07		FILE NAME X5536500C4FA.DWG	.X .XX ±0.01 .XXX ±0.005	UNIT : INCH [mm] SCALE : N/A				

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