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Vishay Semiconductor/Diodes Division SAC10-E3/73

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SAC5.0 thru SAC50

Vishay General Semiconductor

Low Capacitance TRANSZORB® Transient Voltage Suppressors



FEATURES

- Glass passivated chip junction
- 500 W peak pulse power capability with a 10/1000 μs waveform, repetitive rate (duty cycle): 0.01 %

RoHS

COMPLIANT

- Excellent clamping capability
- · Very fast response time
- Low incremental surge resistance
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

Use in sensitive electronics protection against voltage transients induced by inductive load switching and lighting on ICs, MOSFET, signal lines of sensor units for consumer, computer, industrial, and telecommunication.

MECHANICAL DATA

Case: DO-204AC, molded epoxy over passivated body Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 E3 suffix meets JESD 201 class 1A whisker test

Delerity Color band denotes asthede and

Polarity:	Color	band	denotes	cathode	ena

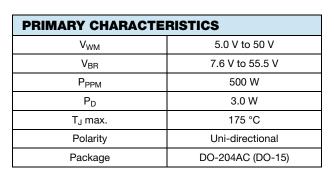
MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	VALUE	UNIT				
Peak pulse power dissipation with a 10/1000 μ s waveform ⁽¹⁾	P _{PPM}	500	W				
Peak pulse current with a 10/1000 μs waveform (fig. 3) $^{(1)}$	I _{PPM}	See next table	A				
Power dissipation on infinite heatsink at $T_L = 75 \text{ °C}$ (fig. 2)	PD	3.0	W				
Operating junction and storage temperature range	TJ, T _{STG}	-55 to +175	°C				

Note

 $^{(1)}$ Non-repetitive current pulse, per fig. 3 and derated above T_A = 25 °C per fig. 2

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1







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SAC5.0 thru SAC50

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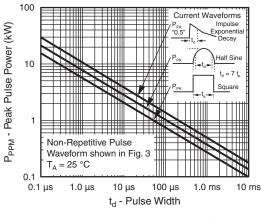
ELECT	ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)								
PART NUMBER	$\label{eq:BREAKDOWN} \begin{array}{c} \text{BREAKDOWN} \\ \text{VOLTAGE} \\ \text{AT} \\ \text{I}_{\text{T}} = 1.0 \text{ mA} \\ \text{V}_{\text{BR}} \left(\text{V} \right) \\ \\ \text{MIN.} \end{array}$	STAND-OFF VOLTAGE ⁽¹⁾ V _{WM} (V)	MAXIMUM REVERSE LEAKAGE AT V _{WM} I _D	MAXIMUM CLAMPING VOLTAGE AT I _{PP} = 5.0 A V _C (V)	MAXIMUM PEAK PULSE CURRENT PER FIG. 3 I _{PP} (A)	MAXIMUM JUNCTION CAPACITANCE AT 0 V (pF)	WORKING INVERSE BLOCKING VOLTAGE V _{WIB} (V)	INVERSE BLOCKING LEAKAGE CURRENT V _{WIB} I _{IB} (mA)	PEAK INVERSE BLOCKING VOLTAGE V _{PIB} (V)
SAC5.0	7.60	5	(μΑ) 300	10.0	44	50	75	1.0	100
SAC6.0	7.90	6	300	11.2	41	50	75	1.0	100
SAC7.0	8.33	7	300	12.6	38	50	75	1.0	100
SAC8.0	8.89	8	100	13.4	36	50	75	1.0	100
SAC8.5	9.44	8.5	50	14.0	34	50	75	1.0	100
SAC10	11.10	10	5.0	16.3	29	50	75	1.0	100
SAC12	13.30	12	5.0	19.0	25	50	75	1.0	100
SAC15	16.70	15	5.0	23.6	20	50	75	1.0	100
SAC18	20.00	18	5.0	28.8	15	50	75	1.0	100
SAC22	24.40	22	5.0	35.4	14	50	75	1.0	100
SAC26	28.90	26	5.0	42.3	11.1	50	75	1.0	100
SAC30	33.30	30	5.0	48.6	10.0	50	75	1.0	100
SAC36	40.00	36	5.0	60.0	8.6	50	75	1.0	100
SAC45	50.00	45	5.0	77.0	6.8	50	150	1.0	200
SAC50	55.50	50	5.0	88.0	5.8	50	150	1.0	200

Note

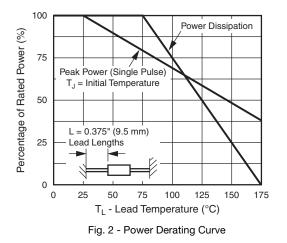
⁽¹⁾ Non-repetitive current pulse, per fig. 3 and derated above $T_A = 25$ °C per fig. 2

ORDERING INFORMATION (Example)							
PREFERRED PIN	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
SAC5.0-E3/54	0.432	54	4000	13" diameter paper tape and reel			

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25 \text{ °C}$ unless otherwise noted)







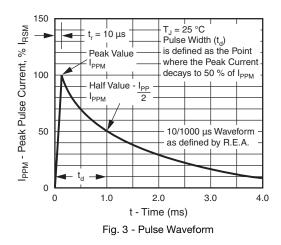
Revision: 11-Feb-15

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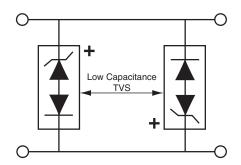
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SCHEMATIC

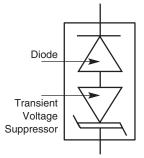
SAC5.0 thru SAC50

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Application Note: Device must be used with two units in parallel, opposite in polarity as shown in circuit for AC signal line protection.

Fig. 4 - AC Line Protection Application



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

0.034 (0.86) 0.028 (0.71) DIA. 1.0 (25.4) MIN. 0.300 (7.6) 0.230 (5.8) 1.0 (25.4) MIN. 1.0 (25.4) MIN. 0.140 (3.6) DIA.

DO-204AC (DO-15)





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