

# **Excellent Integrated System Limited**

Stocking Distributor

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Vishay Semiconductor/Diodes Division VSIB15A20-E3/45

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



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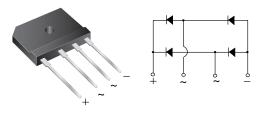
**Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite** Datasheet of VSIB15A20-E3/45 - DIODE 15A 200V SGL BRIDGE 4SIP Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



### VSIB15A20 thru VSIB15A80

Vishay General Semiconductor

# Single-Phase Single In-Line Bridge Rectifiers



Case Style GSIB-5S

#### FEATURES

- UL recognition file number E54214
- Thin single in-line package
- Glass passivated chip junction
- High surge current capability
- High case dielectric strength of 2500 V<sub>RMS</sub>
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **TYPICAL APPLICATIONS**

General purpose use in ac-to-dc bridge full wave rectification for switching power supply, home appliances, office equipment, industrial automation applications.

#### **MECHANICAL DATA**

Case: GSIB-5S

Epoxy meets UL 94 V-0 flammability rating

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test

Polarity: As marked on body

**Mounting Torque:** 10 cm-kg (8.8 inches-lbs) max. **Recommended Torque:** 5.7 cm-kg (5 inches-lbs)

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	VSIB15A20	VSIB15A40	VSIB15A60	VSIB15A80	UNIT	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	200	400	600	800	V	
Maximum RMS voltage	V <sub>RMS</sub>	140	280	420	560	V	
Maximum DC blocking voltage	V <sub>DC</sub>	200	400	600	800	V	
$ \begin{array}{ll} \mbox{Maximum average forward rectified} & T_{C} = 107 \ ^{\circ}C \ ^{(1)} \\ \mbox{output current at} & T_{A} = 25 \ ^{\circ}C \ ^{(2)} \end{array} $	I <sub>F(AV)</sub>	15 3.5			А		
Peak forward surge current single sine-wave superimposed on rated load	I <sub>FSM</sub>	200			А		
Rating for fusing (t < 8.3 ms)	l <sup>2</sup> t	166		A <sup>2</sup> s			
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150			°C		

#### Notes:

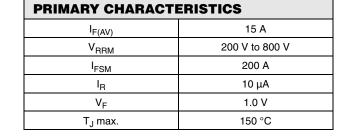
(1) Unit case mounted on aluminum plate heatsink

(2) Units mounted on P.C.B. without heatsink

 Document Number: 84652
 For technical questions within your region, please contact one of the following:

 Revision: 15-Dec-08
 PDD-Americas@vishay.com, PDD-Asia@vishay.com, PDD-Europe@vishay.com







### **New Product**

# VSIB15A20 thru VSIB15A80





<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS	SYMBOL	VSIB15A20	VSIB15A40	VSIB15A60	VSIB15A80	UNIT
Maximum instantaneous forward voltage drop per diode	7.5 A	V <sub>F</sub>	1.00			V	
Maximum DC reverse current at rated DC blocking voltage per diode	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub>	10 250			μΑ	

<b>THERMAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	VSIB15A20	VSIB15A40	VSIB15A60	VSIB15A80	UNIT
Typical thermal resistance	${\sf R}_{ heta {\sf JA}} \ {\sf R}_{ heta {\sf JC}}$	22 <sup>(2)</sup> 1.5 <sup>(1)</sup>		°C/W		

Notes:

(1) Unit case mounted on aluminum plate heatsink

(2) Units mounted on P.C.B. without heatsink

(3) Recommended mounting position is to bolt down on heatsink with silicone thermal compound for maximum heat transfer with #6 screw

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
VSIB15A60-E3/45	7.0	45	20	Tube			

#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

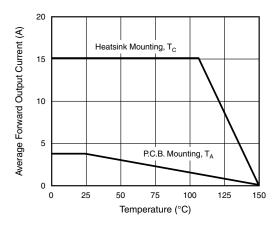


Figure 1. Derating Curve Output Rectified Current

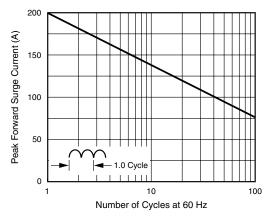


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Diode



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**New Product** 

# VSIB15A20 thru VSIB15A80

Vishay General Semiconductor

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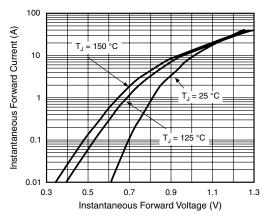


Figure 3. Typical Forward Characteristics Per Diode

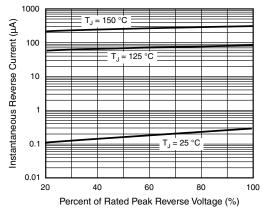
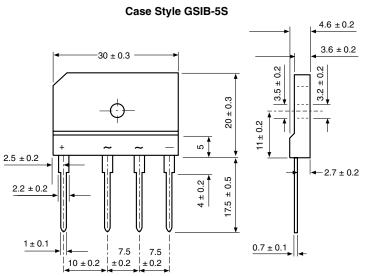
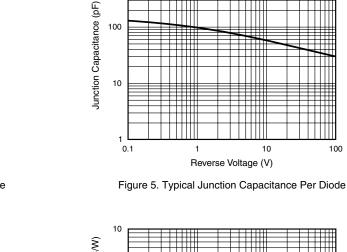


Figure 4. Typical Reverse Characteristics Per Diode







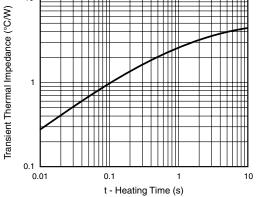


Figure 6. Typical Transient Thermal Impedance

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