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Stocking Distributor

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Diodes Incorporated DMG8822UTS-13

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>





Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 3)
- "Green" Device (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

Pb Lead-free

DMG8822UTS

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Mechanical Data

- Case: TSSOP-8L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4

G1 🤇

Weight: 0.039 grams (approximate)



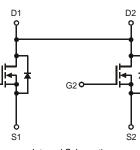




Top View Pin Configuration

D S2

S2 G2



Internal Schematic

Maximum Ratings @T_A = 25°C unless otherwise specified

				1	1
Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 1)	Steady State	T _A = 25°C T _A = 70°C	ID	4.9 3.9	А
Pulsed Drain Current (Note 2)			I _{DM}	31	A

Thermal Characteristics

Notes:

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	PD	0.87	W
Thermal Resistance, Junction to Ambient $@T_A = 25^{\circ}C$	R _{θJA}	143	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

1. Device mounted on FR-4 PCB with minimum recommended pad layout.

2. Repetitive rating, pulse width limited by junction temperature.

3. No purposefully added lead.

4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.



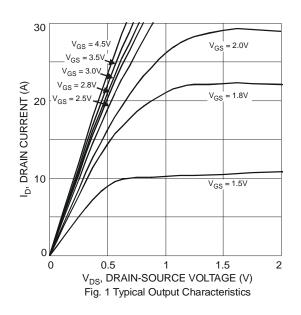


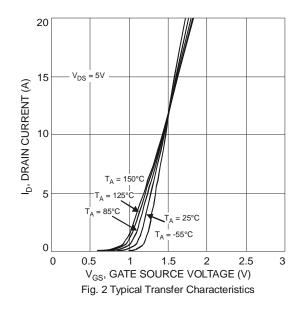
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(th)}	0.5	-	0.9	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
		-	19 22	25 29 37	mΩ	$V_{GS} = 4.5V, I_D = 8.2A$	
Static Drain-Source On-Resistance	R _{DS (ON)}					$V_{GS} = 2.5V, I_D = 3.3A$	
			28			$V_{GS} = 1.8V, I_D = 2.0A$	
Forward Transfer Admittance	Y _{fs}	-	7	-	S	$V_{DS} = 10V, I_D = 4A$	
Diodes Forward Voltage	V _{SD}	-	0.7	0.9	V	Is = 2.25A, V _{GS} = 0V	
DYNAMIC CHARACTERISTICS (Note 6)							
Input Capacitance	C _{iss}	-	841	-	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	C _{oss}	-	88	-	pF		
Reverse Transfer Capacitance	C _{rss}	-	81	-	pF		
Gate Resistance	Rg	-	1.24	-	Ω	V _{DS} =0V, V _{GS} = 0V, f = 1MHz	
SWITCHING CHARACTERISTICS							
Total Gate Charge	Qg	-	9.6	-	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_D = 8.2A$	
Gate-Source Charge	Q _{gs}	-	1.4	-	nC		
Gate-Drain Charge	Q _{gd}	-	2.1	-	nC		
Turn-On Delay Time	t _{D(on)}	-	7.8	-	ns		
Turn-On Rise Time	tr	-	21.1	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$ $R_L = 10\Omega, R_G = 6\Omega$	
Turn-Off Delay Time	t _{D(off)}	-	38.6	-	ns		
Turn-Off Fall Time	t _f	-	10.1	-	ns		

Notes: 5. Short duration pulse test used to minimize self-heating effects.

Guaranteed by design. Not subject to production testing.





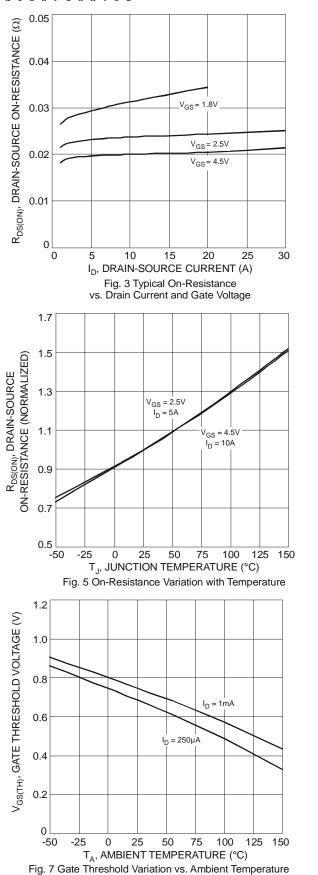


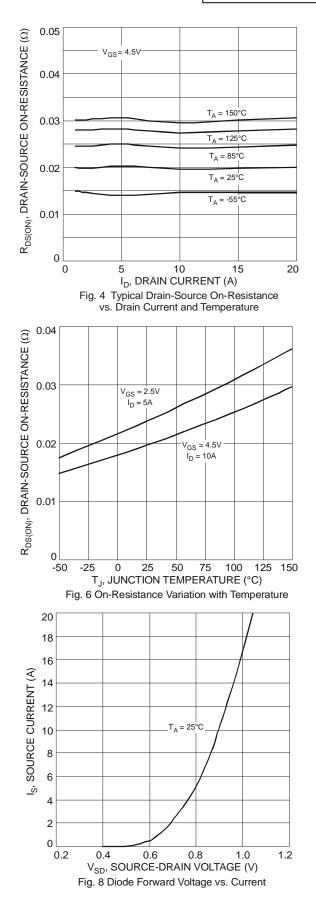
NEW PRODUCT

Distributor of Diodes Incorporated: Excellent Integrated System Limited Datasheet of DMG8822UTS-13 - MOSFET 2N-CH 20V 4.9A 8TSSOP Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

DODES

DMG8822UTS





DMG8822UTS Document number: DS31798 Rev. 2 - 2



NEW PRODUCT

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DMG8822UTS 10.000 10,000 I_{DSS}, DRAIN-SOURCE LEAKAGE CURRENT (nA) f = 1MHz $T_A = 150^{\circ}C$ 1,000 T_A = 125°C C, CAPACITANCE (pF) 1,000 Ciss 100 T_A = 85°C C_{oss} 100 C_{rs} 10 $T_A = 25^{\circ}C$ 10 1 0 5 10 15 20 2 4 6 8 10 12 14 16 18 20 V_{DS}, DRAIN-SOURCE VOLTAGE (V) V_{DS}, DRAIN-SOURCE VOLTAGE (V) Fig. 9 Typical Capacitance Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage D = 0.7 r(t), TRANSIENT THERMAL RESISTANCE D = 0.5 D = 0.3 0.1 D = 0.1 = 0.9 D D = 0.05 $R_{\theta JA}(t) = r(t) * R_{\theta JA}$ $R_{\theta JA} = 141^{\circ}C/W$ D = 0.02 0.01 D = 0.01 P(pk) D = 0.005 4 t₂ $T_J - T_A = P * R_{\theta JA}(t)$ Duty Cycle, $D = t_1/t_2$ - | | | D = Single Puls 0.001 0.00001 0.0001 0.001 0.01 0.1 10 100 1,000 1 t1, PULSE DURATION TIME (s)

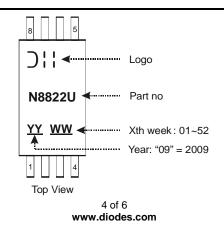
Fig. 11 Transient Thermal Response

Ordering Information (Note 7)

Packaging
2500 / Tape & Reel

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

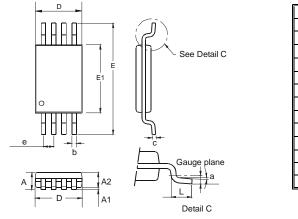






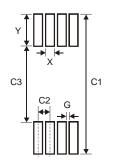
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Package Outline Dimensions



TSSOP-8L						
Dim	Min	Max	Тур			
а	0.09	1	-			
Α	1	1.20	-			
A1	0.05	0.15	_			
A2	0.825	1.025	0.925			
b	0.19	0.30	_			
С	0.09	0.20	-			
D	2.90	3.10	3.025			
е	1	1	0.65			
Е	_	_	6.40			
E1	4.30	4.50	4.425			
L	0.45	0.75	0.60			
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)
Х	0.45
Y	1.78
C1	7.72
C2	0.65
C3	4.16
G	0.20





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