

Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

[Bel Fuse Inc.](#)
[SI-55003-F](#)

For any questions, you can email us directly:
sales@integrated-circuit.com

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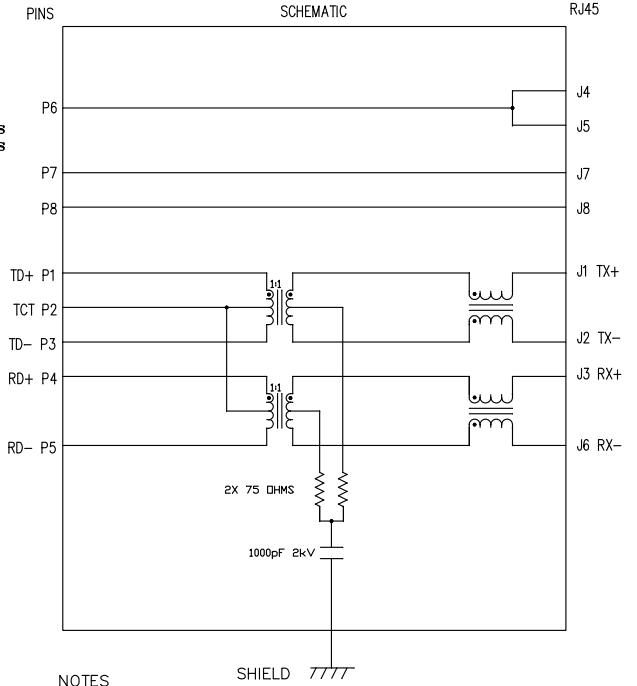


PRELIMINARY

ELECTRICAL CHARACTERISTICS @ 25°C

OPERATING TEMPERATURE RANGE: -40°C TO +85°C

- 1.0 TURNS RATIO: (P4-P2-P5) : (J3-J6) : 1CT : 1CT ± 3%
 (P1-P2-P3) : (J1-J2) : 1CT : 1CT ± 3%
- 2.0 INDUCTANCE: (P1-P3) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
 (P4-P5) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
- 3.0 LEAKAGE INDUCTANCE: P5-P4(WITH J6 AND J3 SHORT) : 0.3uH MAX. @ 1MHz
 P1-P3(WITH J2 AND J1 SHORT) : 0.3uH MAX. @ 1MHz
- 4.0 INTERWINDING CAPACITANCE: (P5,P2,P4) TO (J6,J3) : 60pF MAX. @ 1MHz
 (P3,P2,P1) TO (J2,J1) : 60pF MAX. @ 1MHz
- 5.0 DC RESISTANCE:(J6-J3)=(J2-J1) : 1.5 ohms Max.
- 6.0 RETURN LOSS:(P1-P3)=100 OHMS AND (P4-P5)=100 OHM REF.
 1MHz TO 30MHz : 18dB MIN.
 30MHz TO 80MHz : 12dB MIN.
 NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).
- 7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) : 1500 Vrms
 (J3, J6) TO (P5, P3) : 1500 Vrms
- 8.0 INSERTION LOSS:RS=RL=100 ohms : 1.1 dB TYP
 100KHz TO 100MHz
- 9.0 RISE TIME:RS=100 OHMS AND RL = 100 OHMS : 3.0 nS MAX
 OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX
 PULSE WIDTH= 112ns
- 10.0 CROSS TALK₁-100 MHz : 30 dB TYP
- 11.0 COMMON TO COMMON MODE ATTENUATION:
 1MHz TO 100MHz : 35dB TYP



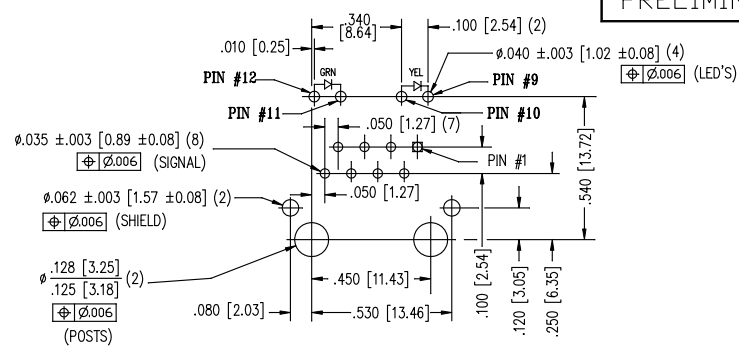
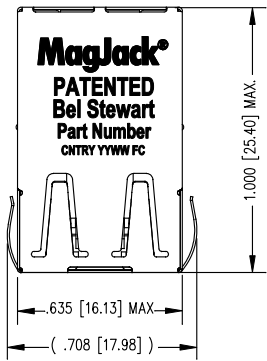
NOTES
 1.0 ALL RESISTORS ARE 5% TOLERANCE.

ORIGINATED BY ZL Wang	DATE 01-21-10	TITLE MagJack® 100/100BT TAB UP SHIELDED PATENTED	PART NO. / DRAWING NO. SI-55003-F	STANDARD DIM. TOL. IN INCH X XX XXX ±0.005	[] METRIC DIM. AS REFERENCE UNIT : INCH [mm] SCALE : N/A	REV. : 01 SIZE : A4 PAGE : 2	
DRAWN BY RS Zhang	DATE 01-21-10		FILE NAME SI-55003-F.DWG				

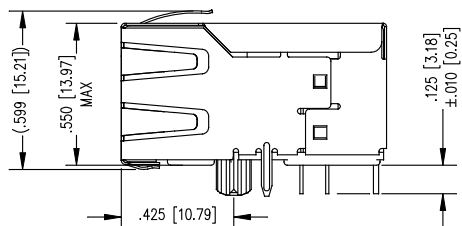
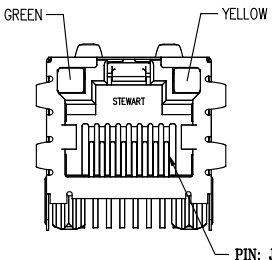
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PRELIMINARY



P.C.B. RECOMMENDED HOLE LAYOUT
 SEEN FROM COMPONENT SIDE
 ALL CENTERLINE DIMENSIONS ARE BASIC.



LED SPECIFICATION			
STANDARD LED	WAVELENGTH	FORWARD V (MAX)	*(TYP)
GREEN	565 nm	2.5 V	2.2 V
YELLOW	590 nm	2.5 V	2.1 V

*WITH A FORWARD CURRENT OF 20 mA (TYP)

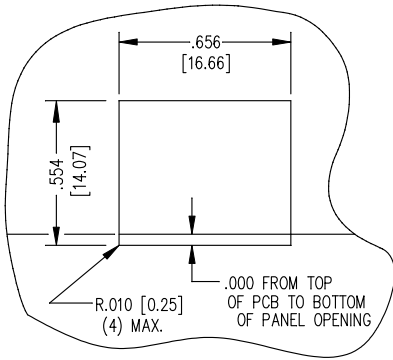
- NOTES:
- CONNECTOR MATERIALS:
 HOUSING: THERMOPLASTIC UL94 V-0
 CONTACT/SHIELD: COPPER ALLOY
 SHIELD PLATING: NICKEL OR TIN
 CONTACT PLATING: SELECTIVE GOLD,
 50 MICRO-INCHES MIN. IN CONTACT AREA.
 - PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
 SEE ELECTRICAL DRAWING FOR OMITTED PINS.
 - TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
 - ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±0.005 [0.13]
 - REFLOW AND WAVE SOLDER COMPATIBLE-260°C FOR 10 SECONDS MAX.

ORIGINATED BY ZL Wang	DATE 01-21-10	TITLE MagJack® 100/100BT TAB UP SHIELDED PATENTED	PART NO. / DRAWING NO. SI-55003-F	STANDARD DIM. TOL. IN INCH .X .XX .XXX ±0.005	[] METRIC DIM. AS REFERENCE UNIT : INCH [mm] SCALE : N/A	REV. : 01 SIZE : A4 PAGE : 3	bel COMPONENTS FOR A CONNECTED PLANET
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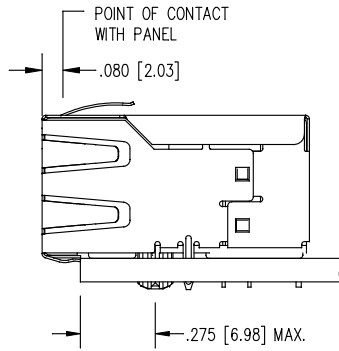


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SUGGESTED PANEL OPENING



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ± 0.005 [0.13]

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