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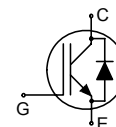
IKP04N60T

TRENCHSTOP™ Series

Low Loss DuoPack : IGBT in TRENCHSTOP™ and Fieldstop technology
 with soft, fast recovery anti-parallel Emitter Controlled HE diode

Very low $V_{CE(sat)}$ 1.5V (typ.)

- Maximum Junction Temperature 175°C
- Short circuit withstand time 5 □
- Designed for :
 - Frequency Converters
 - Drives
- TRENCHSTOP™ and Fieldstop technology for 600V applications offers :
 - very tight parameter distribution
 - high ruggedness, temperature stable behavior
 - very high switching speed
 - low $V_{CE(sat)}$
- Positive temperature coefficient in $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel Emitter Controlled HE diode
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt>



Type	V_{CE}	I_C	$V_{CE(sat), T_j=25^\circ C}$	$T_{j,max}$	Marking	Package
IKP04N60T	600 V	4 A	1.5 V	175 °C	K04T60	PG-TO220-3

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage , $T_j \geq 25^\circ C$	V_{CE}	600	V
DC collector current, limited by $T_{j,max}$	I_C		A
$T_C = 25^\circ C$		8	
$T_C = 100^\circ C$		4	
Pulsed collector current, t_p limited by $T_{j,max}$	I_{Cpuls}	12	
Turn off safe operating area, $V_{CE} = 600V, T_j = 175^\circ C, t_p = 1\mu s$	-	12	
Diode forward current, limited by $T_{j,max}$	I_F		
$T_C = 25^\circ C$		8	
$T_C = 100^\circ C$		4	
Diode pulsed current, t_p limited by $T_{j,max}$	I_{Fpuls}	12	
Gate-emitter voltage	V_{GE}	± 20	V
Short circuit withstand time ²⁾	t_{SC}	5	μs
$V_{GE} = 15V, V_{CC} \leq 400V, T_j \leq 150^\circ C$			
Power dissipation $T_C = 25^\circ C$	P_{tot}	42	W
Operating junction temperature	T_j	-40...+175	$^\circ C$
Storage temperature	T_{stg}	-55...+150	
Soldering temperature, 1.6mm (0.063 in.) from case for 10s	-	260	

¹ J-STD-020 and JESD-022

²⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.



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Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		3.5	K/W
Diode thermal resistance, junction – case	R_{thJCD}		5	
Thermal resistance, junction – ambient	R_{thJA}		62	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=0.2\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{V}, I_C=4\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.5	2.05	
Diode forward voltage	V_F	$V_{GE}=0\text{V}, I_F=4\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.65	2.05	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 60\mu\text{A}, V_{CE} = V_{GE}$	4.1	4.9	5.7	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	-	40	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	
Transconductance	g_{fs}	$V_{CE}=20\text{V}, I_C=4\text{A}$	-	2.2	-	S
Integrated gate resistor	R_{Gint}			-		Ω

Dynamic Characteristic

Input capacitance	C_{ies}	$V_{CE}=25\text{V},$	-	252	-	pF
Output capacitance	C_{oes}	$V_{GE}=0\text{V},$	-	20	-	
Reverse transfer capacitance	C_{fes}	$f=1\text{MHz}$	-	7.5	-	
Gate charge	Q_{Gate}	$V_{CC}=480\text{V}, I_C=4\text{A}$ $V_{GE}=15\text{V}$	-	27	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	7	-	nH
Short circuit collector current ¹⁾	$I_{C(SC)}$	$V_{GE}=15\text{V}, t_{SC}\leq 5\mu\text{s}$ $V_{CC} = 400\text{V},$ $T_j \leq 150^\circ\text{C}$	-	36	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.



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Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=4\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=47\ \Omega$, $L_{\sigma}^{(1)}=150\text{nH}$, $C_{\sigma}^{(1)}=47\text{pF}$ Energy losses include "tail" and diode reverse recovery.	-	14	-	ns
Rise time	t_r		-	7	-	
Turn-off delay time	$t_{d(off)}$		-	164	-	
Fall time	t_f		-	43	-	
Turn-on energy	E_{on}		-	61	-	μJ
Turn-off energy	E_{off}		-	84	-	
Total switching energy	E_{ts}		-	145	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	$T_j=25^\circ\text{C}$, $V_R=400\text{V}$, $I_F=4\text{A}$, $di_F/dt=610\text{A}/\mu\text{s}$	-	28	-	ns
Diode reverse recovery charge	Q_{rr}		-	79	-	nC
Diode peak reverse recovery current	I_{rrm}		-	5.3	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	346	-	$\text{A}/\mu\text{s}$

Switching Characteristic, Inductive Load, at $T_j=175^\circ\text{C}$

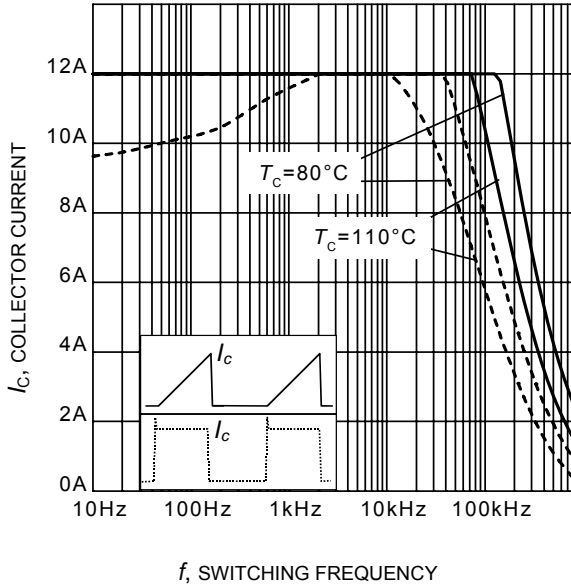
Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=175^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=4\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=47\ \Omega$, $L_{\sigma}^{(1)}=150\text{nH}$, $C_{\sigma}^{(1)}=47\text{pF}$ Energy losses include "tail" and diode reverse recovery.	-	14	-	ns
Rise time	t_r		-	10	-	
Turn-off delay time	$t_{d(off)}$		-	185	-	
Fall time	t_f		-	83	-	
Turn-on energy	E_{on}		-	99	-	μJ
Turn-off energy	E_{off}		-	97	-	
Total switching energy	E_{ts}		-	196	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	$T_j=175^\circ\text{C}$, $V_R=400\text{V}$, $I_F=4\text{A}$, $di_F/dt=610\text{A}/\mu\text{s}$	-	95	-	ns
Diode reverse recovery charge	Q_{rr}		-	291	-	nC
Diode peak reverse recovery current	I_{rrm}		-	6.6	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	253	-	$\text{A}/\mu\text{s}$

¹⁾ Leakage inductance L_{σ} and Stray capacity C_{σ} due to dynamic test circuit in Figure E.

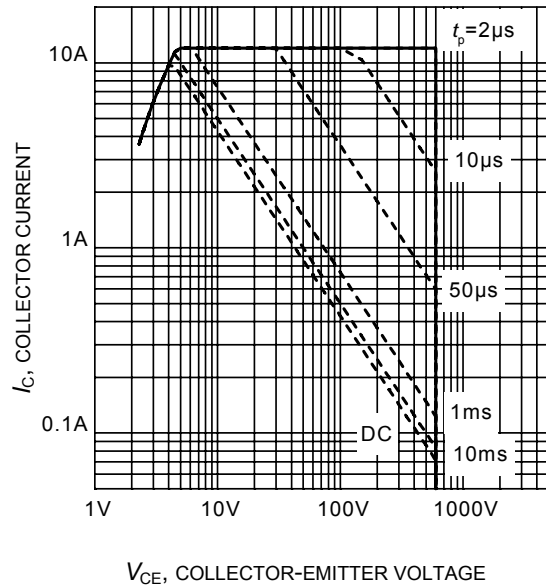


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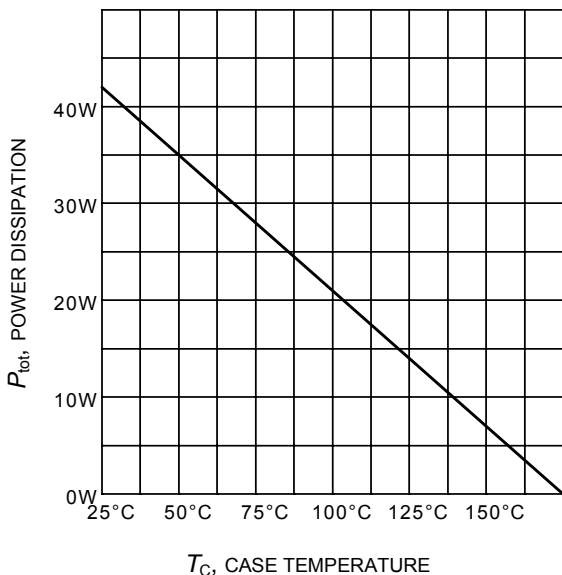
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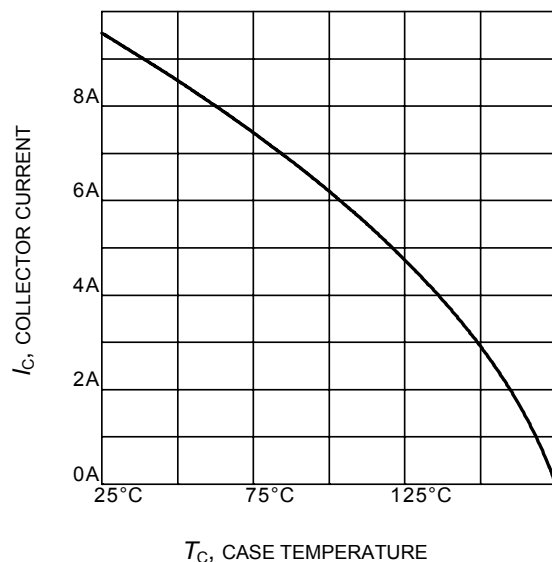
f , SWITCHING FREQUENCY
Figure 1. Collector current as a function of switching frequency
 ($T_j \leq 175^\circ\text{C}$, $D = 0.5$, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $R_G = 47\Omega$)



V_{CE} , COLLECTOR-EMITTER VOLTAGE
Figure 2. Safe operating area
 ($D = 0$, $T_C = 25^\circ\text{C}$, $T_j \leq 175^\circ\text{C}$;
 $V_{GE} = 0/15\text{V}$)



T_C , CASE TEMPERATURE
Figure 3. Power dissipation as a function of case temperature
 ($T_j \leq 175^\circ\text{C}$)



T_C , CASE TEMPERATURE
Figure 4. Collector current as a function of case temperature
 ($V_{GE} \geq 15\text{V}$, $T_j \leq 175^\circ\text{C}$)



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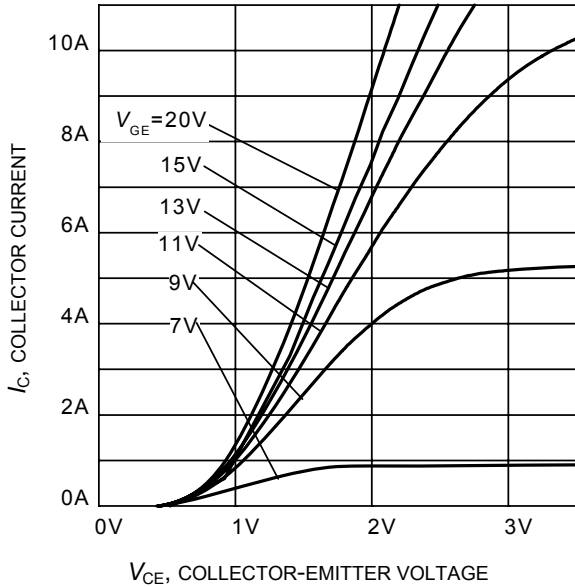


Figure 5. Typical output characteristic
($T_j = 25^\circ\text{C}$)

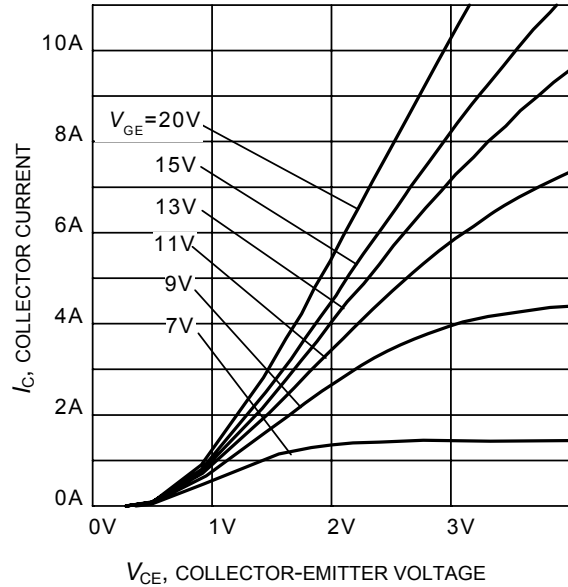


Figure 6. Typical output characteristic
($T_j = 175^\circ\text{C}$)

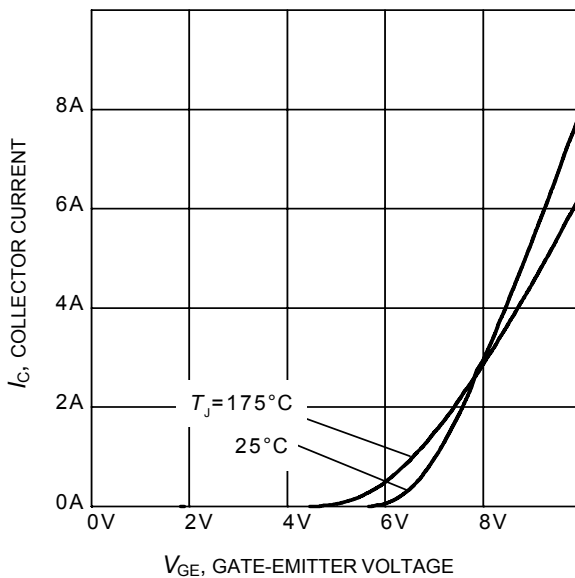


Figure 7. Typical transfer characteristic
($V_{CE} = 20\text{V}$)

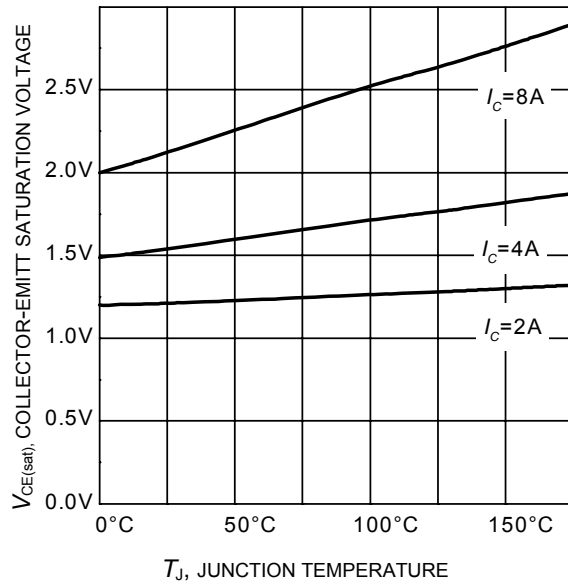


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)



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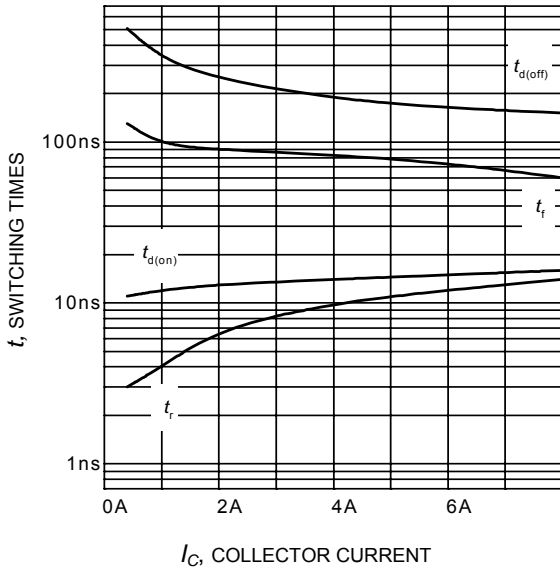


Figure 9. Typical switching times as a function of collector current
 (inductive load, $T_J=175^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $R_G=47\Omega$, Dynamic test circuit in Figure E)

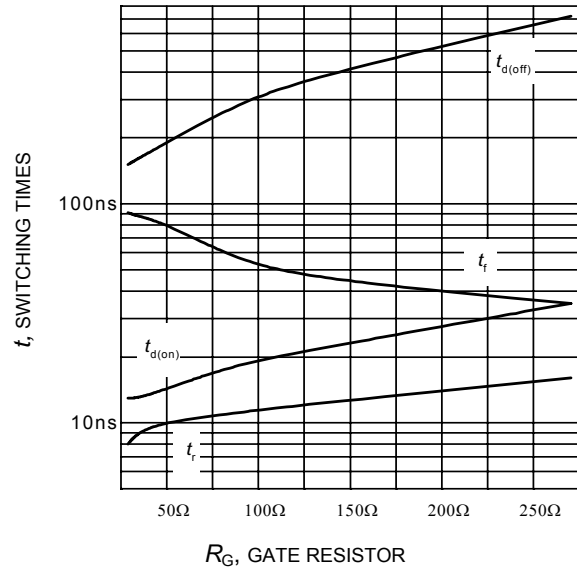


Figure 10. Typical switching times as a function of gate resistor
 (inductive load, $T_J=175^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=4\text{A}$, Dynamic test circuit in Figure E)

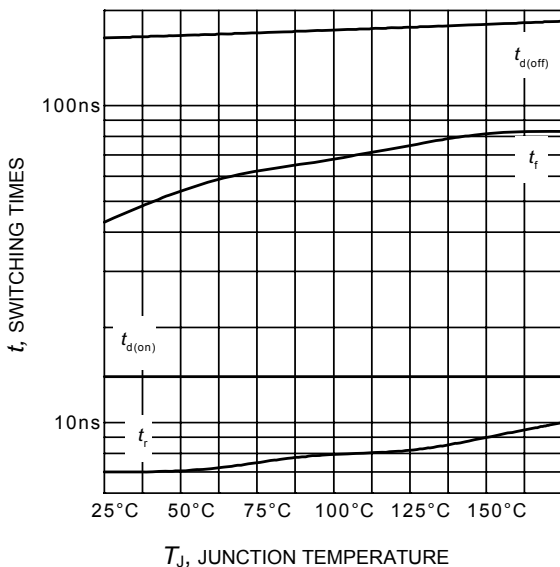


Figure 11. Typical switching times as a function of junction temperature
 (inductive load, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=4\text{A}$, $R_G=47\Omega$, Dynamic test circuit in Figure E)

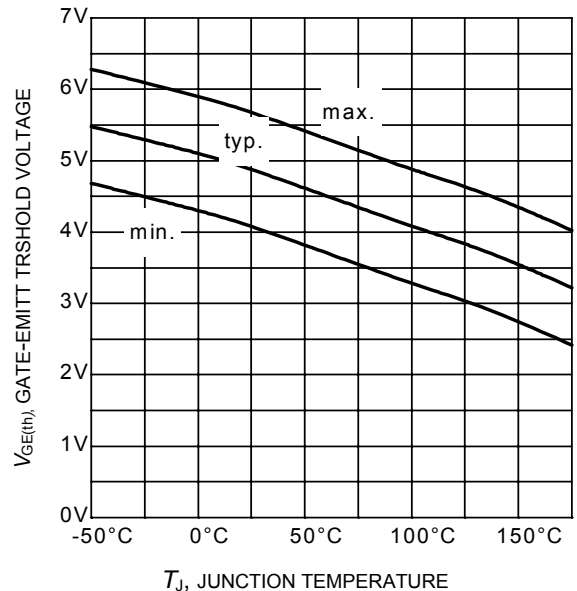


Figure 12. Gate-emitter threshold voltage as a function of junction temperature
 ($I_C=60\mu\text{A}$)



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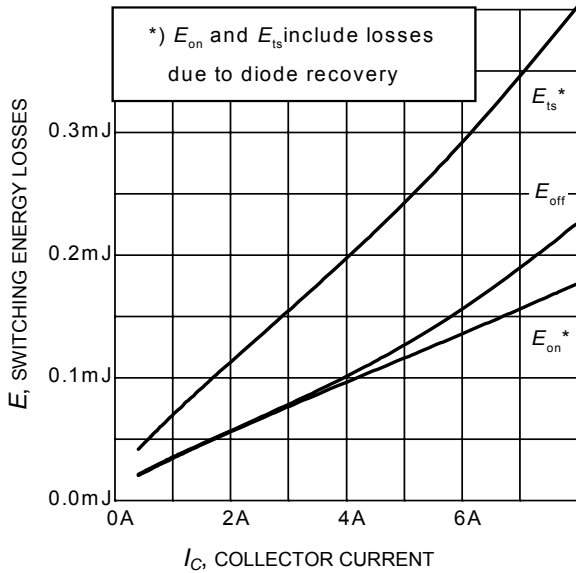


Figure 13. Typical switching energy losses as a function of collector current
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $R_G = 47\Omega$, Dynamic test circuit in Figure E)

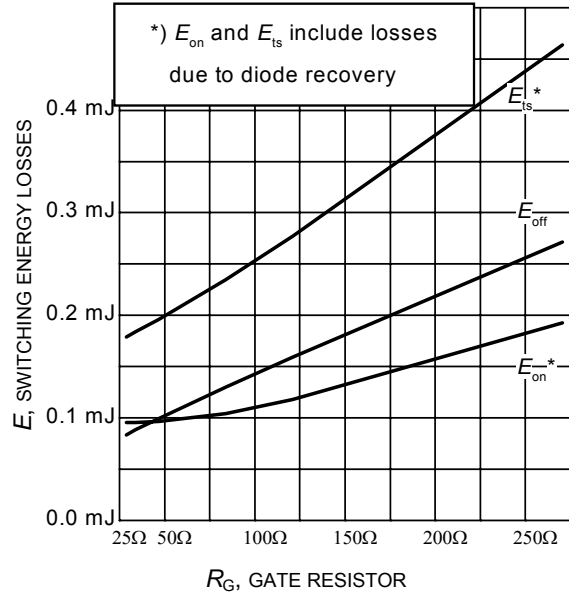


Figure 14. Typical switching energy losses as a function of gate resistor
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 4\text{A}$, Dynamic test circuit in Figure E)

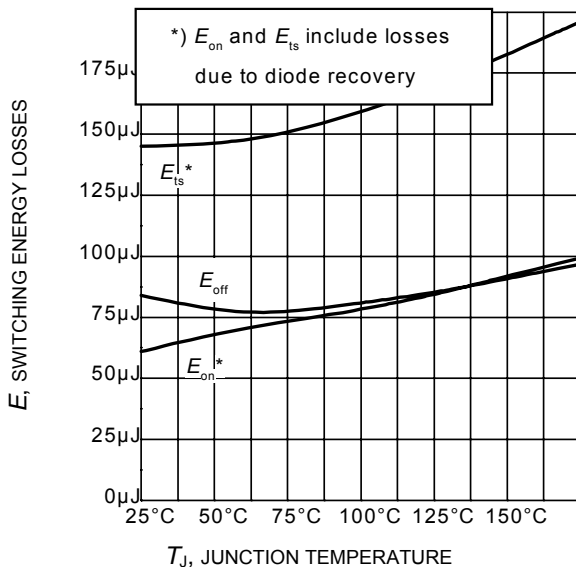


Figure 15. Typical switching energy losses as a function of junction temperature
 (inductive load, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 4\text{A}$, $R_G = 47\Omega$, Dynamic test circuit in Figure E)

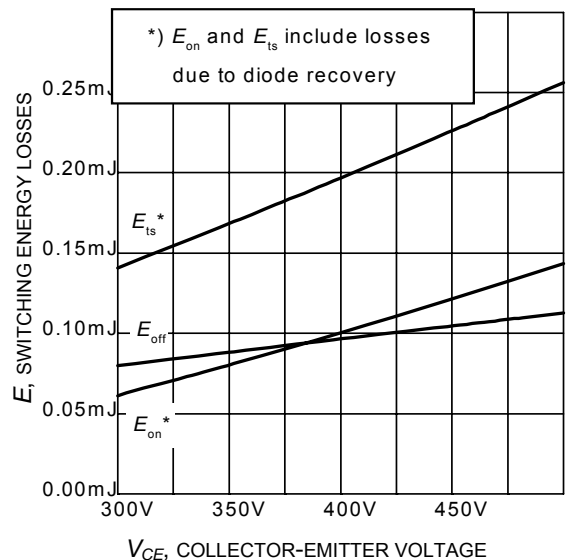


Figure 16. Typical switching energy losses as a function of collector emitter voltage
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{GE} = 0/15\text{V}$, $I_C = 4\text{A}$, $R_G = 47\Omega$, Dynamic test circuit in Figure E)



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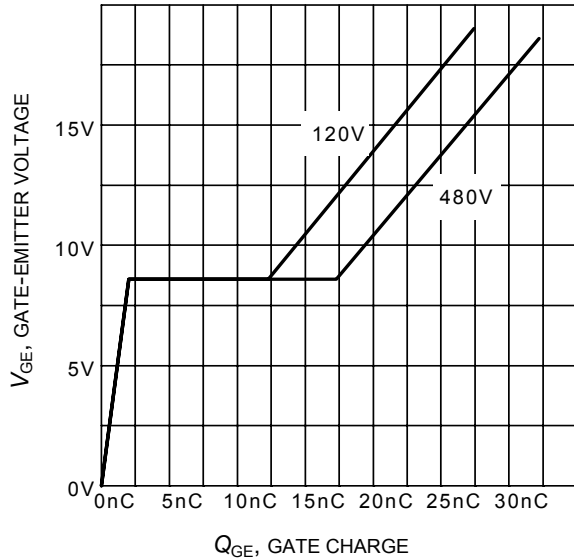


Figure 17. Typical gate charge
($I_C=4\text{ A}$)

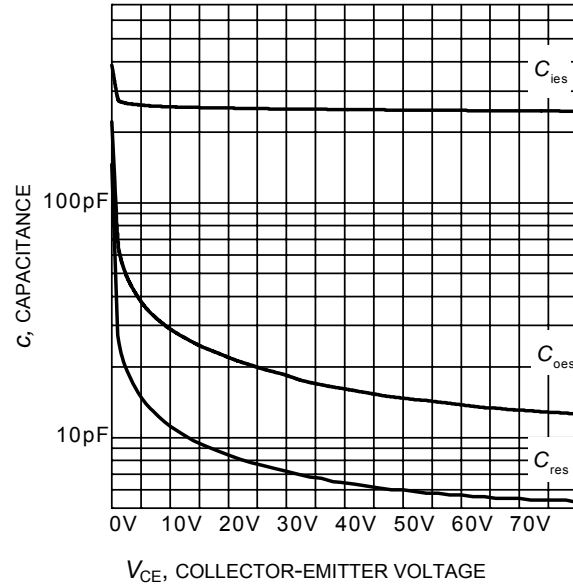


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE}=0\text{V}$, $f = 1\text{ MHz}$)

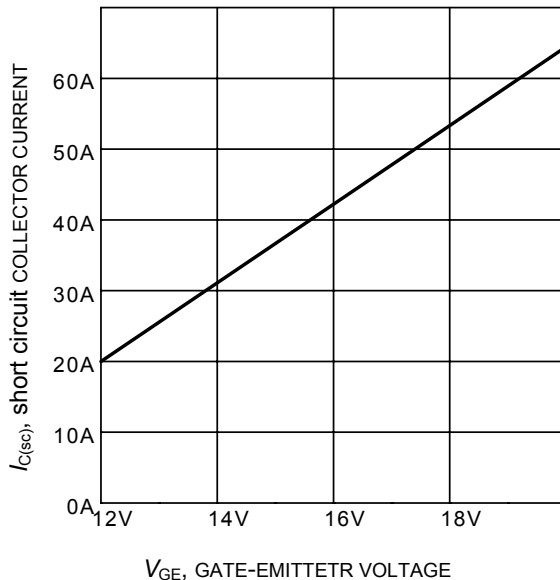


Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 400\text{V}$, $T_J \leq 150^\circ\text{C}$)

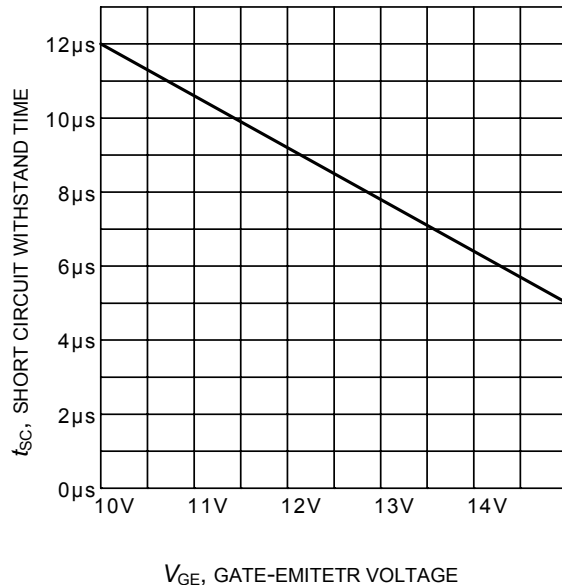


Figure 20. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE}=400\text{V}$, start at $T_J=25^\circ\text{C}$, $T_{Jmax}<150^\circ\text{C}$)



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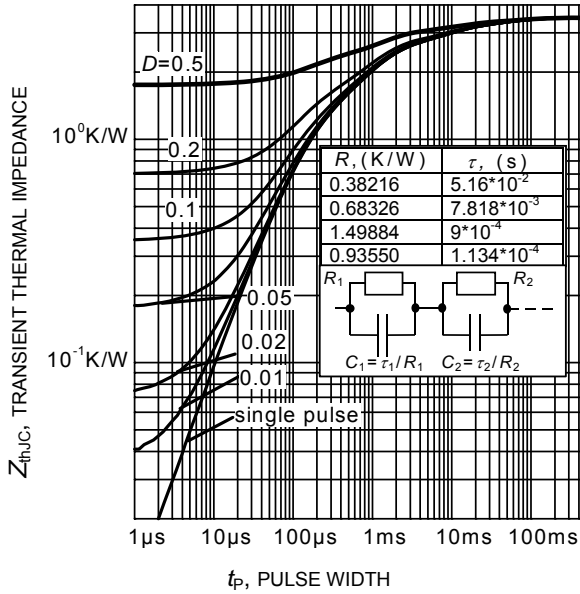


Figure 21. IGBT transient thermal impedance ($D = t_p / T$)

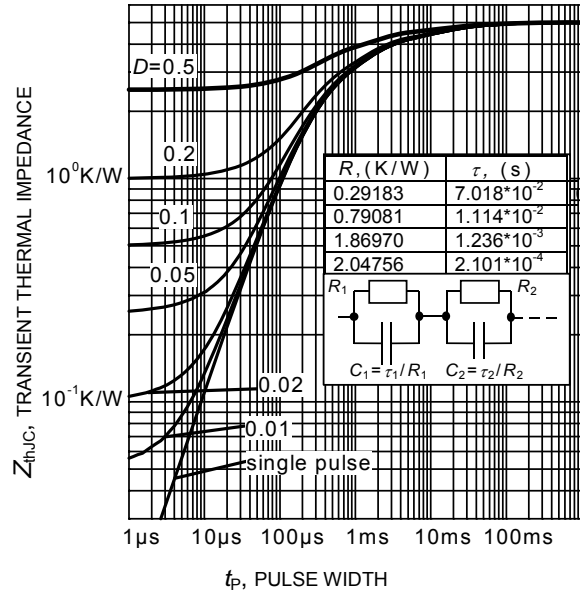


Figure 22. Diode transient thermal impedance as a function of pulse width ($D = t_p / T$)

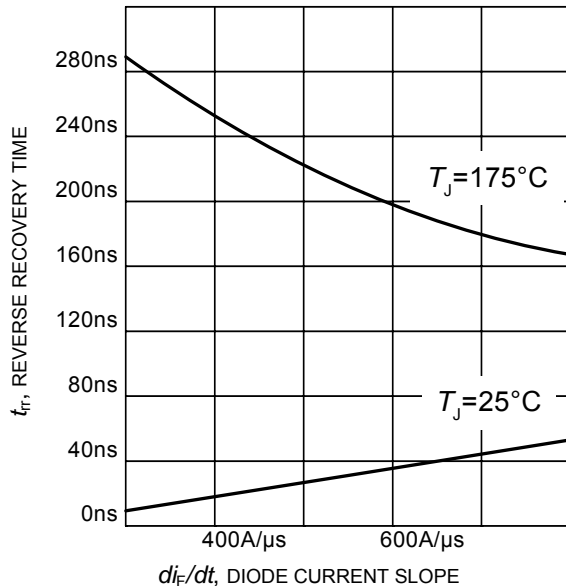


Figure 23. Typical reverse recovery time as a function of diode current slope ($V_R = 400V$, $I_F = 4A$, Dynamic test circuit in Figure E)

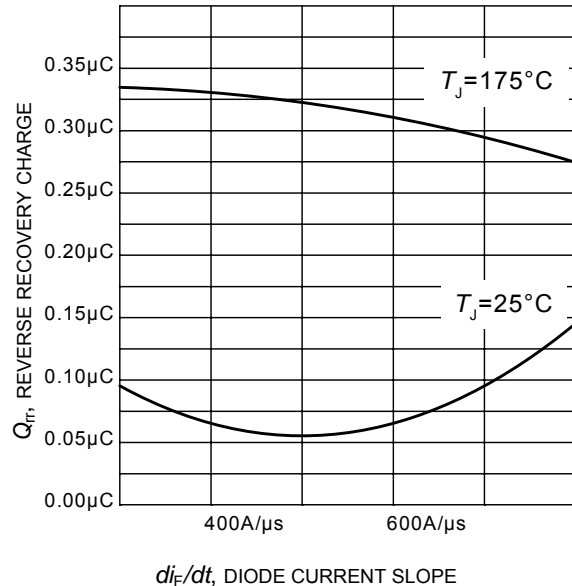
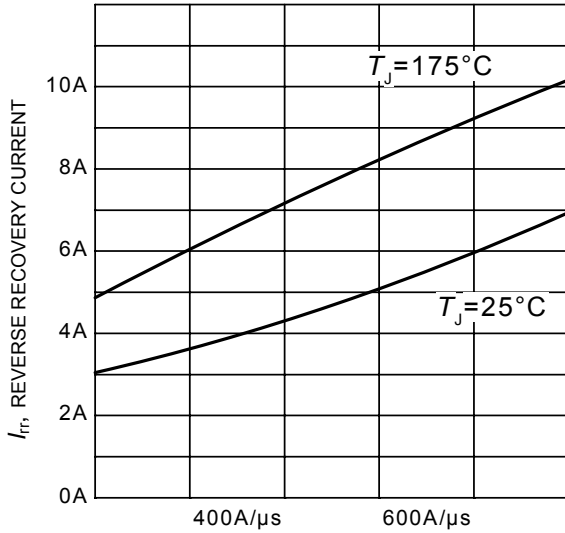


Figure 24. Typical reverse recovery charge as a function of diode current slope ($V_R = 400V$, $I_F = 4A$, Dynamic test circuit in Figure E)

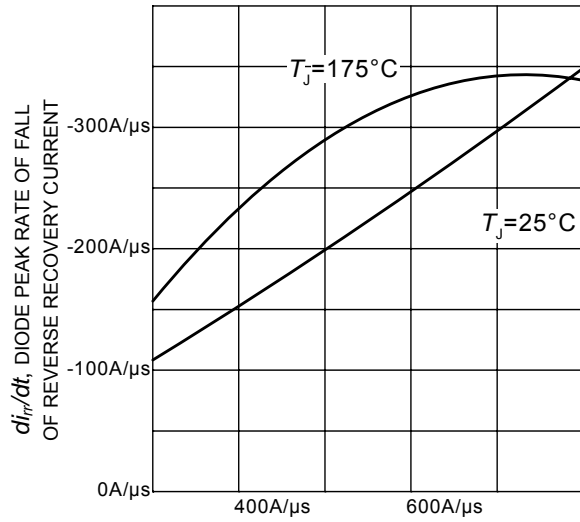


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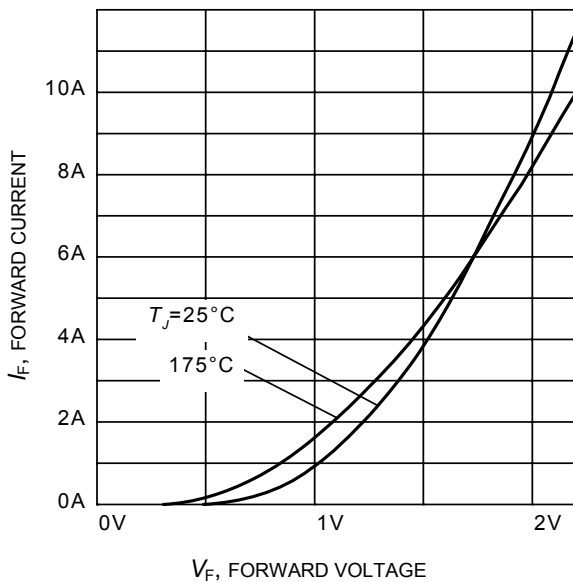
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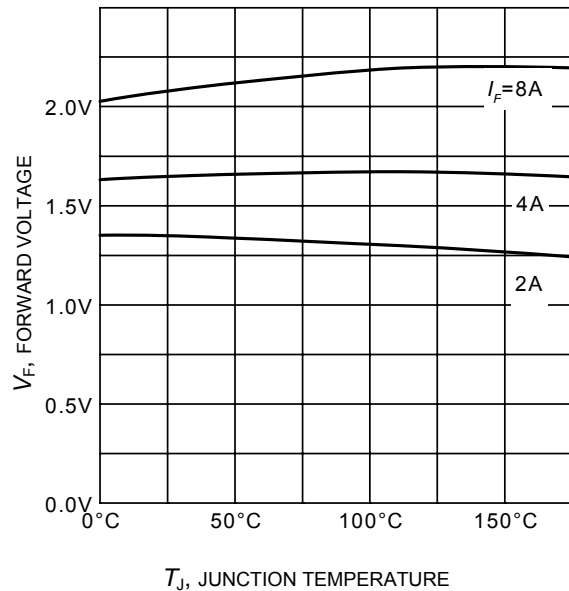
di_F/dt , DIODE CURRENT SLOPE
Figure 25. Typical reverse recovery current as a function of diode current slope
 ($V_R = 400V$, $I_F = 4A$,
 Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE
Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope
 ($V_R = 400V$, $I_F = 4A$,
 Dynamic test circuit in Figure E)



V_F , FORWARD VOLTAGE
Figure 27. Typical diode forward current as a function of forward voltage



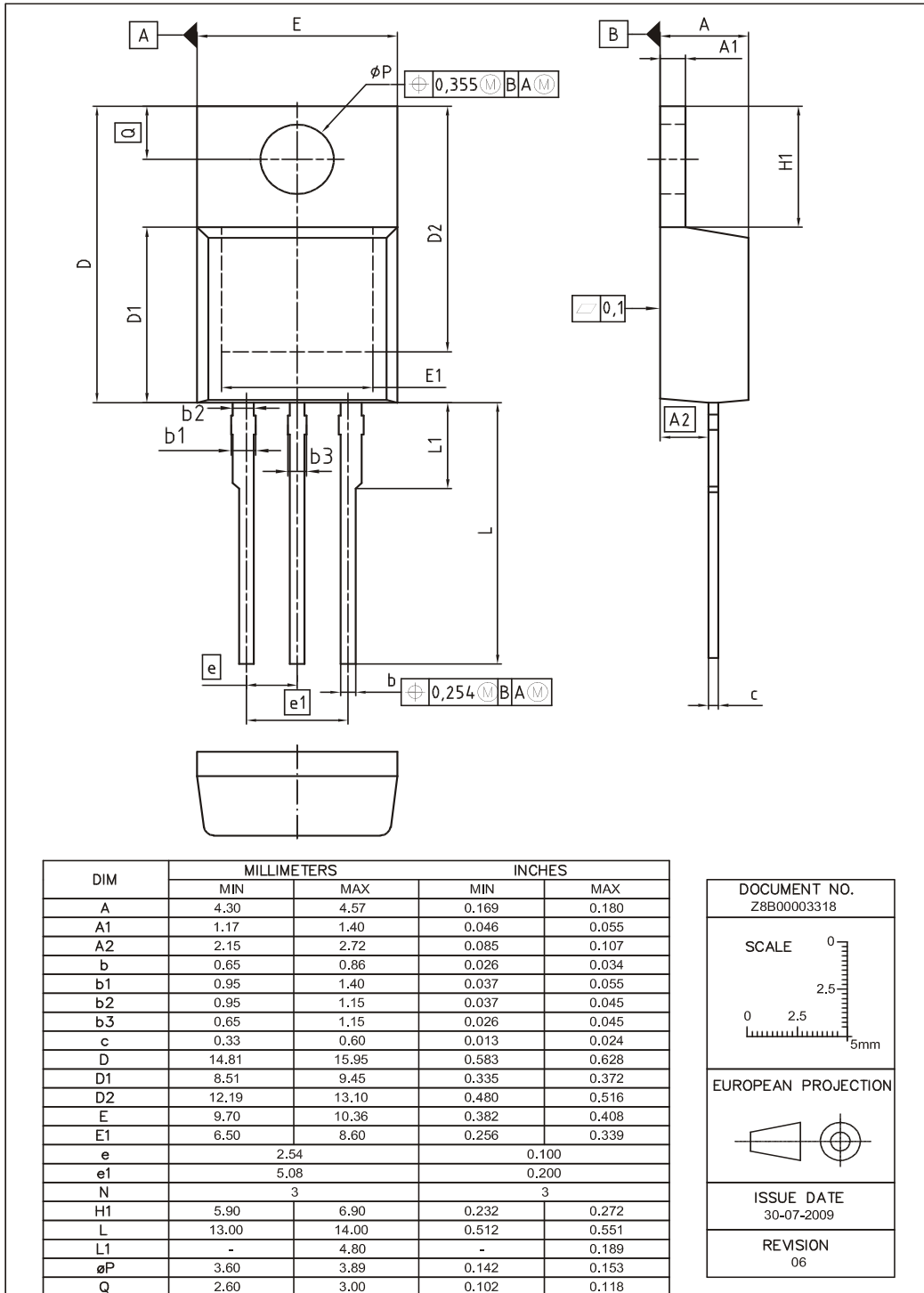
T_J , JUNCTION TEMPERATURE
Figure 28. Typical diode forward voltage as a function of junction temperature



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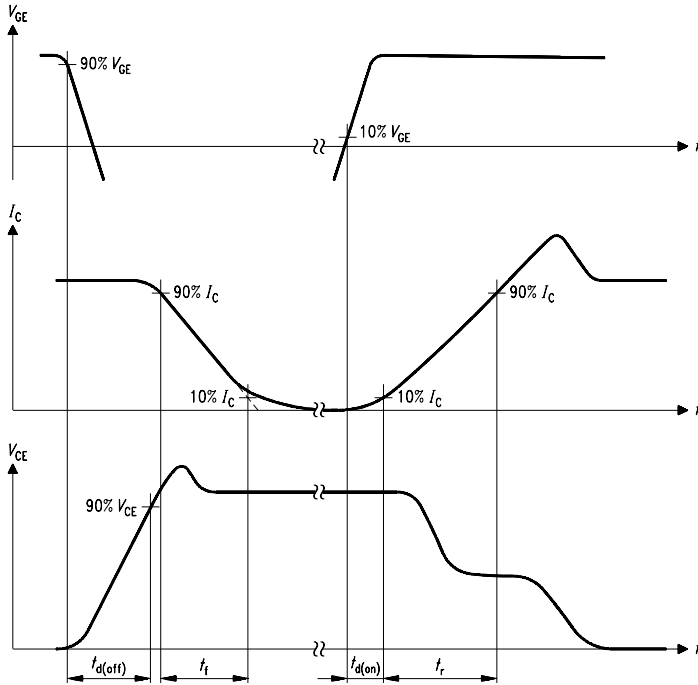


Figure A. Definition of switching times

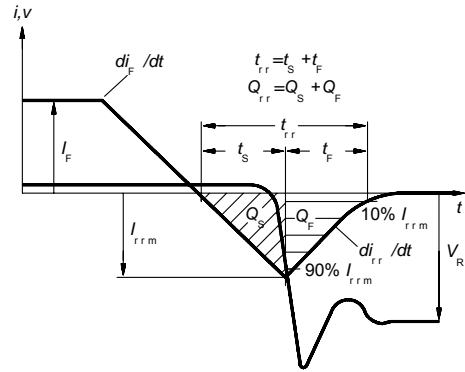


Figure C. Definition of diodes switching characteristics

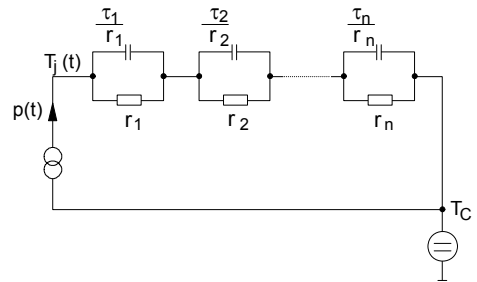


Figure D. Thermal equivalent circuit

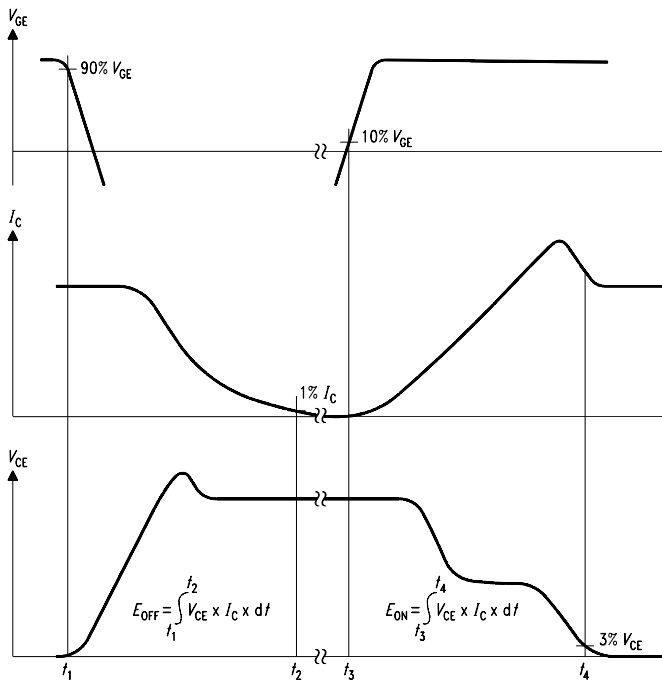


Figure B. Definition of switching losses

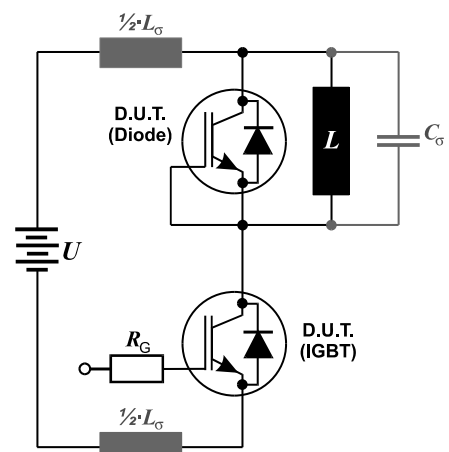


Figure E. Dynamic test circuit



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