

# **Excellent Integrated System Limited**

Stocking Distributor

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Vishay/Siliconix SI4104DY-T1-GE3

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#### **New Product**



## Si4104DY

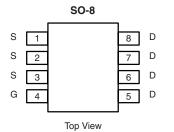
RoHS

FREE

Vishay Siliconix

## N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
100	0.105 at V <sub>GS</sub> = 10 V	4.6	8.5 nC		

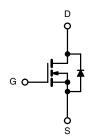


## FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchET<sup>®</sup> Power MOSFET
- 100 % Rg Tested
- 100 % Avalanche Tested
- Compliant to RoHS Directive 2002/95/EC

#### APPLICATIONS

- High Frequency DC/DC Converter
- High Frequency Boost Converter
- LED Backlight for LCD TV



N-Channel MOSFET

Ordering Information: Si4104DY-T1-E3 (Lead (Pb)-free) Si4104DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	100	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	v	
	T <sub>C</sub> = 25 °C		4.6		
Continuous Drain Current (T. 150 °C)	T <sub>C</sub> = 70 °C		3.7		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	3.2 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		2.6 <sup>b, c</sup>		
Pulsed Drain Current		I <sub>DM</sub>	15	A	
	T <sub>C</sub> = 25 °C		4.1		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	2.0 <sup>b, c</sup>		
Single Pulse Avalanche Current		I <sub>AS</sub>	9		
Single Pulse Avalanche Energy L = 0.1 mH		E <sub>AS</sub>	4	mJ	
	T <sub>C</sub> = 25 °C		5.0		
Marian an Diraination	T <sub>C</sub> = 70 °C		3.2		
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5 <sup>b, c</sup>	W	
	T <sub>A</sub> = 70 °C	1	1.6 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150	°C	

#### THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	38	50	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	20	25	0/11	

Notes:

a. Based on T<sub>C</sub> = 25 °C.

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under Steady State conditions is 85  $^{\circ}\text{C/W}.$ 



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<b>SPECIFICATIONS</b> $T_J = 25 \ ^{\circ}C$ ,	unless other	wise noted					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static		•					
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	100			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		112		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250 \mu A$		- 8.5			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	2.5		4.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1		
		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10$ V, $V_{GS} = 10$ V	10			Α	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.085	0.105	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5 A		7		S	
Dynamic <sup>b</sup>				•	•	<b></b>	
Input Capacitance	C <sub>iss</sub>			446		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		47			
Reverse Transfer Capacitance	C <sub>rss</sub>			18			
Total Gate Charge	Qg			8.5	13	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$		3			
Gate-Drain Charge	Q <sub>gd</sub>			2.5			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.3	1.3	2.5	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			9	18	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, \text{ R}_{L} = 10 \Omega$		9	18		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_{D}\cong$ 5 A, $V_{GEN}$ = 10 V, $R_{g}$ = 1 $\Omega$		10	20		
Fall Time	t <sub>f</sub>			8	16		
Drain-Source Body Diode Characteristic	s	•				•	
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			4.6	A	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				15		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 2 A		0.82	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			54	80	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$\begin{bmatrix} 1 \\ -10 \\ 0 \end{bmatrix}$		135	200	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = 10 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		48			
Reverse Recovery Rise Time	t <sub>b</sub>			6		ns	

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

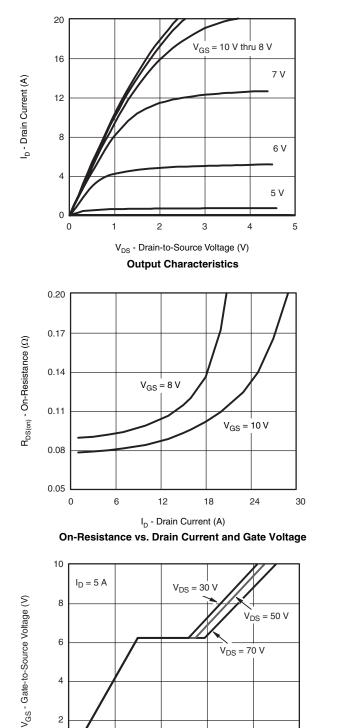


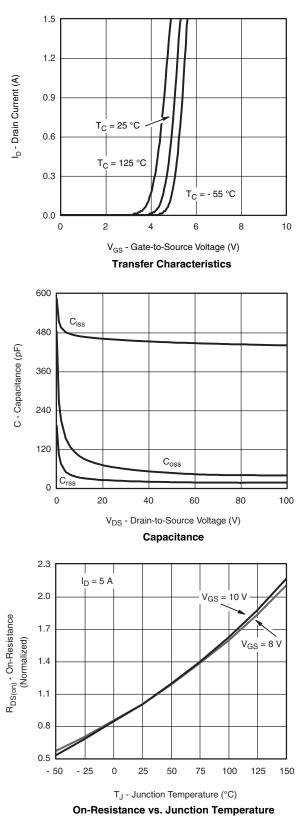
#### **New Product**

# **VISHAY**

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Document Number: 69936 S09-0764-Rev. B, 04-May-09

2

0

0

2

4

Q<sub>q</sub> - Total Gate Charge (nC)

Gate Charge

6

8

10



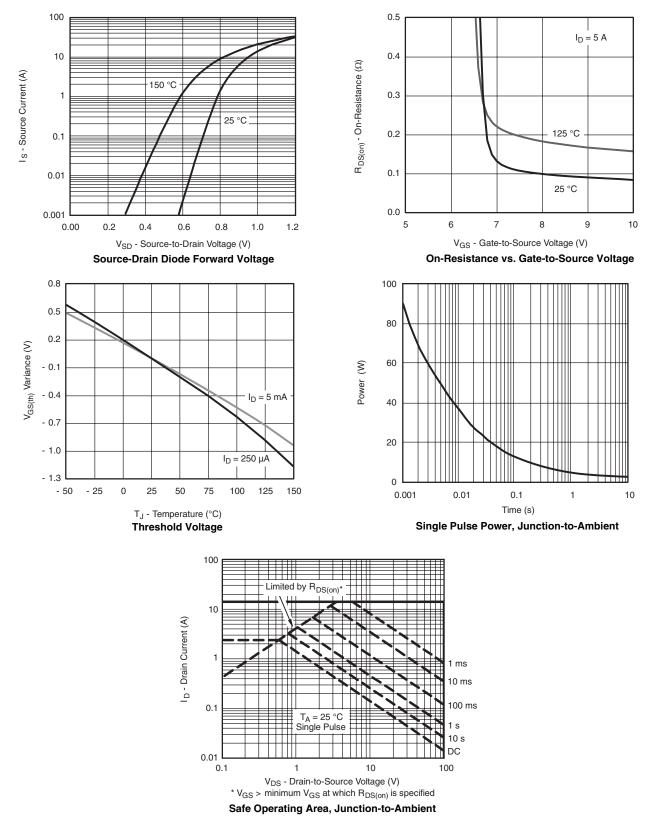
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## Vishay Siliconix



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





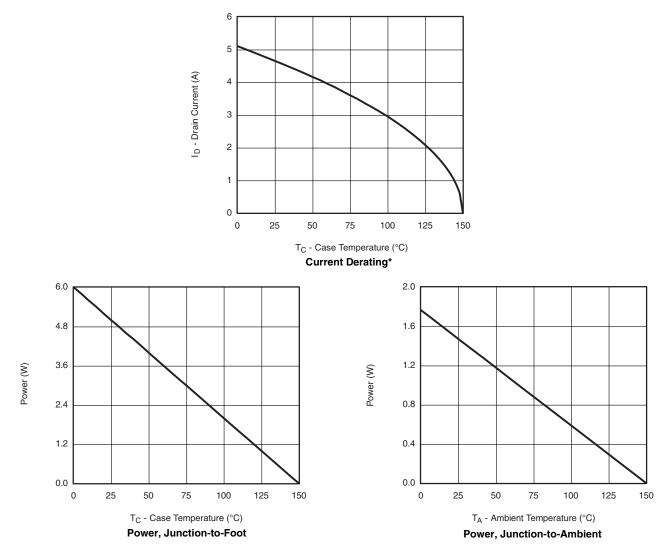
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



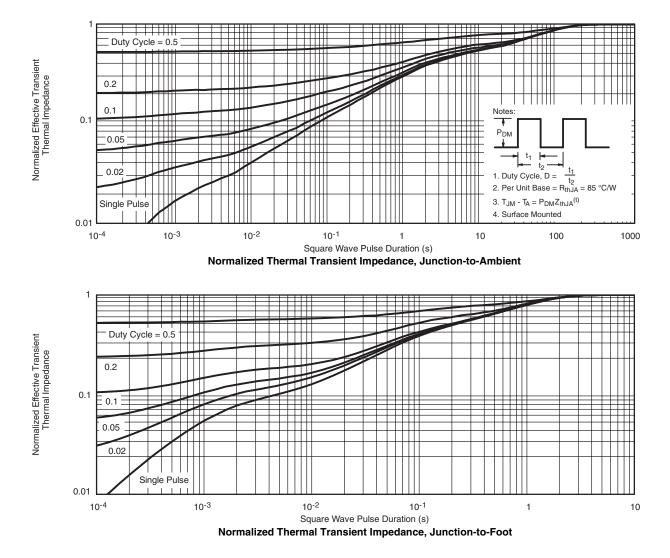
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?69936">www.vishay.com/ppg?69936</a>.





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