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Si4483ADY
 Vishay Siliconix

P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^d	Q _g (Typ.)
- 30	0.0088 at V _{GS} = - 10 V	- 19.2	44.8 nC
	0.0153 at V _{GS} = - 4.5 V	- 14.6	

FEATURES

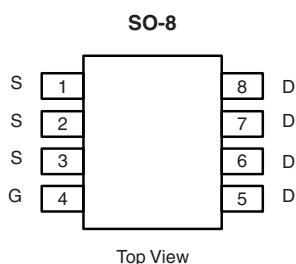
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
 COMPLIANT
 HALOGEN
FREE

APPLICATIONS

- Adaptor Switch



Ordering Information: SI4483ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 30	V	
Gate-Source Voltage	V _{GS}	± 25		
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	- 19.2	A
		T _C = 70 °C	- 15.4	
		T _A = 25 °C	- 13.5 ^{a, b}	
		T _A = 70 °C	- 10.9 ^{a, b}	
Pulsed Drain Current	I _{DM}	- 70	A	
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C		
		T _A = 25 °C	- 2.4 ^{a, b}	
Avalanche Current	I _{AS}	20	mJ	
Single-Pulse Avalanche Energy	E _{AS}	20		
Maximum Power Dissipation	P _D	T _C = 25 °C	5.9	W
		T _C = 70 °C	3.8	
		T _A = 25 °C	2.9 ^{a, b}	
		T _A = 70 °C	1.9 ^{a, b}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, c}	R _{thJA}	33	42	°C/W
Maximum Junction-to-Foot	R _{thJF}	16	21	

Notes:

- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under steady state conditions is 85 °C/W.
- Based on T_C = 25 °C.

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SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 30		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J		5.3			
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 1.2	- 2.1	- 2.6	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 25 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ - 10 V, V _{GS} = - 10 V	- 30			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 10 A		0.0073	0.0088	Ω
		V _{GS} = - 4.5 V, I _D = - 7 A		0.0127	0.0153	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 10 V, I _D = - 10 A		32		S
Dynamic^b						
Input Capacitance	C _{iss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		3900		pF
Output Capacitance	C _{oss}		715			
Reverse Transfer Capacitance	C _{rss}		645			
Total Gate Charge	Q _g	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 10 A		90	135	nC
		V _{DS} = - 15 V, V _{GS} = - 4.5 V, I _D = - 10 A		44.8	68	
Gate-Source Charge	Q _{gs}		12.2			
Gate-Drain Charge	Q _{gd}		21.7			
Gate Resistance	R _g	f = 1 MHz	0.4	1.8	3.6	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 15 V, R _L = 1.5 Ω I _D ≅ - 10 A, V _{GEN} = - 10 V, R _g = 1 Ω		14	28	ns
Rise Time	t _r		13	25		
Turn-Off Delay Time	t _{d(off)}		49	90		
Fall Time	t _f		13	25		
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 15 V, R _L = 1.5 Ω I _D ≅ - 10 A, V _{GEN} = - 4.5 V, R _g = 1 Ω		70	120	
Rise Time	t _r		150	280		
Turn-Off Delay Time	t _{d(off)}		43	80		
Fall Time	t _f		28	55		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 4.9	A
Pulse Diode Forward Current	I _{SM}				- 70	
Body Diode Voltage	V _{SD}	I _S = - 3 A, V _{GS} = 0 V		- 0.72	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 10 A, dI/dt = 100 A/μs, T _J = 25 °C		41	70	ns
Body Diode Reverse Recovery Charge	Q _{rr}		41	70	nC	
Reverse Recovery Fall Time	t _a		18		ns	
Reverse Recovery Rise Time	t _b		23			

Notes:

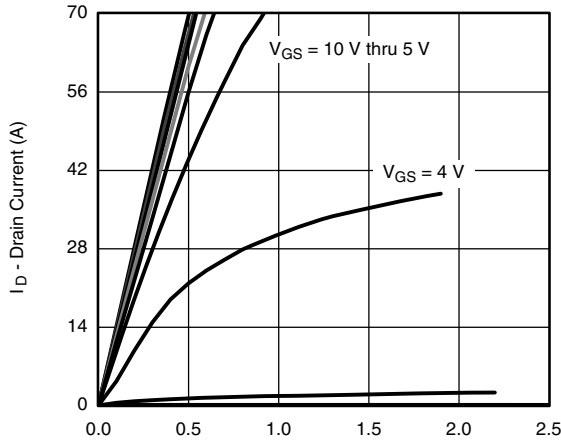
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

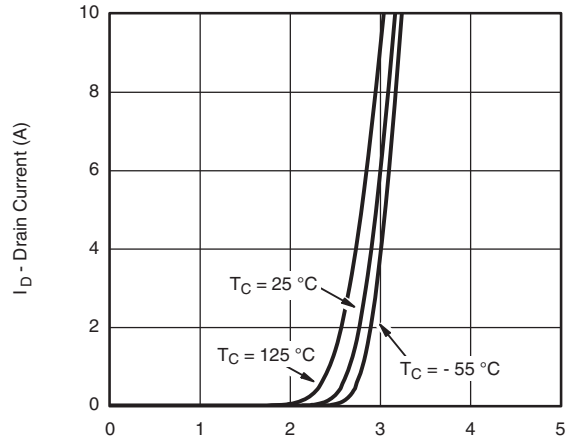


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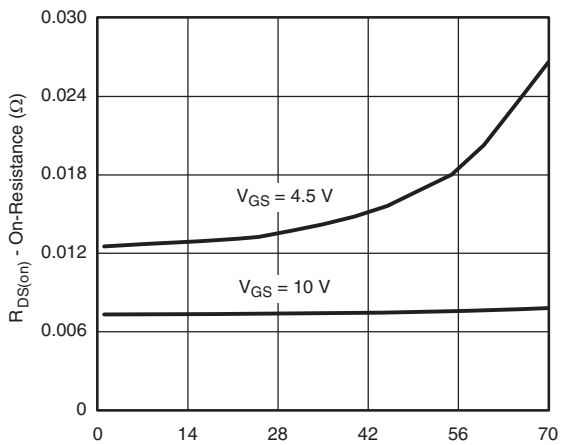
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



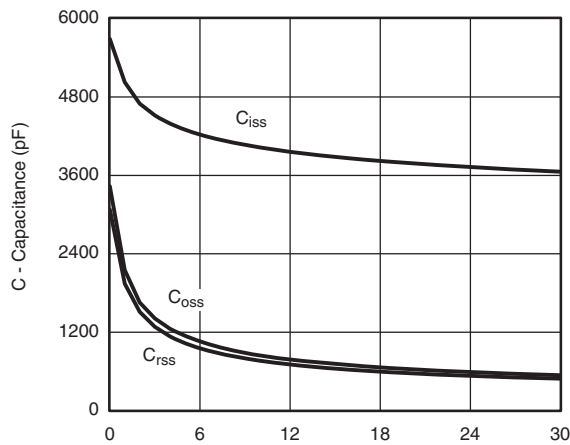
Output Characteristics



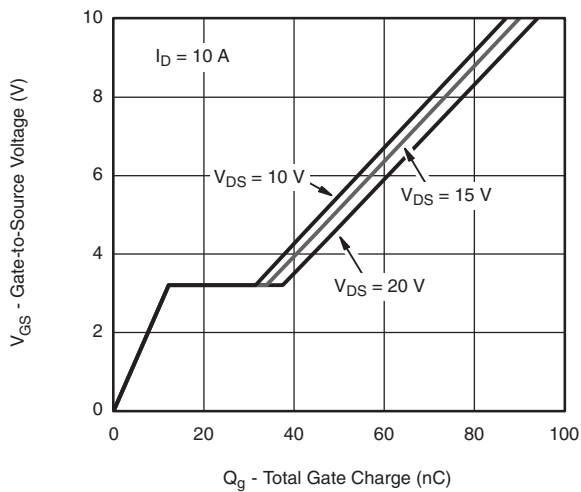
Transfer Characteristics



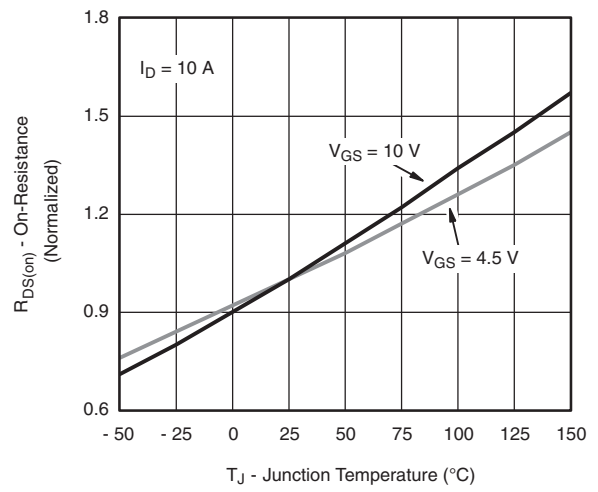
On-Resistance vs. Drain Current



Capacitance



Gate Charge

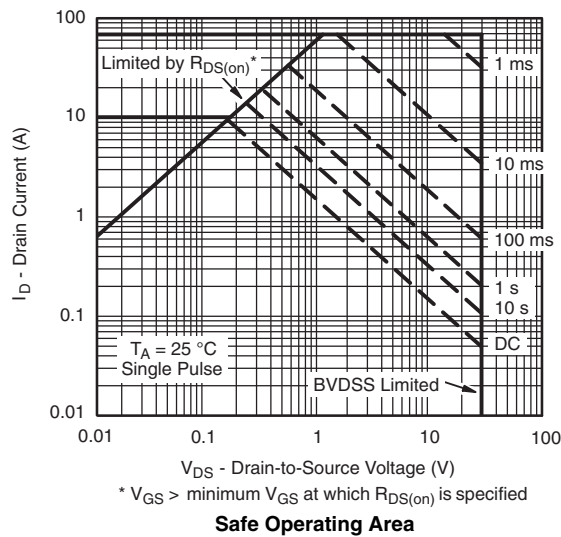
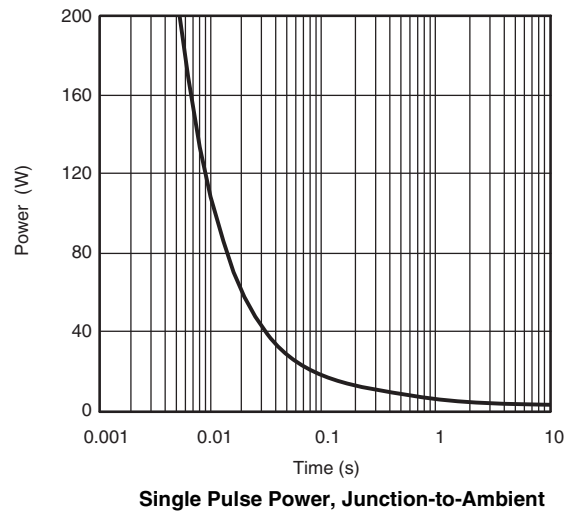
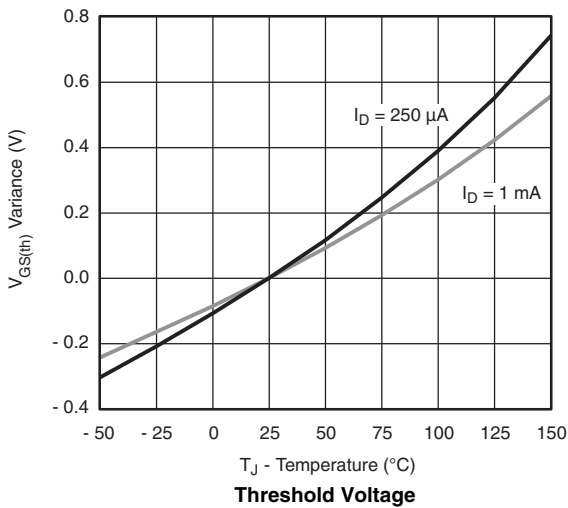
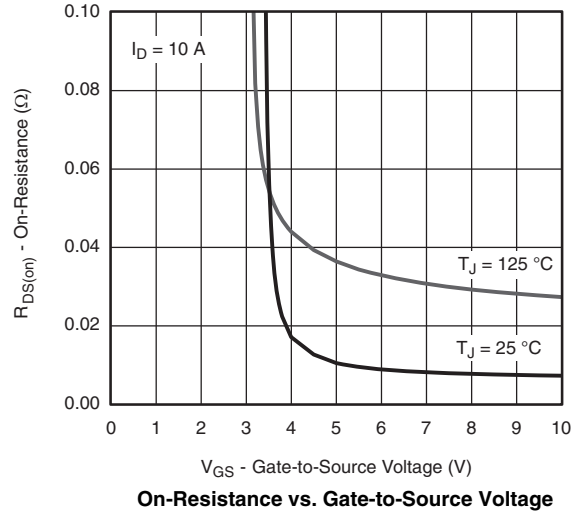
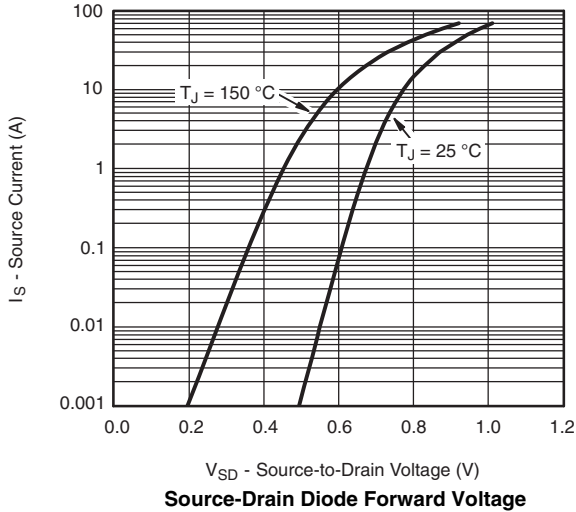


On-Resistance vs. Junction Temperature

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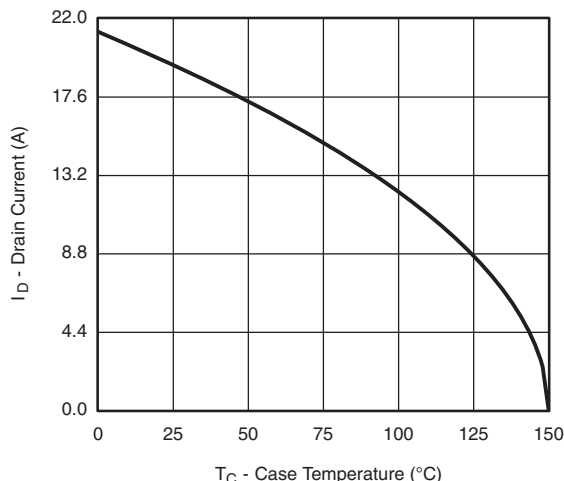
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





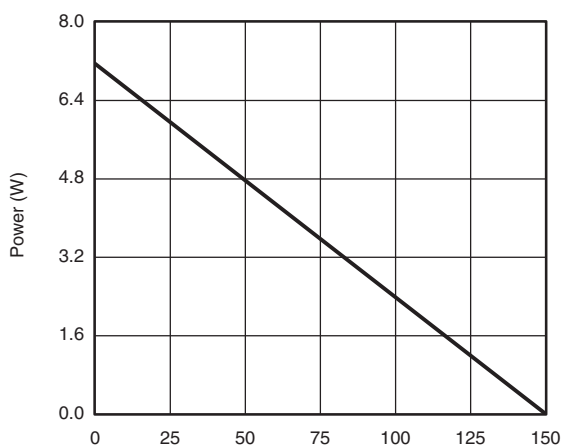
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



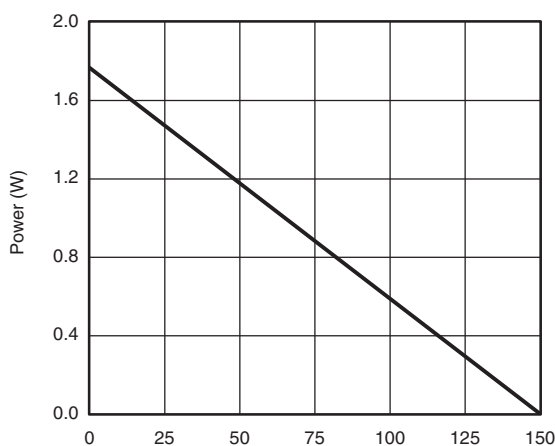
T_C - Case Temperature (°C)

Current Derating*



T_C - Case Temperature (°C)

Power, Junction-to-Foot



T_A - Ambient Temperature (°C)

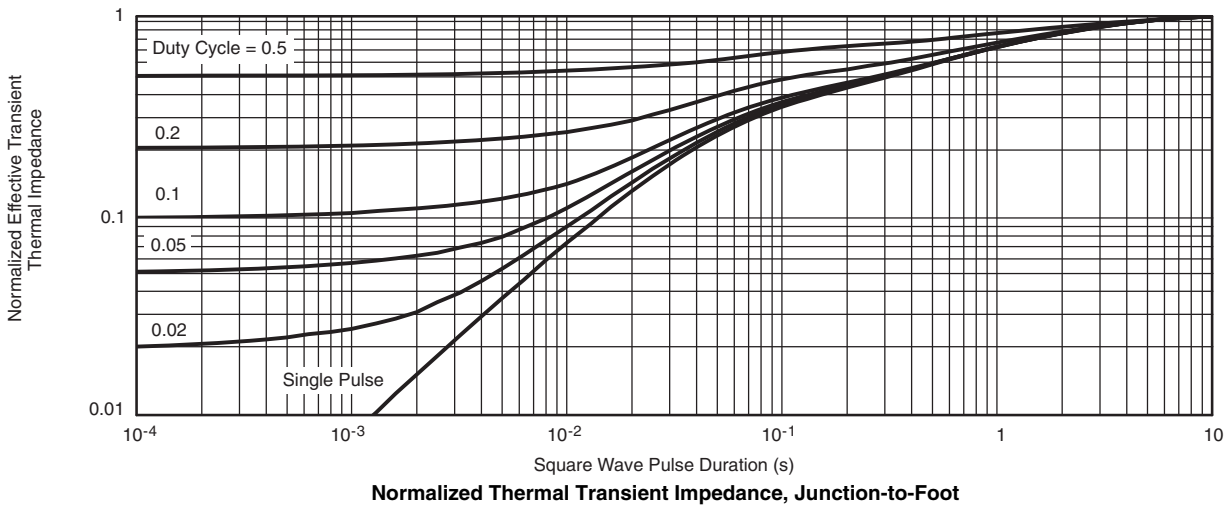
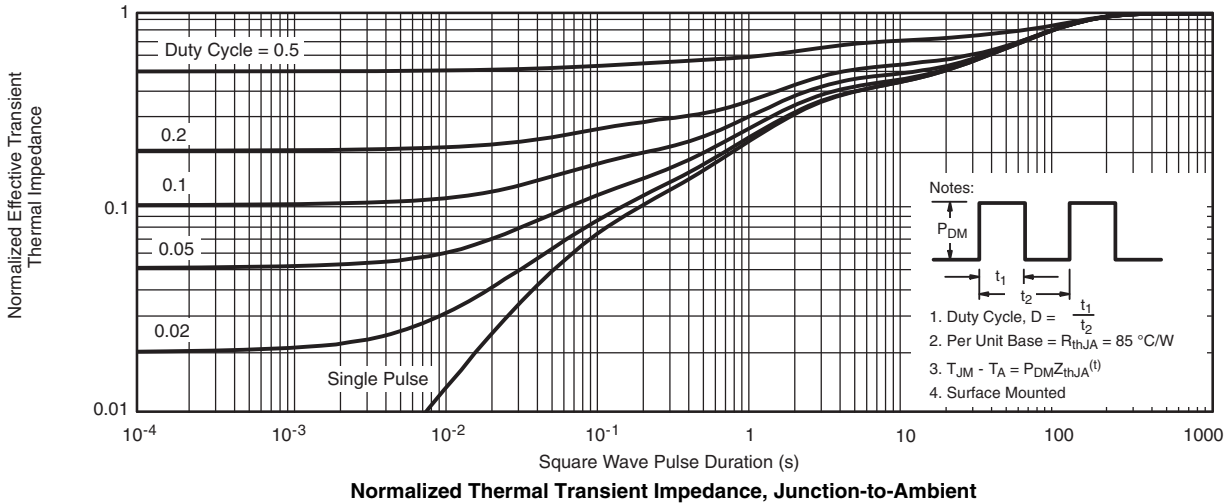
Power Derating, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?68982.

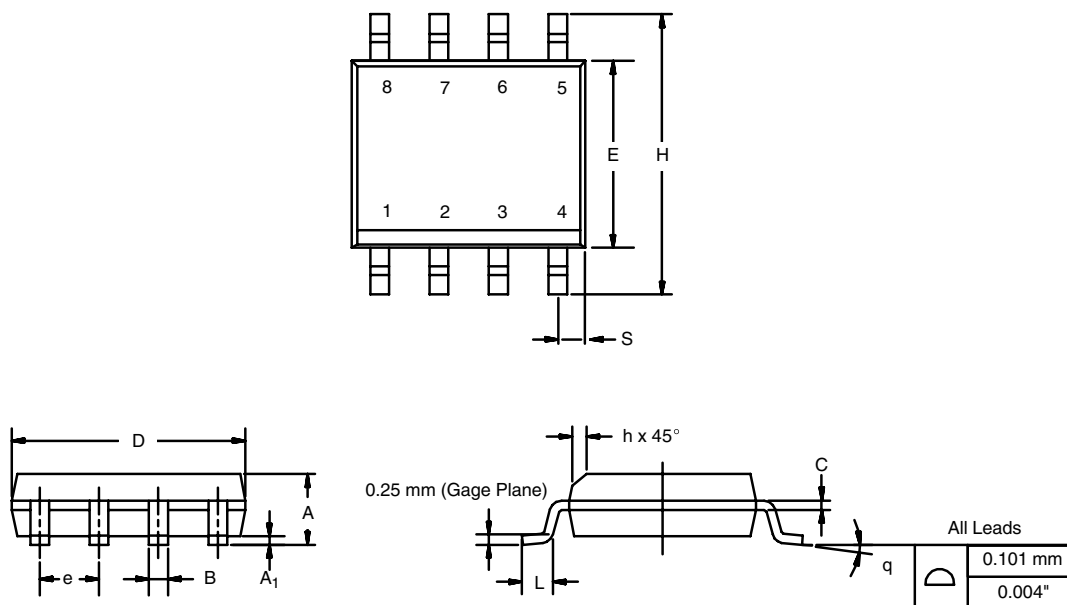


Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026

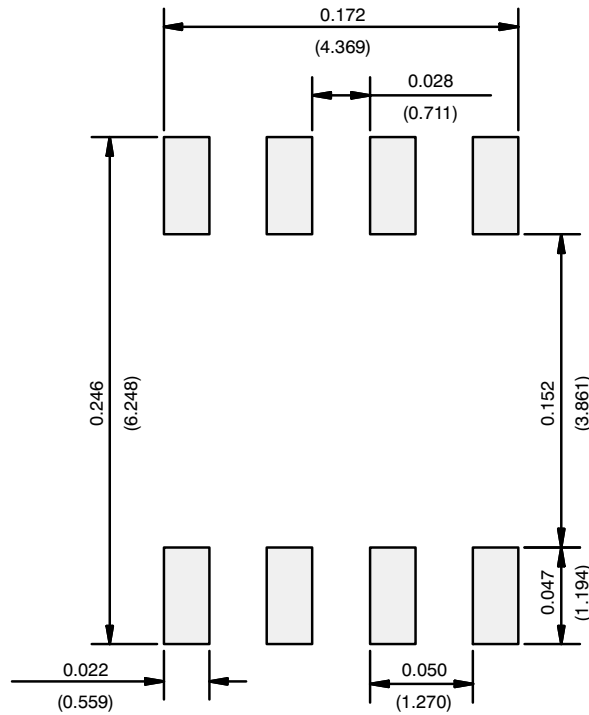
ECN: C-06527-Rev. I, 11-Sep-06
DWG: 5498

Application Note 826

Vishay Siliconix



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
 Dimensions in Inches/(mm)

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APPLICATION NOTE



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