

# **Excellent Integrated System Limited**

Stocking Distributor

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Vishay/Siliconix SI4412ADY-T1-E3

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**Distributor of Vishay/Siliconix: Excellent Integrated System Limited** Datasheet of SI4412ADY-T1-E3 - MOSFET N-CH 30V 5.8A 8-SOIC Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



### Si4412ADY

Vishay Siliconix

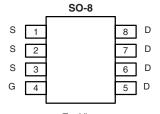
### N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)			
30	0.024 at V <sub>GS</sub> = 10 V	8			
	0.035 at V <sub>GS</sub> = 4.5 V	6.6			

#### **FEATURES**

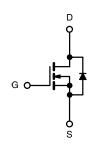
- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC







Ordering Information: Si4412ADY-T1-E3 (Lead (Pb)-free) Si4412ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unles Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	8	5.8	
	T <sub>A</sub> = 70 °C		6.4	4.6	
Pulsed Drain Current (10 µs Pulse Width)		I <sub>DM</sub>	30		A
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	2.3	1.2	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5	1.3	W
	T <sub>A</sub> = 70 °C		1.6	0.8	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R <sub>thJA</sub>	45	50	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		80	95	°C/W
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	16	20	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



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<b>SPECIFICATIONS</b> $T_J = 25 \text{ °C}$ , unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1.0			V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1			
		$V_{DS}$ = 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			5	- μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 10 V	30			А		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8 A	$V_{GS} = 10 \text{ V}, \text{ I}_D = 8 \text{ A}$ 0.1 $V_{GS} = 4.5 \text{ V}, \text{ I}_D = 6.6 \text{ A}$ 0.1		0.024	Ω		
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 6.6 \text{ A}$			0.035			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 8 A		21		S		
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.1	V		
Dynamic <sup>b</sup>								
Total Gate Charge	Qg			16	20			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$		3		nC		
Gate-Drain Charge	Q <sub>gd</sub>			1.5		1		
Gate Resistance	Rg		0.5		2.0	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>			15	20			
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		6	12			
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong\text{1}$ A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 6 $\Omega$		26	50	ns		
Fall Time	t <sub>f</sub>			10	20			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.3 A, dl/dt = 100 A/μs		40	80			

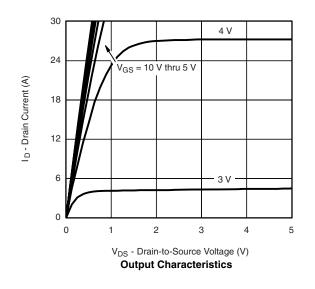
Notes:

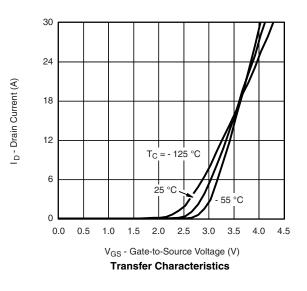
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





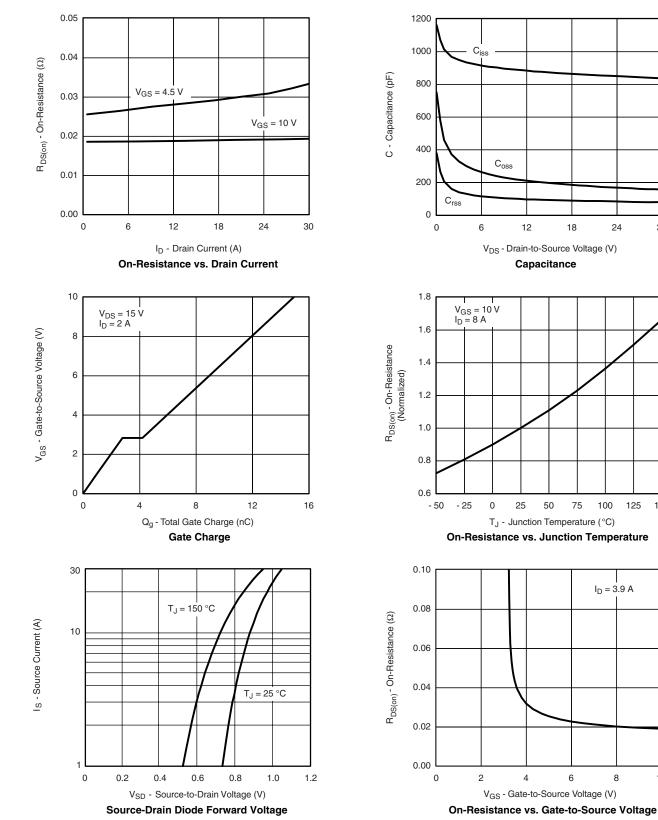


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30 18 24  $V_{\text{DS}}$  - Drain-to-Source Voltage (V) Capacitance 150 50 75 100 125 T<sub>J</sub> - Junction Temperature (°C) **On-Resistance vs. Junction Temperature**  $I_{D} = 3.9 \text{ A}$ 

8

6

10



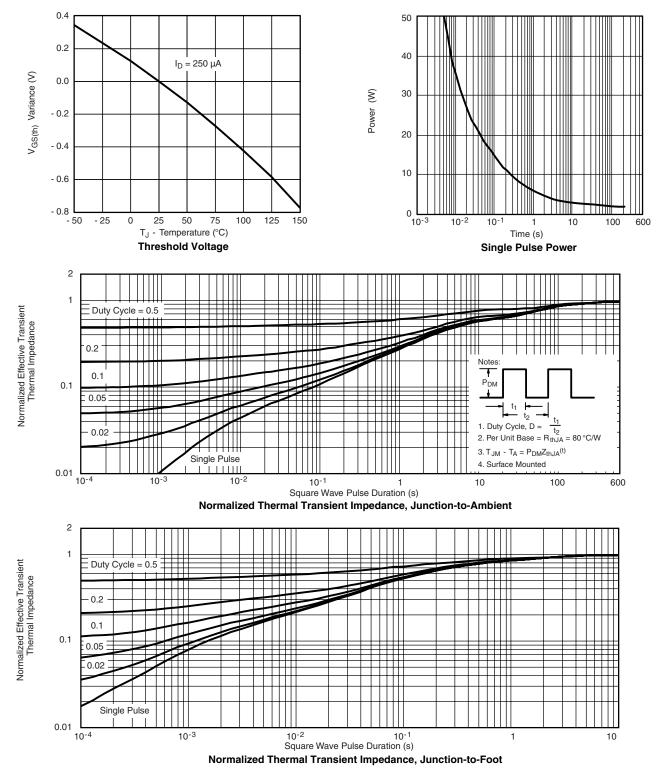
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