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[Vishay/Siliconix](#)
[SI4493DY-T1-E3](#)

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Si4493DY
 Vishay Siliconix

P-Channel 2.5-V (G-S) MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
- 20	0.00775 at $V_{GS} = - 4.5$ V	- 14
	0.01225 at $V_{GS} = - 2.5$ V	- 11

FEATURES

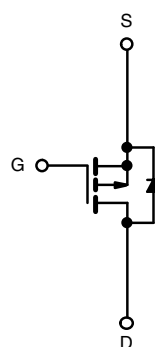
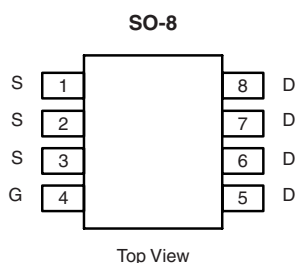
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



RoHS
 COMPLIANT
 HALOGEN
FREE
 Available

APPLICATIONS

- Load Switch



Ordering Information: Si4493DY-T1-E3 (Lead (Pb)-free)
 Si4493DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted					
Parameter	Symbol	10 s	Steady State	Unit	
Drain-Source Voltage	V_{DS}	- 20		V	
Gate-Source Voltage	V_{GS}	± 12			
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	$T_A = 25^\circ\text{C}$	- 14	- 10	A
		$T_A = 70^\circ\text{C}$	- 11	- 8	
Pulsed Drain Current	I_{DM}	- 50			
Continuous Source Current (Diode Conduction) ^a	I_S	- 2.7	- 1.36	W	
Maximum Power Dissipation ^a	P_D	$T_A = 25^\circ\text{C}$	3.0		1.5
		$T_A = 70^\circ\text{C}$	1.9	0.95	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ s	R_{thJA}	33	42	$^\circ\text{C/W}$
	Steady State		70	84	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	16	21	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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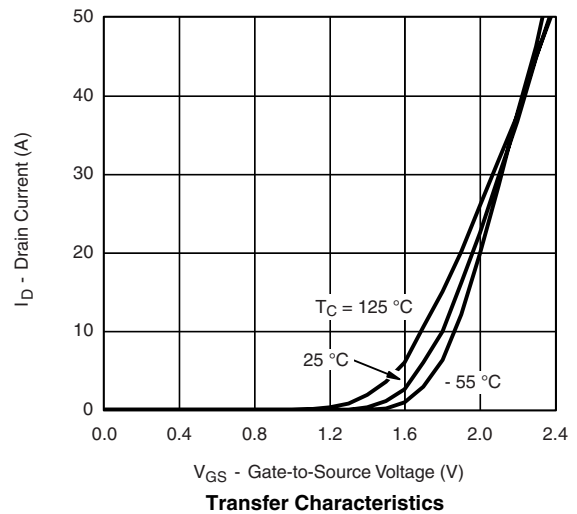
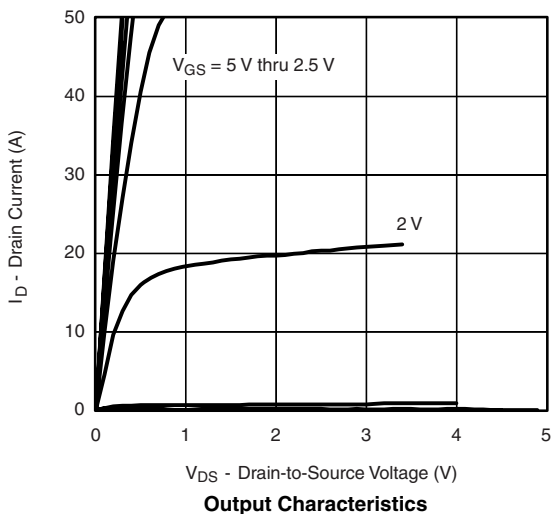
SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.6		-1.4	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 12\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}, T_J = 70\text{ }^\circ\text{C}$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -4.5\text{ V}$	-30			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -14\text{ A}$		0.0065	0.00775	Ω
		$V_{GS} = -2.5\text{ V}, I_D = -11\text{ A}$		0.010	0.01225	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -10\text{ V}, I_D = -14\text{ A}$		60		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -2.7\text{ A}, V_{GS} = 0\text{ V}$		-0.68	-1.1	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -10\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -14\text{ A}$		65	110	nC
Gate-Source Charge	Q_{gs}			14.5		
Gate-Drain Charge	Q_{gd}			21		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\text{ V}, R_L = 10\text{ }\Omega$ $I_D = -1\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 6\text{ }\Omega$		110	165	ns
Rise Time	t_r			150	225	
Turn-Off Delay Time	$t_{d(off)}$			220	330	
Fall Time	t_f			140	210	
Gate Resistance	R_g			3.8		
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = -2.7\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		85	130	ns

Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

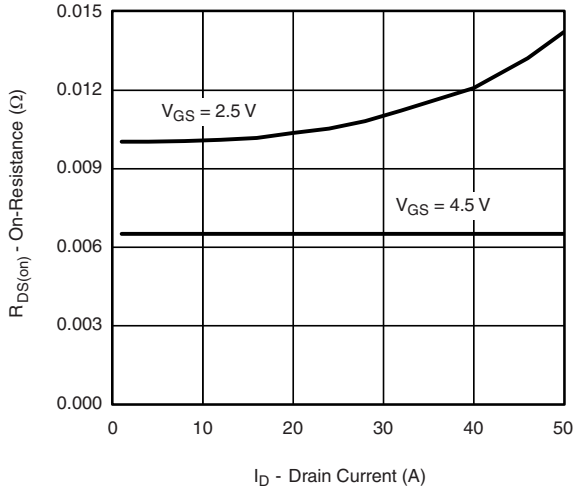
TYPICAL CHARACTERISTICS $25\text{ }^\circ\text{C}$, unless otherwise noted



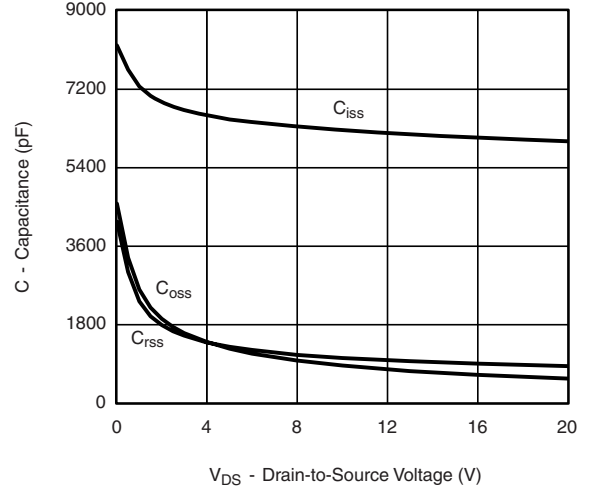


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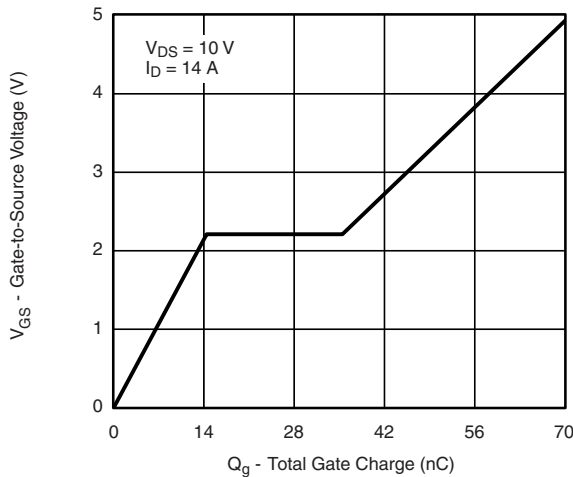
TYPICAL CHARACTERISTICS 25 °C unless noted



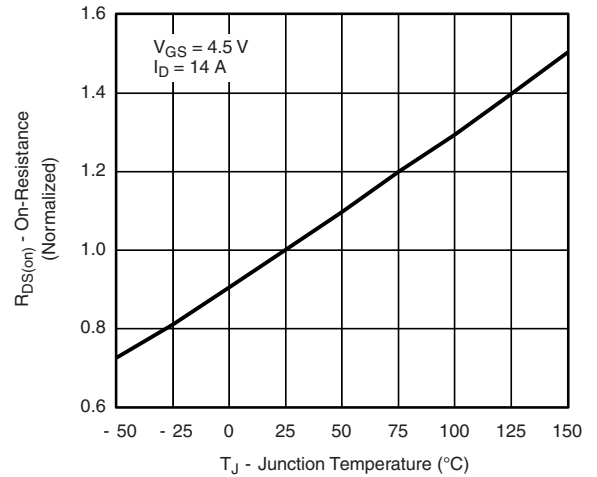
On-Resistance vs. Drain Current



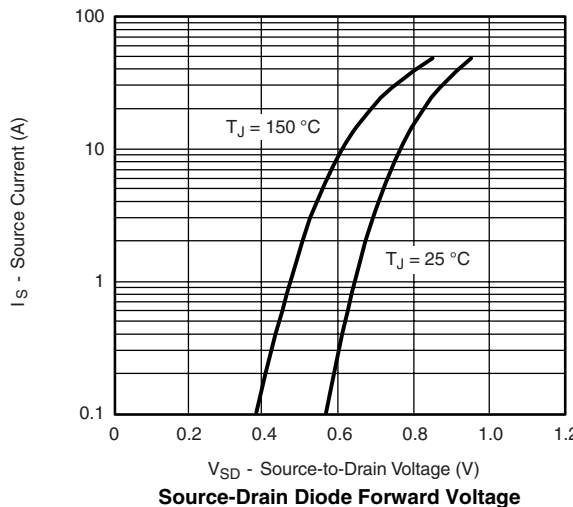
Capacitance



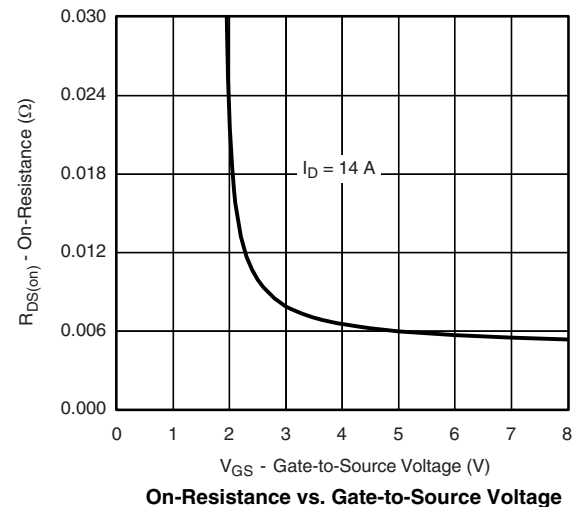
Gate Charge



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



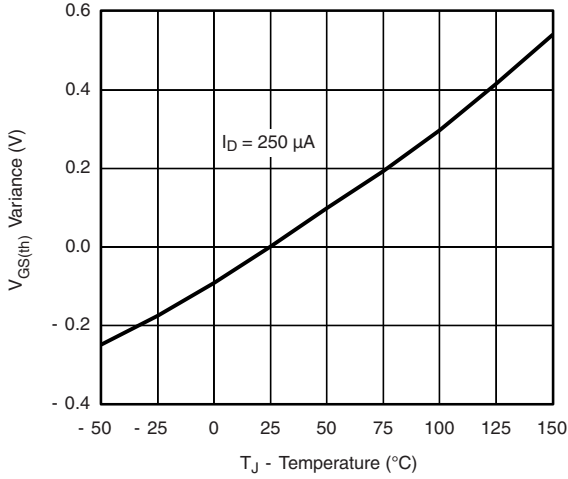
On-Resistance vs. Gate-to-Source Voltage

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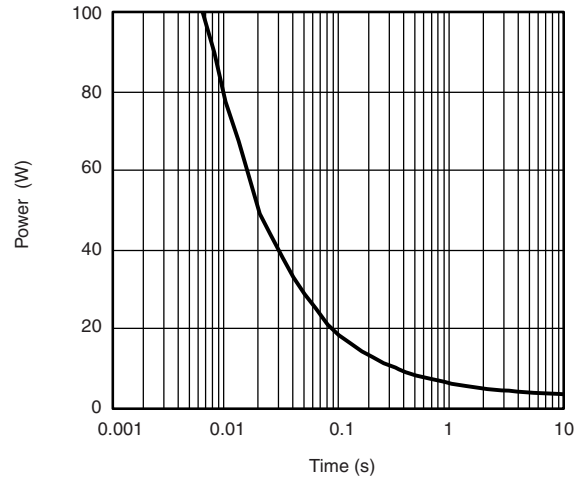
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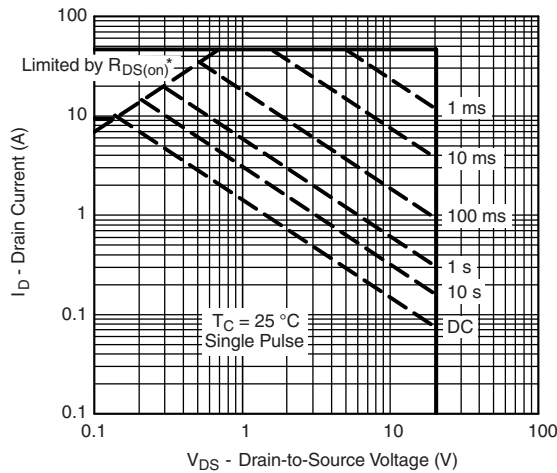
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Threshold Voltage

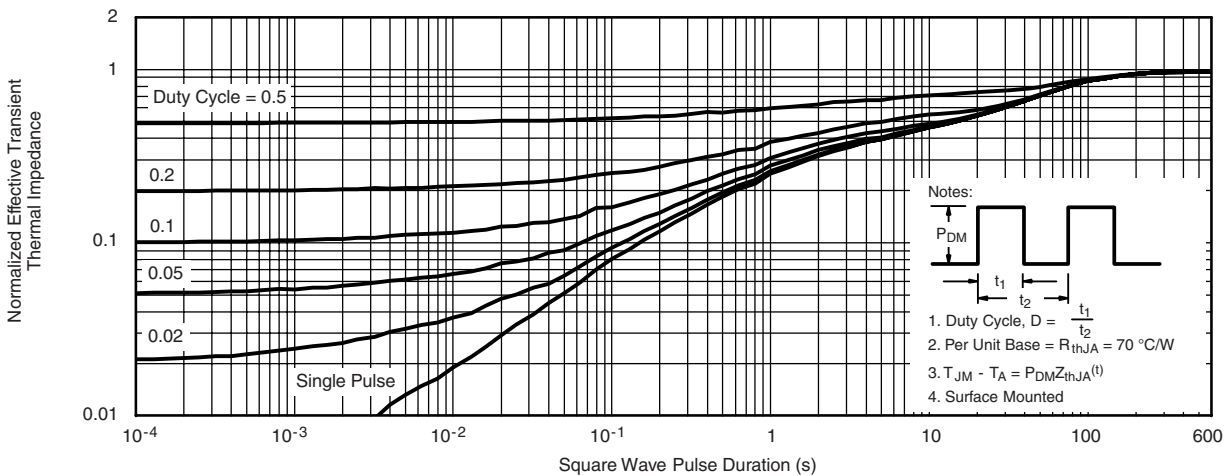


Single Pulse Power, Junction-to-Ambient



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Case



Notes:

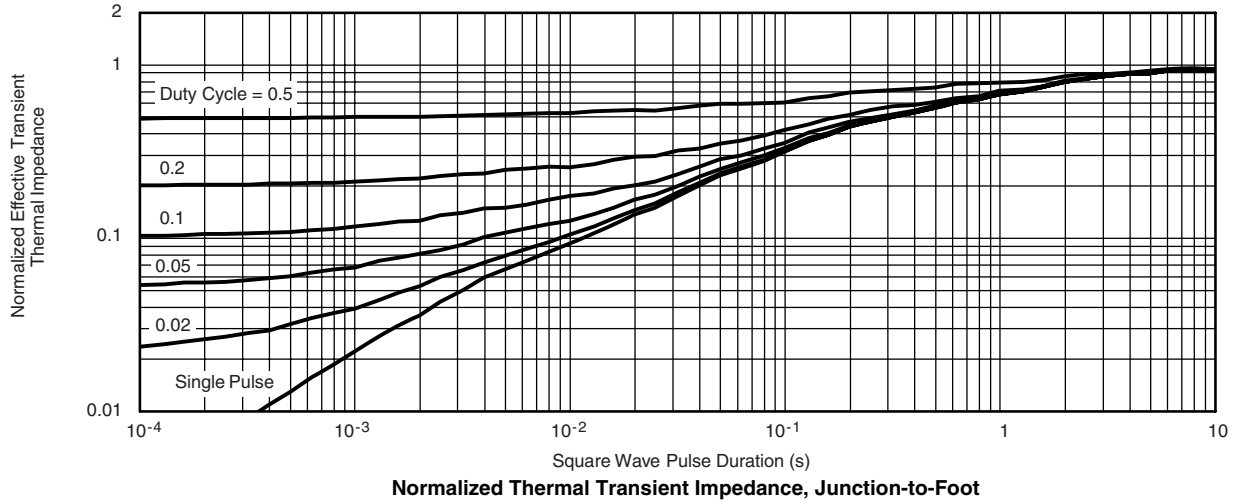
1. Duty Cycle, $D = \frac{t_1}{t_2}$
2. Per Unit Base = $R_{thJA} = 70 \text{ }^\circ\text{C/W}$
3. $T_{JM} - T_A = P_{DM}Z_{thJA}^{(t)}$
4. Surface Mounted

Normalized Thermal Transient Impedance, Junction-to-Ambient



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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