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Vishay/Siliconix
SIP4280ADT-1-T1-E3

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Product is End of Life 12/2014

Slew Rate Controlled Load Switch

FEATURES

- **1.5 V to 5.5 V Input Voltage range**
- **Very Low $R_{DS(ON)}$, typically 80 mΩ (5 V)**
- Slew rate limited turn-on time options
  - SiP4280A-1: 1 ms
  - SiP4280A-3: 100 µs
- Fast shutdown load discharge option
- Low quiescent current
  < 25 nA (typ)
- 4 kV ESD Rating
- 6 pin SOT23 package

APPLICATIONS

- Cellular telephones
- Digital still cameras
- Personal digital assistants (PDA)
- Hot swap supplies
- Notebook computers
- Personal communication devices

Pb-free

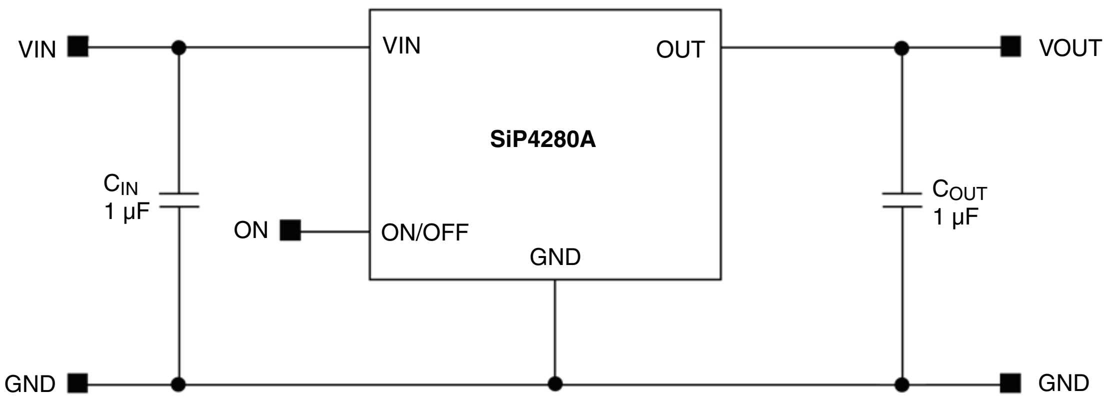
DESCRIPTION

The SiP4280A is a P-Channel MOSFET power switch designed for high-side load switching applications. The output pass transistor is a P-Channel MOSFET transistor with typically 80 mΩ $R_{DS(ON)}$. The SiP4280A is available in two different versions of turn-on times. The SiP4280A-1 version has a slew rate limited turn-on time typically of 1 ms. The SiP4280A-3 version has a slew rate limited turn-on time typically of 100 µs and additionally offers a shutdown load discharge circuit to rapidly turn off a load circuit when the switch is disabled.

Both SiP4280A load switch versions operate with an input voltage ranging from 1.5 V to 5.5 V, making them ideal for both 3 V and 5 V applications. The SiP4280A also features an under-voltage lock out which turns the switch off when an input undervoltage condition exists. Input logic levels are TTL and 2.5 V to 5.0 V CMOS compatible. The quiescent supply current is very low, typically 25 nA. In shutdown mode, the supply current decreases to less than 1.0 µA.

The SiP4280A is available in a 6 pin SOT23 package and is specified over - 40 °C to 85 °C temperature range.

TYPICAL APPLICATION CIRCUIT



ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Steady State	Unit
Supply Input Voltage		V_{IN}	- 0.3 to 6	V
Enable Input Voltage		V_{ON}	- 0.3 to 6	
Output Voltage		V_{OUT}	- 0.3 to V_{IN} + 0.3	
Maximum Switch Current		I_{MAX}	2.3	A
Maximum Pulsed Current	$V_{IN} \geq 2.5$	I_{DM}	6	
	$V_{IN} < 2.5$	I_{DM}	3	
Junction Temperature		T_J	- 40 to 150	°C
Thermal Resistance	SOT23-6L	Φ_{JA}[a]	180	°C/W
Power Dissipation	SOT23-6L[b]	P_D	440	mW

Notes:
a. Device mounted with all leads soldered or welded to PC board.
b. Derate 5.5 mW/°C above T_A = 70 °C.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating/conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING RANGE all voltages referenced to GND = 0 V

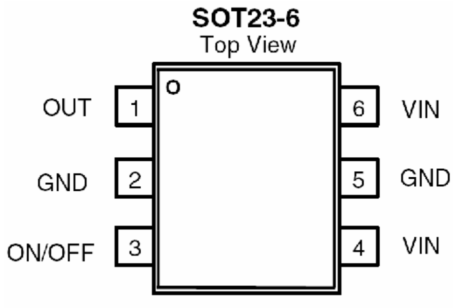
Parameter	Symbol	Steady State	Unit
	V_{IN}	1.5 to 5.5	V
Operating Temperature Range		- 40 to 85	°C

SPECIFICATIONS

Parameter	Symbol	Test Conditions Unless Specified V_{IN} = 5 V, T_A = - 40 to 85 °C	Limits Min[a]	Typ[b]	Max[a]	Unit
SiP4280A All Versions						
Operating Voltage	V_{IN}		1.5	-	5.5	V
Quiescent Current	I_Q	ON/OFF = active	-		1	µA
Off Supply Current	$I_{Q(OFF)}$	ON/OFF = inactive, OUT = open	-	0.01	1	
Off Switch Current	$I_{SD(OFF)}$	ON/OFF = inactive, V_{OUT} = 0	-	0.01	1	
On-Resistance	$R_{DS(ON)}$	V_{IN} = 5 V, T_A = 25 °C	-	80	120	mΩ
		V_{IN} = 4.2 V, T_A = 25 °C	-	85	130	
		V_{IN} = 3 V, T_A = 25 °C	-	100	150	
		V_{IN} = 1.8 V, T_A = 25 °C	-	160	250	
On-Resistance Temp-Coefficient	TC_{RDS}		-	2800	-	ppm/°C
ON/OFF Input Low Voltage[c]	V_{IL}	V_{IN} = 1.8 V to 5.5 V	0.4	-	-	V
ON/OFF Input High Voltage	V_{IH}	V_{IN} = 1.5 V to 2.7 V	-	-	1.4	
		V_{IN} = 2.7 V to < 4.2 V	-	-	2	
		$V_{IN} \geq$ 4.2 V to 5.5 V	-	-	2.4	
ON/OFF Input Leakage	I_{SINK}	$V_{ON/OFF}$ = 5.5 V	-	-	1	µA
SiP4280A-1 Version						
Output Turn-On Delay Time	$T_{D(ON)}$	V_{IN} = 5 V, R_{LOAD} = 10 Ω, T_A = 25 °C	-	20	40	µs
Output Turn-On Rise Time	T_{ON}	V_{IN} = 5 V, R_{LOAD} = 10 Ω, T_A = 25 °C	-	1000	1500	
Output Turn-Off Delay Time	$T_{D(OFF)}$	V_{IN} = 5 V, R_{LOAD} = 10 Ω, T_A = 25 °C	-	4	10	
SiP4280A-3 Version						
Output Turn-On Delay Time	$T_{D(ON)}$	V_{IN} = 5 V, R_{LOAD} = 10 Ω, T_A = 25 °C	-	20	40	µs
Output Turn-On Rise Time	T_{ON}	V_{IN} = 5 V, R_{LOAD} = 10 Ω, T_A = 25 °C	-	100	150	
Output Turn-Off Delay Time	$T_{D(OFF)}$	V_{IN} = 5 V, R_{LOAD} = 10 Ω, T_A = 25 °C	-	4	10	
Output Pull-Down Resistance	R_{PD}	ON/OFF = inactive, T_A = 25 °C	-	150	250	Ω

Notes:
a. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum.
b. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
c. For $V_{IN} \leq$ 1.5 V see typical ON/OFF threshold curve.

PIN CONFIGURATION

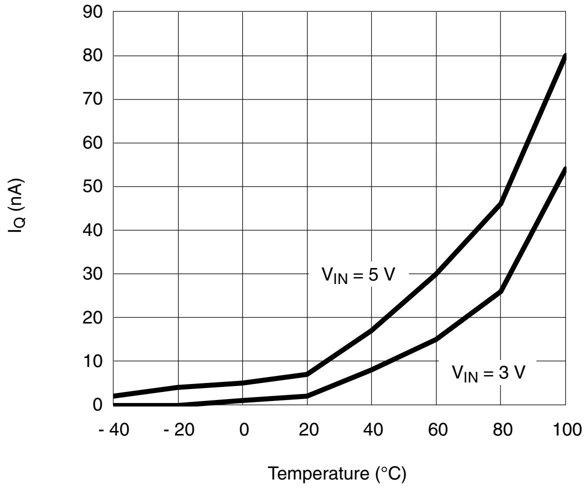


PIN DESCRIPTION		
Pin Number SOT23-6	Pin Name	Description
4, 6	V_{IN}	This pin is the P-Channel MOSFET source connection
3	ON/OFF	Logic high enables the IC; logic low disables the IC
2, 5	GND	Ground connection
1	OUT	This pin is the P-Channel MOSFET drain connection

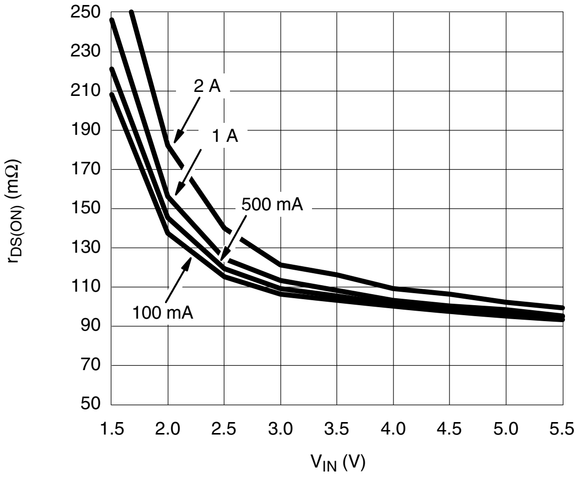
SELECTION GUIDE			
Part Number	Slew Rate (typ)	Active Pull Down	Enable
SiP4280A-1-T1-E3	1 ms	No	Active High
SiP4280A-3-T1-E3	100 µs	Yes	Active High

ORDERING INFORMATION			
Part Number	Marking	Temperature Range	Package
SiP4280ADT-1-T1-E3	L4XX	- 40 °C to 85 °C	SOT23-6L
SiP4280ADT-3-T1-E3	L6XX		SOT23-6L

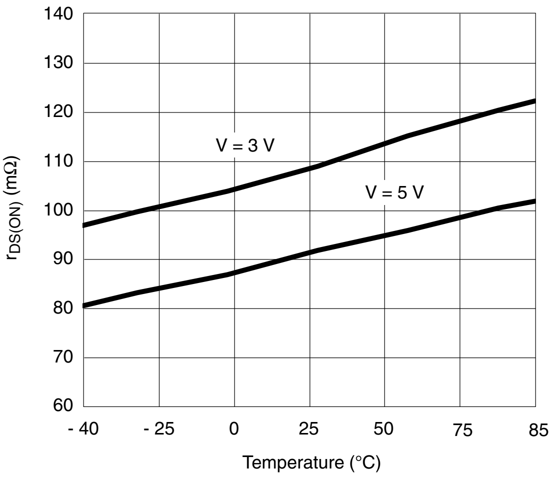
TYPICAL CHARACTERISTICS internally regulated, 25 °C unless noted



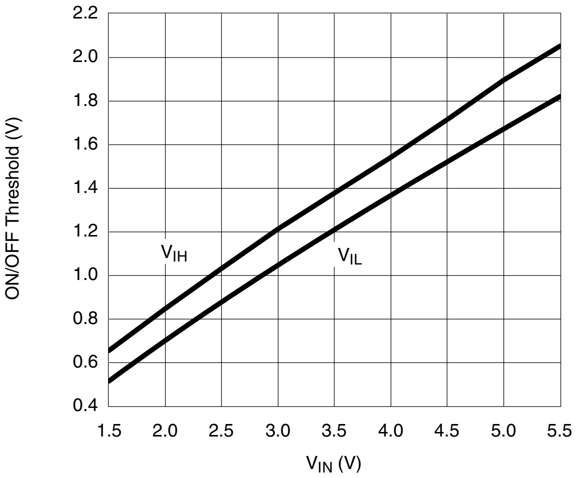
Quiescent Current vs. Temperature



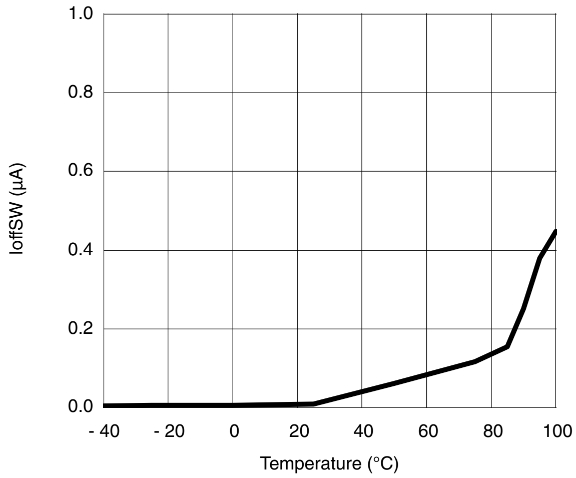
$R_{DS(ON)}$ vs. Input Voltage



$R_{DS(ON)}$ vs. Temperature

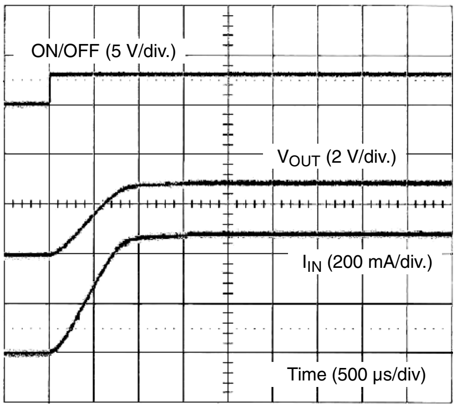


ON/OFF Threshold vs. Input Voltage

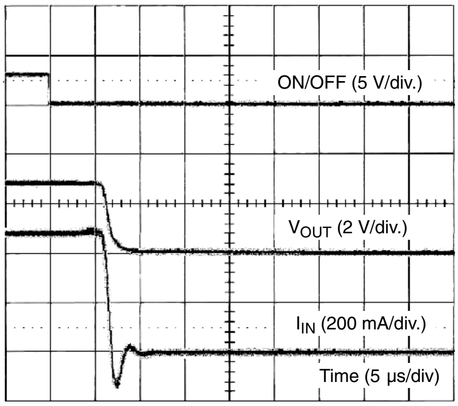


Off Switch Current vs. Temperature

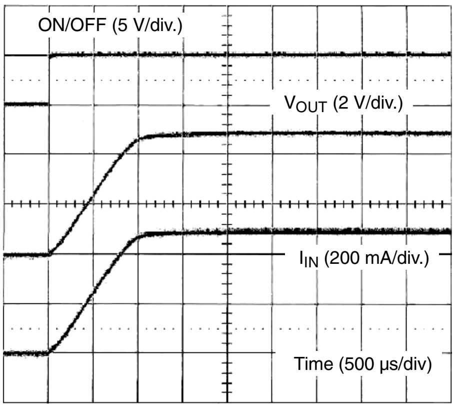
TYPICAL WAVEFORMS



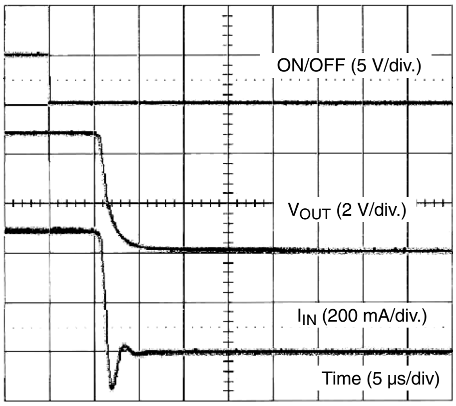
SiP4280A-1 Turn-On (V_{IN} = 3 V, R_{LOAD} = 6 Ω)



SiP4280A-1 Turn-Off (V_{IN} = 3 V, R_{LOAD} = 6 Ω)

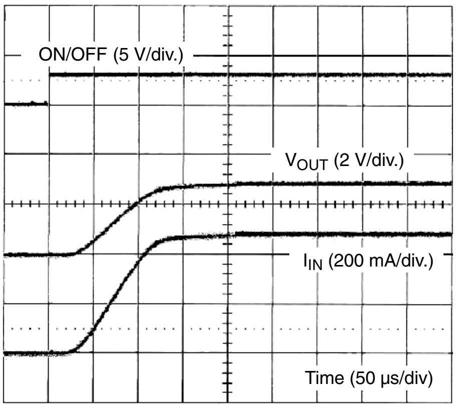


SiP4280A-1 Turn-On (V_{IN} = 5 V, R_{LOAD} = 10 Ω)

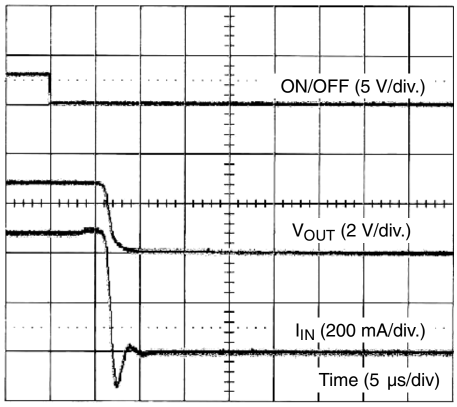


SiP4280A-1 Turn-Off (V_{IN} = 5 V, R_{LOAD} = 10 Ω)

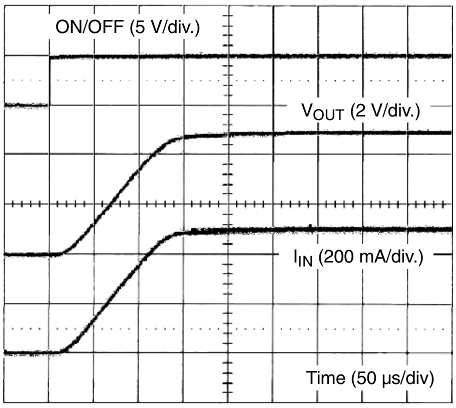
TYPICAL WAVEFORMS



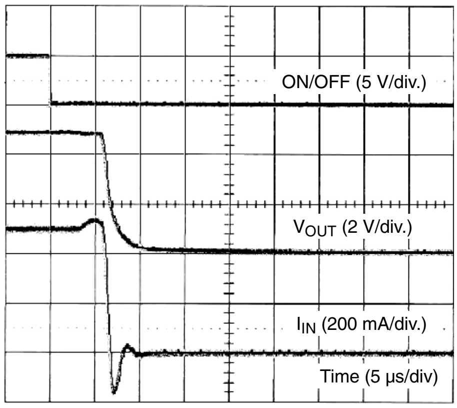
SiP4280A-3 Turn-On (V_{IN} = 3 V, R_{LOAD} = 6 Ω)



SiP4280A-3 Turn-Off (V_{IN} = 3 V, R_{LOAD} = 6 Ω)

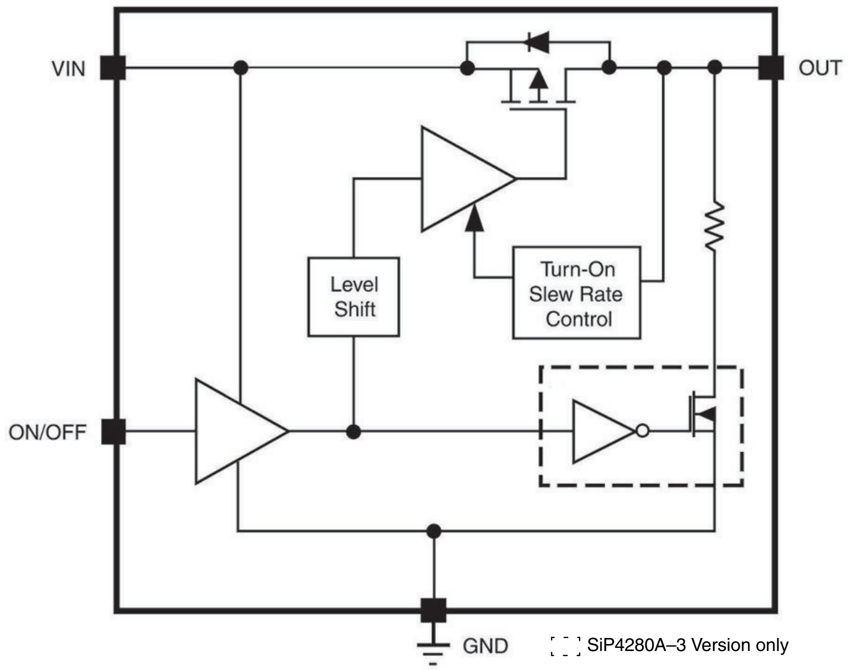


SiP4280A-3 Turn-On (V_{IN} = 5 V, R_{LOAD} = 10 Ω)



SiP4280A-3 Turn-Off (V_{IN} = 5 V, R_{LOAD} = 10 Ω)

BLOCK DIAGRAM



SiP4280A Functional Block Diagramm

DETAILED DESCRIPTION

The SiP4280A is a P-Channel MOSFET power switches designed for high-side slew rate controlled load switching applications. Once turned on, the slew-rate control circuitry is activated and current is ramped in a linear fashion until it reaches the level required for the output load condition. This is accomplished by first elevating the gate voltage of the MOSFET up to its threshold voltage and then by linearly increasing the gate voltage until the MOSFET becomes fully enhanced. At this point, the gate voltage is then quickly increased to the full input voltage to reduce $R_{DS(ON)}$ of the MOSFET switch and minimize any associated power losses.

The SiP4280A-1 version has a modest 1 ms turn on slew rate feature, which significantly reduces in-rush current at turned on time and permits the load switch to be implemented with a small input capacitor, or no input capacitor at all, saving cost and space. In addition to a 100 µs minimized slew rate, the SIP4280A-3 features a shutdown output discharge circuit which is activated at shutdown (when the part is disabled through the ON/OFF pin) and discharges the output pin through a small internal resistor hence, turning off the load.

In instances where the input voltage falls below 1.4 V (typically) the under voltage lock-out circuitry protects the MOSFET switch from entering the saturation region or operation by shutting down the chip.

APPLICATION INFORMATION

Input Capacitor

While a bypass capacitor on the input is not required, a 1 µF or larger capacitor for C_{IN} is recommended in almost all applications. The Bypass capacitor should be placed as physically close as possible to the SiP4280A to be effective in minimizing transients on the input. Ceramic capacitors are recommended over tantalum because of their ability to withstand input current surges from low impedance sources such as batteries in portable devices.

Output Capacitor

A 0.1 µF capacitor or larger across V_{OUT} and GND is recommended to insure proper slew operation. C_{OUT} may be increased without limit to accommodate any load transient condition with only minimal affect on the SiP4280A turn on slew rate time. There are no ESR or capacitor type requirement.

Enable

The ON/OFF pin is compatible with both TTL and CMOS logic voltage levels.

Reverse Voltage Conditions and Protection

The P-Channel MOSFET pass transistor has an intrinsic diode that is reversed biased when the input voltage is greater than the output voltage. Should V_{OUT} exceed V_{IN}, this intrinsic diode will become forward biased and allow excessive current to flow into the IC thru the V_{OUT} pin and potentially damage the IC device. Therefore extreme care should be taken to prevent V_{OUT} from exceeding V_{IN}.

In conditions where V_{OUT} exceeds V_{IN} a Schottky diode in parallel with the internal intrinsic diode is recommended to protect the SiP4280A.

Thermal Considerations

The SiP4280A is designed to maintain a constant output load current. The internal switch is designed to operate at 2.3 A of current, as stated in the ABS MAX table. However, The real limiting factor for the safe operating load current is the thermal power dissipation of the package. To obtain the highest power dissipation the power pad of the device should be connected to a heat sink on the printed circuit board.

The maximum power dissipation in any application is dependant on the maximum junction temperature, $T_{J(MAX)}$ = 125 °C, the junction-to-ambient thermal resistance θ_{J-A} = 180 °C for SOT23-6, and the ambient temperature, T_A, which may be formulaically expressed as:

$$P\,(max) = \frac{T_J\,(max) - T_A}{\theta_{J-A}} = \frac{125 - T_A}{140}$$

It then follows that, assuming an ambient temperature of 70 °C, the maximum power dissipation will be limited to about 305 mW for SOT23-6.

In any application, the maximum continuous switch current is a function two things: the package power dissipation and the $R_{DS(ON)}$ at the ambient temperature.

As an example let us calculate the worst-case maximum load current at T_A= 70 °C. The worst case $R_{DS(ON)}$ at 25 °C occurs at an input voltage of 1.8 V and is equal to 250 mΩ. The $R_{DS(ON)}$ at 70 °C can be extrapolated from this data using the following formula

$$R_{DS(ON)}\text{ (at 70 °C)} = R_{DS(ON)}\text{ (at 25 °C)} \times (1 + T_C \times \Delta T)$$

Where T_C is 2090 ppm/°C. Continuing with the calculation we have

$$R_{DS(ON)}\text{ (at 70 °C)} = 250\text{ m}\Omega \times (1 + 0.00209 \times (70\text{ °C} - 25\text{ °C})) = 278\text{ m}\Omega$$

The maximum current limit is then determined by

$$I_{LOAD}\,(max) < \sqrt{\frac{P\,(max)}{R_{DS(ON)}}}$$

which in case is 1.05 A for SOT23-6. Under the stated input voltage condition, if the calculated current limit is exceeded the internal die temperature will rise and eventually, possibly damage the device.

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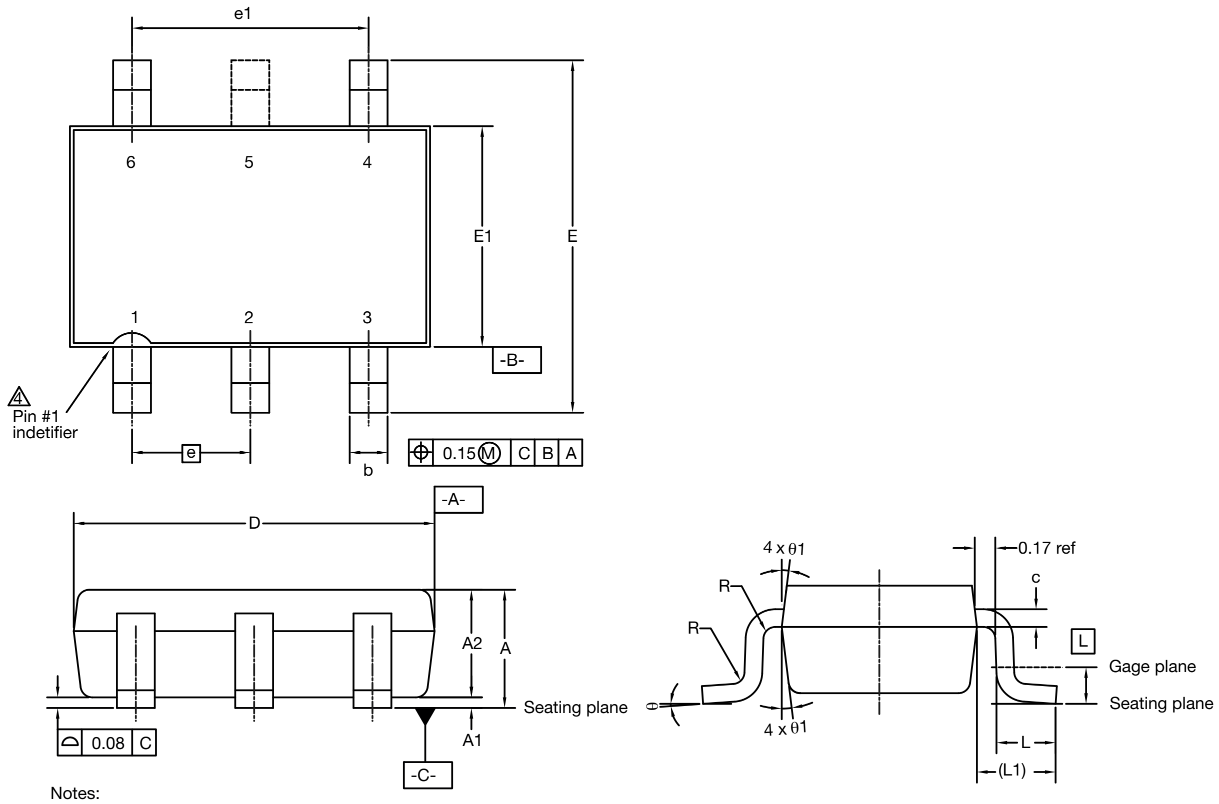
Thin SOT-23 : 5- and 6-Lead (Power IC only)



Notes:
1. Use millimeters as the primary measurement.
2. Dimensioning and tolerances conform to ASME Y14.5M. - 1994.
3. This part is fully compliant with JEDEC MO-193.
4. Detail of Pin #1 indentifier is optional.

DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.91	1.00	1.10	0.036	0.039	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.85	0.90	1.00	0.033	0.035	0.039
b	0.30	0.40	0.45	0.012	0.016	0.018
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.85	2.95	3.10	0.112	0.116	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E1	1.525	1.65	1.70	0.060	0.065	0.067
e	0.95 BSC			0.0374 BSC		
L	0.30	0.40	0.50	0.014	-	0.020
L1	0.60 ref.			0.024 BSC		
L2	0.25 BSC			0.010 BSC		
θ	0°	4°	8°	0°	4°	8°
θ1	4°	10°	12°	4°	10°	12°

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DWG: 5926

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