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Vishay/Siliconix SUD35N05-26L-E3

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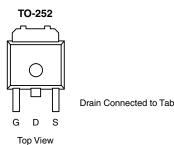


SUD35N05-26L

Vishay Siliconix

N-Channel 55 V (D-S) 175 °C MOSFET

PRODUC	PRODUCT SUMMARY			
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^a		
55	0.0200 at V _{GS} = 10 V	35		
55	0.0260 at V _{GS} = 4.5 V	30		

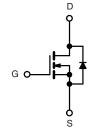


- TrenchFET[®] Power MOSFETS
- 175 °C Rated Maximum Junction Temperature
- Low Input Capacitance



RoHS

Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>



N-Channel MOSFET

Ordering Information: SUD35N05-26L-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS ($T_A = 2$	25 °C, unless othe	rwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	55	N/	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current (T 175 °C)b	T _C = 25 °C	- I _D	35		
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 100 °C		25	۸	
Pulsed Drain Current		I _{DM}	80	A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	35		
Maximum Dawar Dissinction	T _C = 25 °C	Р	50 ^c	W	
Maximum Power Dissipation	T _A = 25 °C	P _D	7.5 ^b		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Junction-to-Ambient ^b	t ≤ 10 s	- R _{thJA}	17	20	
	Steady State		50	60	
Junction-to-Case		R _{thJC}	2.5	3	°C/W
Junction-to-Lead		R _{thJL}	5	6	

Notes:

a. Package limited.

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b. Surface mounted on 1" x1" FR4 board, t \leq 10 s.

c. See SOA curve for voltage derating.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

For more information please contact: pmostechsupport@vishay.com

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Parameter	Symbol	Test Conditions	Min.	Тур ^а	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{BR}	$V_{GS} = 0 V, I_D = 250 \mu A$	55			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1			
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zana Oata Malta na Durin Origin I		$V_{DS} = 44 V, V_{GS} = 0 V$			1	μA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 44 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 5 V$	35			Α
Drain-Source On-State Resistance ^b		V _{GS} = 10 V, I _D = 20 A		0.0165	0.0200	
	R _{DS(on)}	V _{GS} = 10 V, I _D = 10 A, T _J = 125 °C			0.0350	
		V _{GS} = 4.5 V, I _D = 15 A		0.0215	0.0260	
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		25		S
Dynamic ^a						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		885		pF
Output Capacitance	C _{oss}			185		
Reverse Transfer Capacitance	C _{rss}			80		
Total Gate Charge ^c	Qg			10.5	13	nC
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 25 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 35 \text{ A}$		4		
Gate-Drain Charge ^c	Q _{gd}			4.8		
Turn-On Delay Time ^c	t _{d(on)}			5	8	
Rise Time ^c	t _r	$V_{DD} = 25 \text{ V}, \text{ R}_{\text{L}} = 0.3 \Omega$ $\text{I}_{\text{D}} \cong 35 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{G}} = 2.5 \Omega$		18	30	
Turn-Off Delay Time ^c	t _{d(off)}			20	30	ns
Fall Time ^c	t _f			100	150	
Source-Drain Diode Ratings and Cha	racteristic (⊺	_C = 25 °C)	•	•	·	
Continuous Current	۱ _S				35	٨
Pulsed Current	I _{SM}				80	A
Diode Forward Voltage ^b	V _{SD}	I _F = 80 A, V _{GS} = 0 V			1.5	V
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 35 A, di/dt = 100 A/μs		25	40	ns

Notes:

a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





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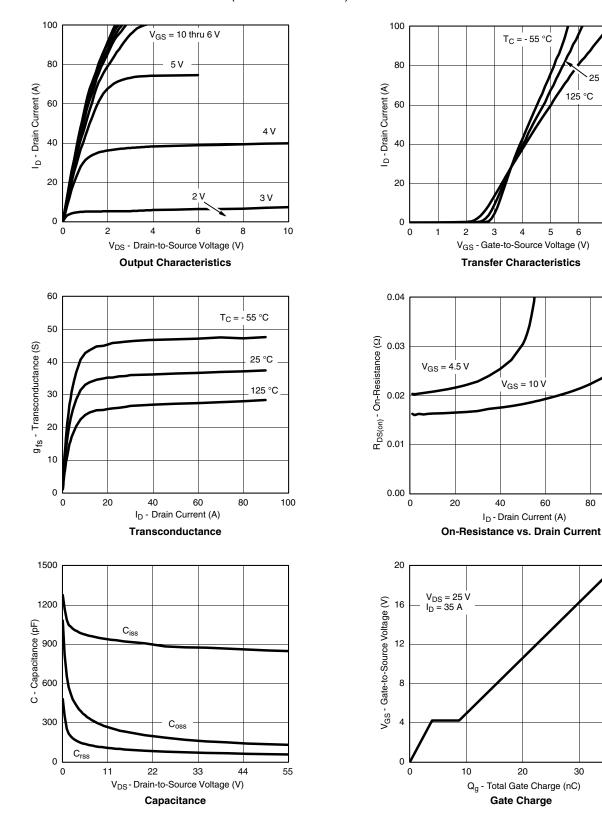
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°C

7

8

100



TYPICAL CHARACTERISTICS (25 °C unless noted)

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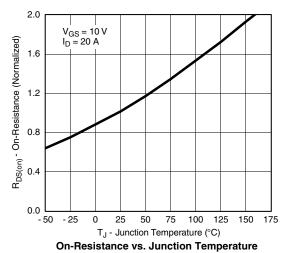


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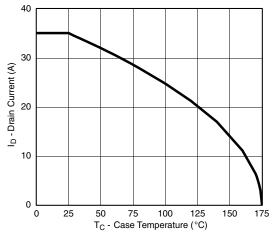
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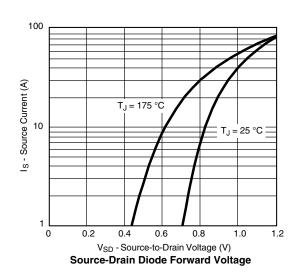
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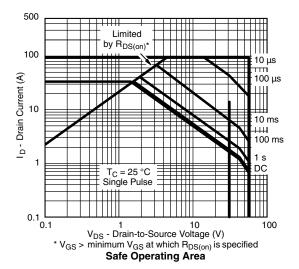


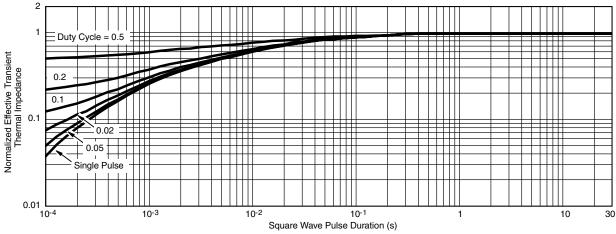
THERMAL RATINGS



Max. Avalanche and Drain Current vs. Case Temperature







Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71443.

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