

# **Excellent Integrated System Limited**

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<u>Diodes Incorporated</u> 74AHCT1G08W5-7

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### **74AHCT1G08**

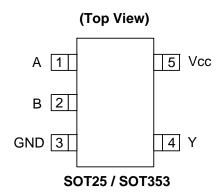
#### SINGLE 2 INPUT POSITIVE AND GATE

#### **Description**

The 74AHCT1G08 is a single 2-input positive AND gate with a standard totem pole output. The device is designed for operation with a power supply range of 4.5V to 5.5V. The gate performs the positive Boolean function:

$$Y = A \bullet B$$
 or  $Y = \overline{A + B}$ 

#### **Pin Assignments**



#### **Features**

- Supply Voltage Range from 4.5V to 5.5V
- ± 8 mA Output Drive at 5.0V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time.
- ESD Protection per JESD 22
  - o Exceeds 200-V Machine Model (A115-A)
  - o Exceeds 2000-V Human Body Model (A114-A)
  - o Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 100mA per JESD 78, Class II
- SOT25 and SOT353: Assembled with "Green" Molding Compound (no Br, Sb)
- Lead Free Finish / RoHS Compliant (Note 1)

#### **Applications**

- General Purpose Logic
- Wide array of products such as:
  - o PCs, networking, notebooks, netbooks, PDAs
  - o Computer peripherals, hard drives, CD/DVD ROM
  - TV, DVD, DVR, set top box
  - Phones, Personal Navigation / GPS
  - MP3 players ,Cameras, Video Recorders

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead\_free.html.





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#### **Pin Descriptions**

Pin Name	Pin No.	Description
А	1	Data Input
В	2	Data Input
GND	3	Ground
Y	4	Data Output
V <sub>CC</sub>	5	Supply Voltage

### **Logic Diagram**



#### **Function Table**

Inp	Output	
Α	В	Υ
Н	Н	Н
L	Х	L
X	L	L





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#### **Absolute Maximum Ratings (Note 2)**

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	K۷
ESD CDM	Charged Device Model ESD Protection	1	K۷
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage applied to output in high or low state	-0.5 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input Clamp Current VI<0	-20	mA
I <sub>OK</sub>	Output Clamp Current (V <sub>O</sub> < 0 or V <sub>O</sub> > V <sub>CC</sub> )	±20	mA
Io	Continuous output current (V <sub>O</sub> = 0 to V <sub>CC</sub> )	±25	mA
Icc	Continuous current through V <sub>CC</sub>	50	mA
I <sub>GND</sub>	I <sub>GND</sub> Continuous current through GND		mA
TJ	T <sub>J</sub> Operating Junction Temperature		°C
T <sub>STG</sub>	Storage Temperature	-65 to 150	°C

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

#### **Recommended Operating Conditions (Note 3)**

Symbol	Paramete	er Min	Max	Unit
V <sub>CC</sub>	Operating Voltage	4.5	5.5	V
V <sub>IH</sub>	High-level Input Voltage	2.0		V
V <sub>IL</sub>	Low-level input voltage		0.8	V
VI	Input Voltage	0	5.5	V
Vo	Output Voltage	0	V <sub>CC</sub>	V
I <sub>OH</sub>	High-level output current		-8	mA
l <sub>OL</sub>	Low-level output current		8	mA
Δt/ΔV	Input transition rise or fall rate		20	ns/V
T <sub>A</sub>	Operating free-air temperature	-40	125	°C

Notes: 3. Unused inputs should be held at  $V_{\hbox{CC}}$  or Ground.





#### **SINGLE 2 INPUT POSITIVE AND GATE**

#### **Electrical Characteristics**

	_	T 10 III	.,		25ºC		-40°C 1	o 85°C	-40°C to	o 125ºC	
Symbol Parameter		Test Conditions	V <sub>CC</sub>	Min	Тур.	Max	Min	Max	Min	Max	Unit
.,	High Level	I <sub>OH</sub> = -50μA	4.5V	4.4	4.5		4.4		4.4		.,
V <sub>OH</sub>	Output Voltage	$I_{OH} = -8mA$	4.5V	3.94			3.8		3.70		V
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Low Level	$I_{OL} = 50\mu A$	4.5V		0	0.1		0.1		0.1	V
V <sub>OL</sub>	Output Voltage	$I_{OL} = 8mA$	4.5V			0.36		0.44		0.55	V
I <sub>I</sub>	Input Current	$V_I = 5.5V$ or GND	0 to 5.5V			± 0.1		± 1		± 2	μA
Icc	Supply Current	$V_I = 5.5V$ or GND $I_{O}=0$	5.5V			1		10		40	μΑ
C <sub>i</sub>	Input Capacitance	$V_I = V_{CC} - or$ GND	5.5V		2.0	10		10		10	pF
ΔI <sub>CC</sub>	Additional Supply Current	One input at 3.4V Other inputs at V <sub>CC</sub> or GND	5.5V			1.35		1.5			mA
	Thermal Resistance	SOT25	(A) ( A)		204						90.00
θ <sub>JA</sub>	Junction-to- Ambient	SOT353	(Note 4)		371						°C/W
Thermal	SOT25	(NI=(= 4)		52						90.00	
θ <sub>JC</sub>	Junction-to- Case	SOT353	(Note 4)		143						°C/W

Note: 4. Test conditions for SOT25, and SOT353: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

#### **Switching Characteristics**

 $V_{CC} = 5V \pm 0.5V$  (see Figure 1)

I	Doromotor	From	TO			25°C		-40°C t	o 85ºC	-40°C to	125ºC	Unit
	Parameter (Input) (OU	(OUTPUT)		Min	Тур.	Max	Min	Max	Min	Max	Onit	
		A or B		C <sub>L</sub> =15pF	0.6	3.6	6.2	0.6	7.1	0.6	8.0	ns
	<sup>t</sup> pd	AOIB	Ť	C <sub>L</sub> =50pF	0.6	5.1	7.9	0.6	9.0	0.6	10.5	ns

#### **Operating Characteristics**

 $T_A = 25$  °C

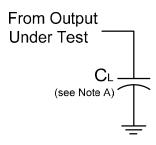
Parameter		Test	V <sub>CC</sub> = 5 V	Unit
	i arameter	Conditions	Тур.	Oilit
C <sub>pd</sub>	Power dissipation capacitance	f = 1 MHz No Load	14	pF



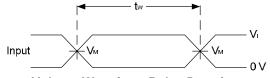


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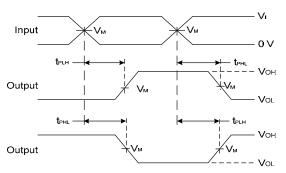
#### **Parameter Measurement Information**



Vcc	In	puts	V	
VCC	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	CL
5V±0.5V	3V	≤3ns	1.5V	15pF
5V±0.5V	3V	≤3ns	1.5V	50pF



**Voltage Waveform Pulse Duration** 



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t<sub>PLH</sub> and t<sub>PHL</sub> are the same as t<sub>pd</sub>.

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#### **Ordering Information**

T4 AHCT 1G 08 XX - 7

Logic Device Function Package Packing

74 : Logic Prefix 08 : 2-Input W5 : SOT25 7 : Tape & Reel

AND -Gate

AHCT: 2 to 5.5V

Family with TTL input level

1G: One gate

	Device	Package	Packaging	7" Tape	and Reel
	Device	Code	(Note 5)	Quantity	Part Number Suffix
<b>Pb</b> ,	74AHCT1G08W5-7	W5	SOT25	3000/Tape & Reel	-7
<b>Pb</b> ,	74AHCT1G08SE-7	SE	SOT353	3000/Tape & Reel	-7

Notes: 5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

#### **Marking Information**

#### (Top View)

XX: Identification code

Y : Year 0~9

<u>W</u>: Week: A~Z: 1~26 week;

SE: SOT353

a~z: 27~52 week; z represents 52 and 53 week

 $\underline{X}$ : A $^{\sim}$ Z: Internal code

Part Number	Package	Identification Code
74AHCT1G08W5	SOT25	ZU
74AHCT1G08SE	SOT353	ZU

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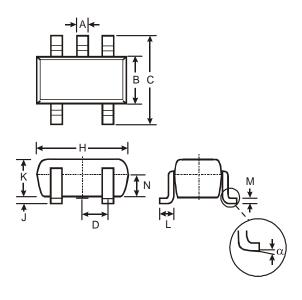


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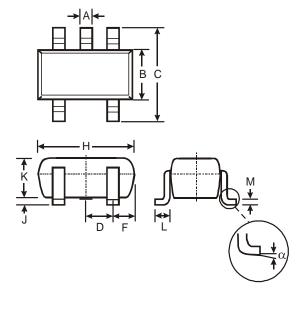
### Package Outline Dimensions (All Dimensions in mm)

#### (1) Package Type: SOT25



	SOT25							
Dim	Min	Max	Тур					
Α	0.35	0.50	0.38					
В	1.50	1.70	1.60					
С	2.70	3.00	2.80					
D		_	0.95					
Н	2.90	3.10	3.00					
J	0.013	0.10	0.05					
K	1.00	1.30	1.10					
L	0.35	0.55	0.40					
М	0.10	0.20	0.15					
N	0.70	0.80	0.75					
α	0°	8°	_					
All D	imensi	ons in	mm					

#### (2) Package Type: SOT353



SOT353						
Dim	Min	Max				
Α	0.10	0.30				
В	1.15	1.35				
С	2.00	2.20				
D	0.65	Тур				
F	0.40	0.45				
Н	1.80	2.20				
J	0	0.10				
K	0.90	1.00				
L	0.25	0.40				
М	0.10	0.22				
α	0°	8°				
All Di	mensions	in mm				





#### **SINGLE 2 INPUT POSITIVE AND GATE**

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