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Diodes Incorporated DMN3730U-7

For any questions, you can email us directly: sales@integrated-circuit.com

Datasheet of DMN3730U-7 - MOSFET N-CH 30V 750MA SOT23

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com





A Product Line of Diodes Incorporated



DMN3730U

30V N-CHANNEL ENHANCEMENT MODE MOSFET IN SOT23

Product Summary

V _{(BR)DSS}	Max R _{DS(on)}	I _D Max (Note 5) T _A = 25°C
201/	460mΩ @ V _{GS} = 4.5V	0.94A
30V	560mΩ @ V _{GS} = 2.5V	0.85A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Load switch
- · Portable applications
- Power Management Functions

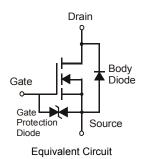
Features and Benefits

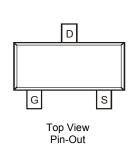
- Low V_{GS(th),} can be driven directly from a battery
- Low R_{DS(on)}
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- ESD Protected Gate 2kV
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish-Matte Tin.
- Weight: 0.08 grams (approximate)







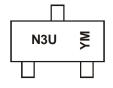
Ordering Information (Note 3)

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN3730U-7	N3U	7	8	3,000

Notes:

- 1. No purposefully added lead
- 2. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



N3U = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date Code Key

Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Υ		Z		Α	E	3	С		D		E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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Maximum Ratings @TA = 25°C unless otherwise specified

Cha	racteristic		Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current	Steady T _A = 25°C (Note 5)		I _D	0.94 0.68 0.75	А
Pulsed Drain Current (Note 6)	•		I _{DM}	10	А

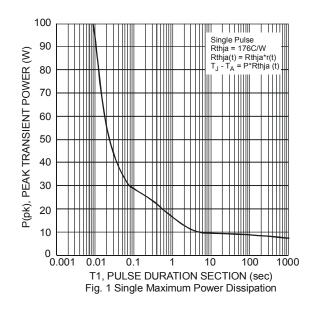
Thermal Characteristics @TA = 25°C unless otherwise specified

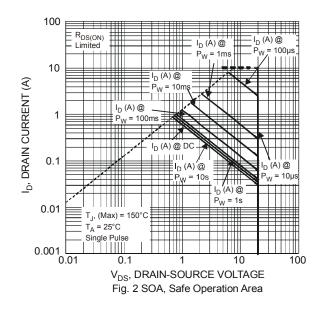
Characteristic	Symbol	Value	Unit	
Power Dissipation	(Note 4)	D	0.45	W
Power Dissipation	(Note 5)	P_{D}	0.71	W
Thermal Resistance, Junction to Ambient	(Note 4)	Б	275	°C/W
	(Note 5)	$R_{\theta JA}$	177	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes:

- 4. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout
- 5. Device mounted on 25mm X 25mm square copper plate with FR-4 substrate PC board, 2oz copper
- 6. Device mounted on minimum recommended pad layout test board, 10μs pulse duty cycle = 1%.

Thermal Characteristics





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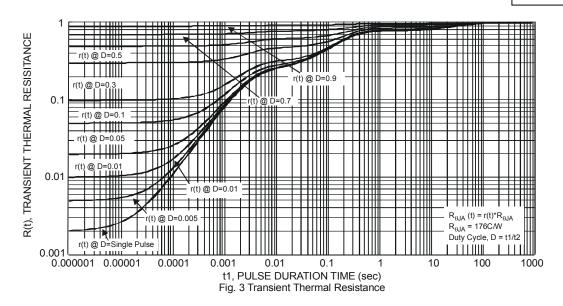
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Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	-	-	3	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS	-						
Gate Threshold Voltage	V _{GS(th)}	0.45	-	0.95	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
				460		$V_{GS} = 4.5V, I_D = 200mA$	
Static Drain-Source On-Resistance (Note 7)	R _{DS(on)}	-	-	560	$m\Omega$	$V_{GS} = 2.5V, I_D = 100mA$	
				730		$V_{GS} = 1.8V, I_D = 75mA$	
Forward Transfer Admittance	Y _{fs}	40	-	-	mS	$V_{DS} = 3V, I_{D} = 10mA$	
Diode Forward Voltage (Note 7)	V _{SD}	-	0.7	1.2	V	$V_{GS} = 0V, I_S = 300mA$	
DYNAMIC CHARACTERISTICS (Note 8)						•	
Input Capacitance	C _{iss}	-	64.3	-	pF	V 05)/)/ 0)/	
Output Capacitance	Coss	-	6.1	-	pF	$V_{DS} = 25V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance	Crss	-	4.5	-	pF	71 - 1.0101112	
Gate Resistance	Rg	-	70	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	-	1.6	-	nC	4.574.37	
Gate-Source Charge	Q _{gs}	-	0.2	-	nC	$V_{GS} = 4.5V, V_{DS} = 15V,$ $I_{D} = 1A$	
Gate-Drain Charge	Q_{qd}	-	0.2	-	nC		
Turn-On Delay Time	t _{D(on)}	-	3.5	-	ns		
Turn-On Rise Time	t _r	-	2.8	-	ns	V _{DS} = 10V, I _D = 1A	
Turn-Off Delay Time	t _{D(off)}	-	38	-	ns	V_{GS} = 10V, R_G = 6 Ω	
Turn-Off Fall Time	t _f	-	13	-	ns		

Notes:

- 7. Measured under pulsed conditions to minimize self-heating effect. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%$
- 8. For design aid only, not subject to production testing.

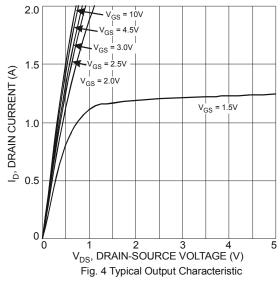
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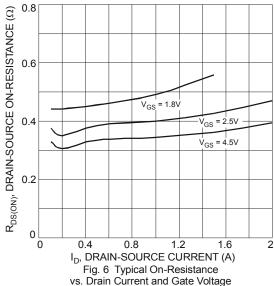
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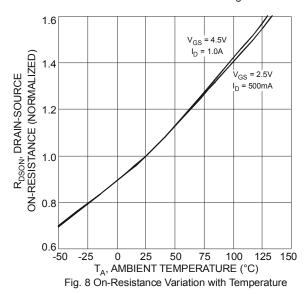


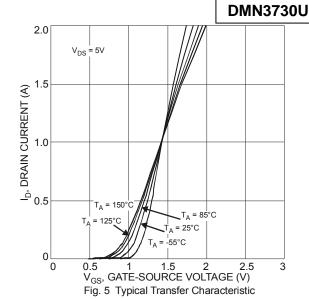
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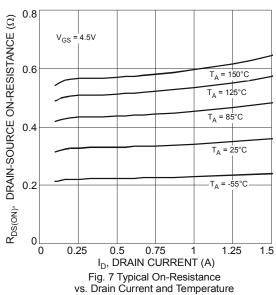












Vs. Drain Current and Temperature

0.8

0.8

V_{GS} = 2.5V

V_{GS} = 4.5V

V_D = 1.0A

V_{GS} = 4.5V

V_D = 1.0A

T_A, AMBIENT TEMPERATURE (°C)

1.2

1.0

0.8

0.6

0.4

0.2

-50 -25

 $V_{\mathrm{GS(TH)}}$, GATE THRESHOLD VOLTAGE (V)

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I_D = 1mA

100 125 150

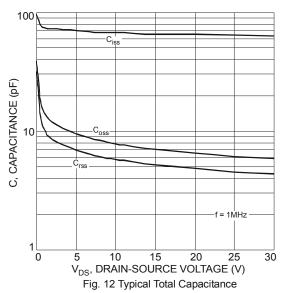
T_A, AMBIENT TEMPERATURE (°C) Fig. 10 Gate Threshold Variation vs. Ambient Temperature

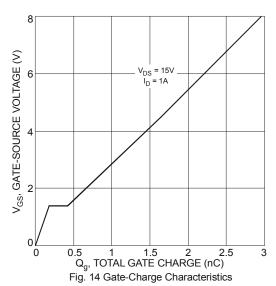
50

75

25

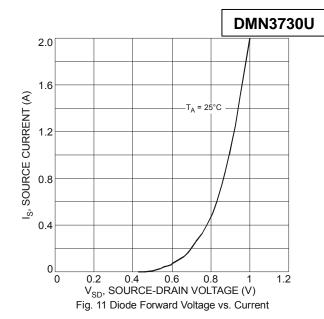
 $I_{D} = 250 \mu A$

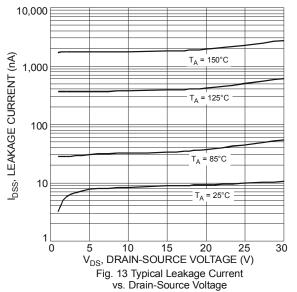




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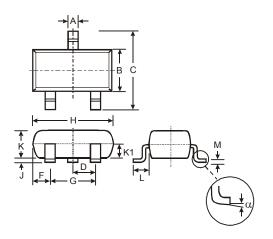






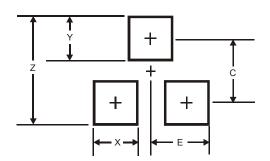
DMN3730U

Package Outline Dimensions



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Η	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	K 0.903		1.00				
K1	-	ı	0.400				
L	0.45	0.61	0.55				
M	0.085	0.18	0.11				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
C	2.0
Е	1.35



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