

## Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

[Fairchild Semiconductor](#)

[FDB86102LZ](#)

For any questions, you can email us directly:

[sales@integrated-circuit.com](mailto:sales@integrated-circuit.com)



May 2011



## FDB86102LZ

### N-Channel PowerTrench® MOSFET 100 V, 30 A, 24 mΩ

#### Features

- Max  $r_{DS(on)}$  = 24 mΩ at  $V_{GS} = 10$  V,  $I_D = 8.3$  A
- Max  $r_{DS(on)}$  = 35 mΩ at  $V_{GS} = 4.5$  V,  $I_D = 6.8$  A
- HBM ESD protection level > 6 kV typical (Note 4)
- Very low  $Q_g$  and  $Q_{gd}$  compared to competing trench technologies
- Fast switching speed
- 100% UIL Tested
- RoHS Compliant

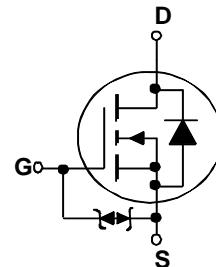
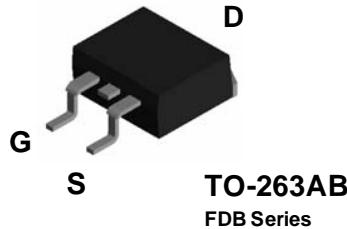


#### General Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been especially tailored to minimize the on-state resistance and switching loss. G-S zener has been added to enhance ESD voltage level.

#### Applications

- DC-DC conversion
- Inverter
- Synchronous Rectifier



#### MOSFET Maximum Ratings $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain to Source Voltage	100	V
$V_{GS}$	Gate to Source Voltage	$\pm 20$	V
$I_D$	Drain Current -Continuous(Package limited)	30	A
	-Continuous (Silicon limited)	40	
	-Continuous	8.3	
	-Pulsed	50	
$E_{AS}$	Single Pulse Avalanche Energy	(Note 3)	121
$P_D$	Power Dissipation	$T_A = 25$ °C	W
	Power Dissipation	$T_A = 25$ °C	
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	°C

#### Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Note 1)	1.9	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	40	

#### Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB86102LZ	FDB86102LZ	TO-263AB	330mm	24 mm	800 units

## Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain to Source Breakdown Voltage	$I_D = 250 \mu\text{A}$ , $V_{GS} = 0 \text{ V}$	100			V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , referenced to $25^\circ\text{C}$		69		$\text{mV}/^\circ\text{C}$
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 80 \text{ V}$ , $V_{GS} = 0 \text{ V}$		1		$\mu\text{A}$
$I_{GSS}$	Gate to Source Leakage Current	$V_{GS} = \pm 20 \text{ V}$ , $V_{DS} = 0 \text{ V}$			$\pm 10$	$\mu\text{A}$

## On Characteristics

$V_{GS(\text{th})}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 250 \mu\text{A}$	1.0	1.5	3.0	V
$\Delta V_{GS(\text{th})} / \Delta T_J$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , referenced to $25^\circ\text{C}$		-6		$\text{mV}/^\circ\text{C}$
$r_{DS(\text{on})}$	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}$ , $I_D = 8.3 \text{ A}$		18	24	$\text{m}\Omega$
		$V_{GS} = 4.5 \text{ V}$ , $I_D = 6.8 \text{ A}$		23	35	
		$V_{GS} = 10 \text{ V}$ , $I_D = 8.3 \text{ A}$ , $T_J = 125^\circ\text{C}$		31	42	
$g_{FS}$	Forward Transconductance	$V_{DS} = 5 \text{ V}$ , $I_D = 8.3 \text{ A}$		29		s

## Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{DS} = 50 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $f = 1\text{MHz}$		959	1275	pF
$C_{oss}$	Output Capacitance			181	240	pF
$C_{rss}$	Reverse Transfer Capacitance			9	13	pF
$R_g$	Gate Resistance			0.4		$\Omega$

## Switching Characteristics

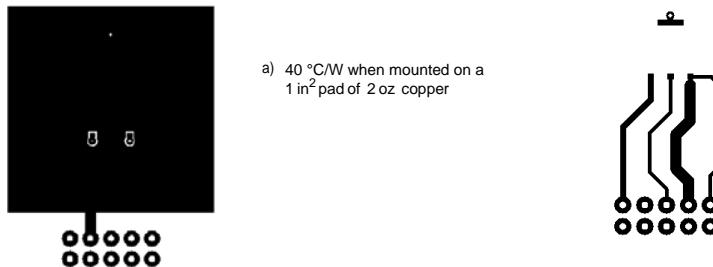
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 50 \text{ V}$ , $I_D = 8.3 \text{ A}$ , $V_{GS} = 10 \text{ V}$ , $R_{\text{GEN}} = 6 \Omega$		6.6	13	ns
$t_r$	Rise Time			2.1	10	ns
$t_{d(off)}$	Turn-Off Delay Time			18.2	33	ns
$t_f$	Fall Time			2.3	10	ns
$Q_{g(\text{TOT})}$	Total Gate Charge	$V_{GS} = 0 \text{ V}$ to $10 \text{ V}$		15.2	21	nC
$Q_{g(\text{TOT})}$	Total Gate Charge		$V_{DD} = 50 \text{ V}$	7.6	11	nC
$Q_{gs}$	Gate to Source Charge	$I_D = 8.3 \text{ A}$		2.4		nC
$Q_{gd}$	Gate to Drain "Miller" Charge			2.5		nC

## Drain-Source Diode Characteristics

$V_{SD}$	Source to Drain Diode Forward Voltage	$V_{GS} = 0 \text{ V}$ , $I_S = 8.3 \text{ A}$	(Note 2)	0.8	1.3	V
		$V_{GS} = 0 \text{ V}$ , $I_S = 2.4 \text{ A}$	(Note 2)	0.7	1.2	
$t_{rr}$	Reverse Recovery Time	$I_F = 8.3 \text{ A}$ , $di/dt = 100 \text{ A}/\mu\text{s}$		42	67	ns
				40	64	

NOTES:

1.  $R_{0JA}$  is determined with the device mounted on a  $1 \text{ in}^2$  pad 2 oz copper pad on a  $1.5 \times 1.5 \text{ in.}$  board of FR-4 material.  $R_{0JC}$  is guaranteed by design while  $R_{0CA}$  is determined by the user's board design.



a)  $40^\circ\text{C}/\text{W}$  when mounted on a  $1 \text{ in}^2$  pad of 2 oz copper

b)  $62.5^\circ\text{C}/\text{W}$  when mounted on a minimum pad of 2 oz copper

2. Pulse Test: Pulse Width  $< 300 \mu\text{s}$ , Duty cycle  $< 2.0\%$ .

3. Starting  $T_J = 25^\circ\text{C}$ ,  $L = 3 \text{ mH}$ ,  $I_{AS} = 9 \text{ A}$ ,  $V_{DD} = 100 \text{ V}$ ,  $V_{GS} = 10 \text{ V}$ .

4. The diode connected between gate and source serves only as protection against ESD. No gate overvoltage rating is implied.

**Typical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise noted

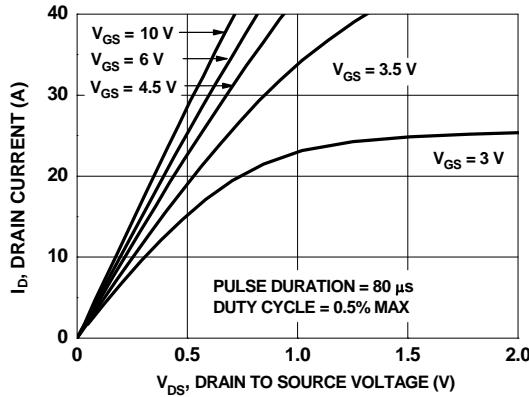


Figure 1. On Region Characteristics

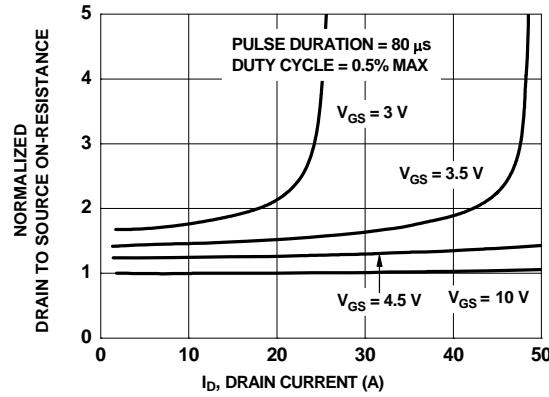


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

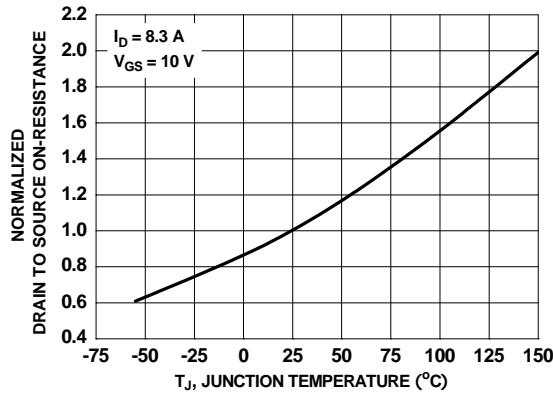


Figure 3. Normalized On Resistance vs Junction Temperature

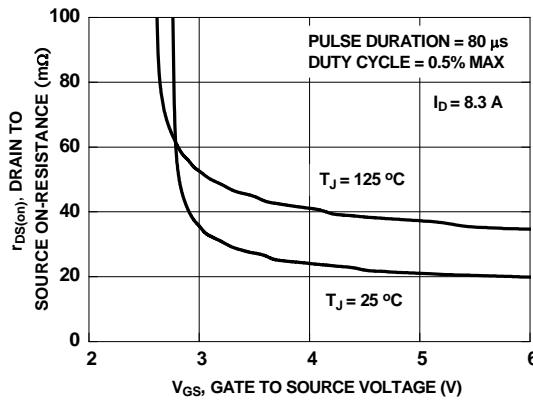


Figure 4. On-Resistance vs Gate to Source Voltage

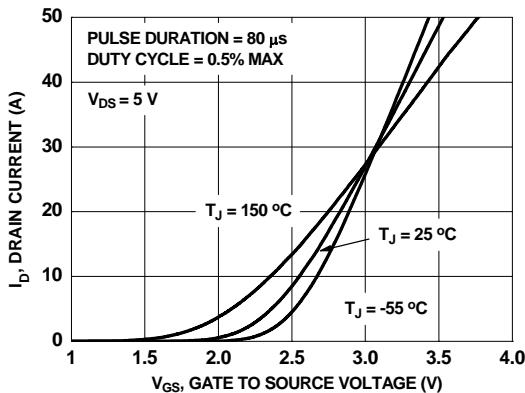


Figure 5. Transfer Characteristics

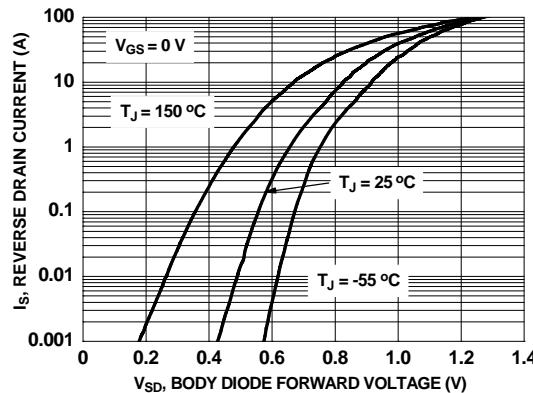


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

**Typical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise noted

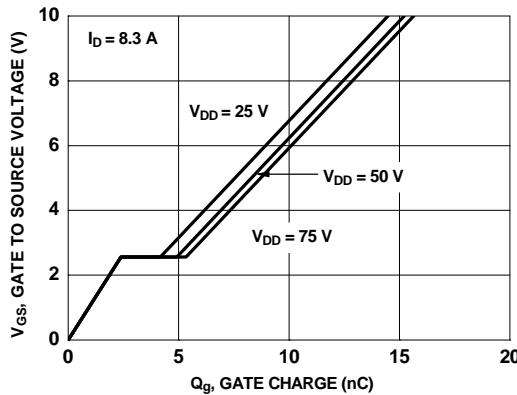


Figure 7. Gate Charge Characteristics

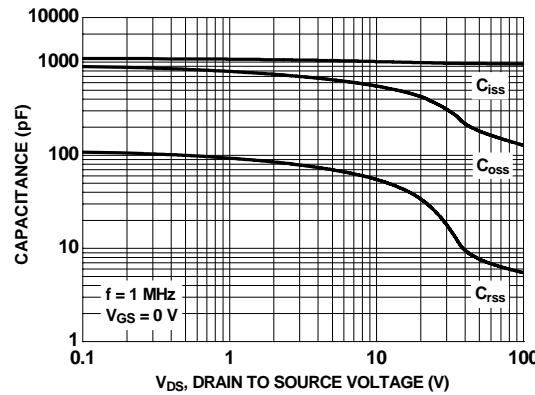


Figure 8. Capacitance vs Drain to Source Voltage

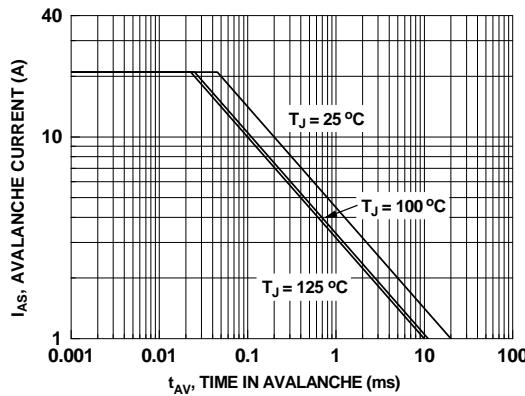


Figure 9. Unclamped Inductive Switching Capability

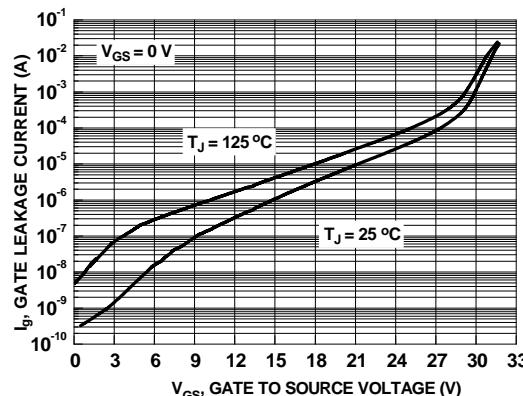


Figure 10. Gate Leakage Current vs Gate to Source Voltage

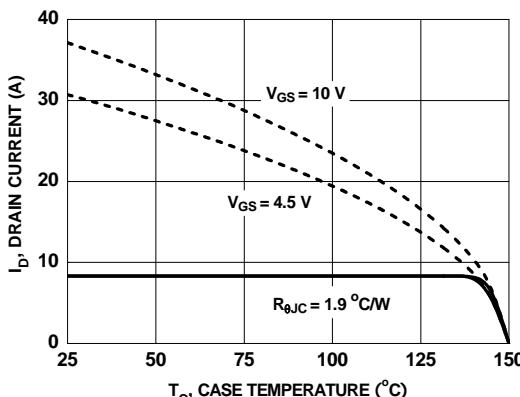


Figure 11. Maximum Continuous Drain Current vs Case Temperature

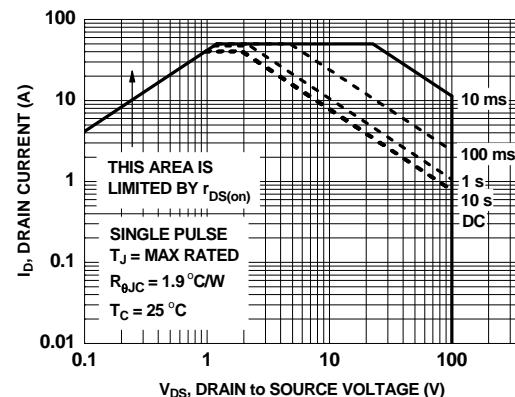


Figure 12. Forward Bias Safe Operating Area

**Typical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise noted

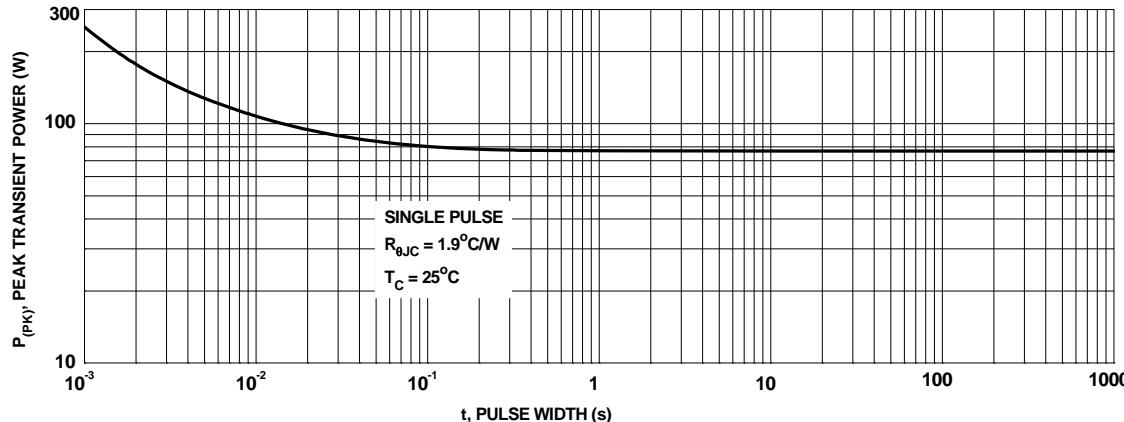


Figure 13. Single Pulse Maximum Power Dissipation

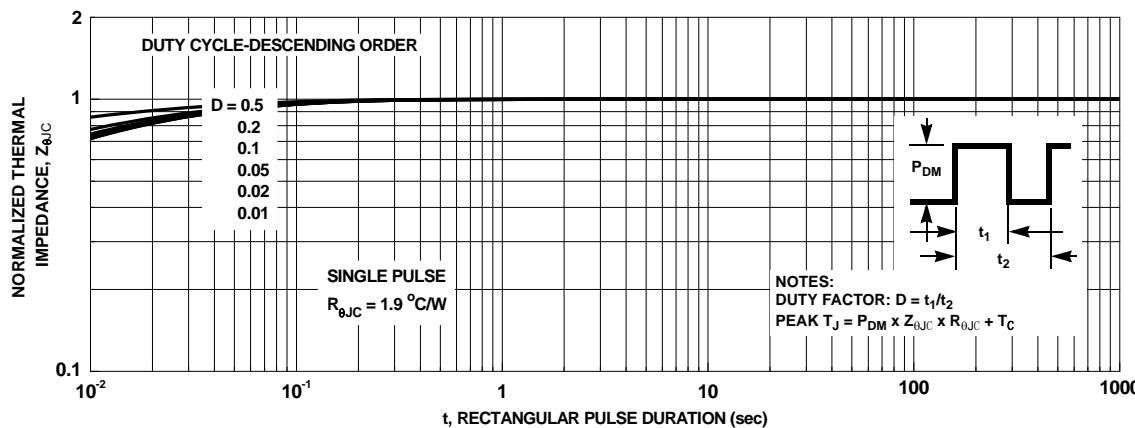


Figure 14. Junction-to-Case Transient Thermal Response Curve



#### TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™	FPST™	Power-SPM™	The Power Franchise®
Auto-SPM™	F-PFST™	PowerTrench®	The Right Technology for Your Success™
AX-CAP™*	FRFET®	PowerXSTM	the  powerfranchise
BitSiC®	Global Power Resource™	Programmable Active Droop™	TinyBoost™
Build it Now™	Green FPST™	QFET®	TinyBuck™
CorePLUS™	Green FPST™ e-Series™	QS™	TinyCalc™
CorePOWER™	Gmax™	Quiet Series™	TinyLogic®
CROSSVOLT™	GTO™	RapidConfigure™	TINYOPTO™
CTL™	IntelliMAX™		TinyPower™
Current Transfer Logic™	ISOPLANAR™	Saving our world, 1mW/W/kW at a time™	TinyPWM™
DEUXPEED®	MegaBuck™	SignalWise™	TinyWire™
Dual Cool™	MICROCOUPLER™	SmartMax™	TransIC®
EcoSPARK®	MicroFET™	SMART START™	TriFault Detect™
EfficientMax™	MicroPak™	SPM®	TRUECURRENT®*
ESBC™	MicroPak2™	STEALTH™	μSerDes™
	MillerDrive™	SuperFET®	UHC®
Fairchild®	MotionMax™	SupersOTT™-3	Ultra FRFET™
Fairchild Semiconductor®	mWSaver™	SupersOTT™-6	UniFET™
FACT Quiet Series™	OptiHiT™	SupersOTT™-8	VCXTM
FACT®	OPTOLOGIC®	SupreMOS®	VisualMax™
FAST®	OPTOPLANAR®	SyncFET™	XS™
FastvCore™		Sync-Lock™	
FETBench™	PDP SPM™		

\*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, [www.Fairchildsemi.com](http://www.Fairchildsemi.com), under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

#### PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.