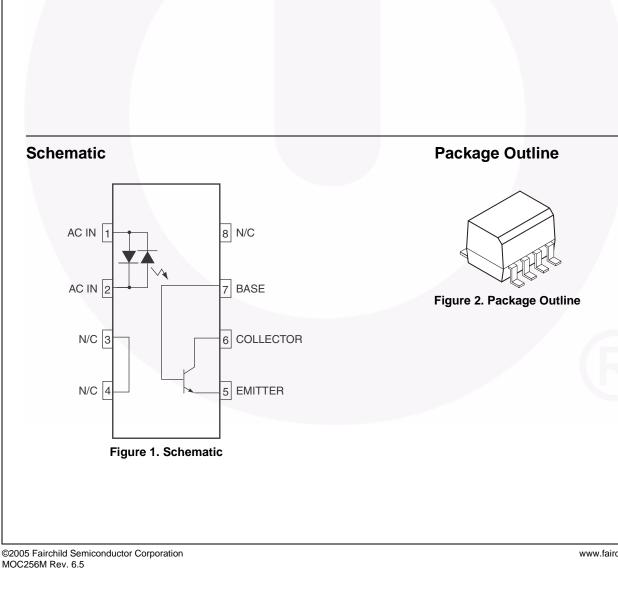
July 2015



MOC256M 8-pin SOIC AC Input Phototransistor Output Optocoupler

Features

Bidirectional AC Input
Protection Against Reversed DC Bias

FAIRCHILD

- Guaranteed CTR Symmetry of 2:1 Maximum
- Convenient Plastic SOIC-8 Surface Mountable Package Style, with 0.050" Lead Spacing
- Safety and Regulatory Approvals:
 - UL1577, 2,500 VAC_{RMS} for 1 Minute
 - DIN-EN/IEC60747-5-5, 565 V Peak Working Insulation Voltage

Description

The MOC256M is an AC input phototransistor optocoupler. The device consists of two infrared emitters connected in anti-parallel and coupled to a silicon NPN phototransistor detector. It is designed for applications requiring the detection or monitoring of AC signals. The device is constructed with a standard SOIC-8 footprint.

Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter	Characteristics	
Installation Classifications per DIN VDE	< 150 V _{RMS}	I–IV
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V _{RMS}	I–III
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
V	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$, Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC	904	V _{peak}
V _{PR}	Input-to-Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PR}$, 100% Production Test with $t_m = 1 \text{ s}$, Partial Discharge < 5 pC	1060	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	565	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	4000	V _{peak}
	External Creepage	≥ 4	mm
	External Clearance	≥ 4	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.4	mm
Τ _S	Case Temperature ⁽¹⁾	150	°C
I _{S,INPUT}	Input Current ⁽¹⁾	200	mA
Ps,output	Output Power ⁽¹⁾	300	mW
R _{IO}	Insulation Resistance at T_S , V_{IO} = 500 $V^{(1)}$	> 10 ⁹	Ω

Note:

1. Safety limit values - maximum values allowed in the event of a failure.

MOC256M — 8-pin SOIC AC Input Phototransistor Output Optocoupler

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. $T_A = 25^{\circ}C$ unless otherwise specified.

Symbol	Rating	Value	Unit	
TOTAL DEV	ICE			
T _{STG}	Storage Temperature	-40 to +125	°C	
T _A	Ambient Operating Temperature	-40 to +100	°C	
ТJ	Junction Temperature	-40 to +125	°C	
T _{SOL}	Lead Solder Temperature	260 for 10 seconds	°C	
	Total Device Power Dissipation @ T _A = 25°C	240	mW	
PD	Derate Above 25°C	2.94	mW/°C	
EMITTER				
I _F	Continuous Forward Current	60	mA	
l _F (pk)	Forward Current – Peak (PW = 100 µs, 120 pps)	1.0	А	
V _R	Reverse Voltage	6.0	V	
Р	LED Power Dissipation @ T _A = 25°C	90	mW	
PD	Derate Above 25°C	0.8	mW/°C	
DETECTOR				
۱ _C	Continuous Collector Current	150	mA	
V _{CEO}	Collector-Emitter Voltage	30	V	
V _{CBO}	Collector-Base Voltage	70	V	
V _{ECO}	Emitter-Collector Voltage	7	V	
Р	Detector Power Dissipation @ T _A = 25°C	150	mW	
PD	Derate Above 25°C	1.76	mW/°C	

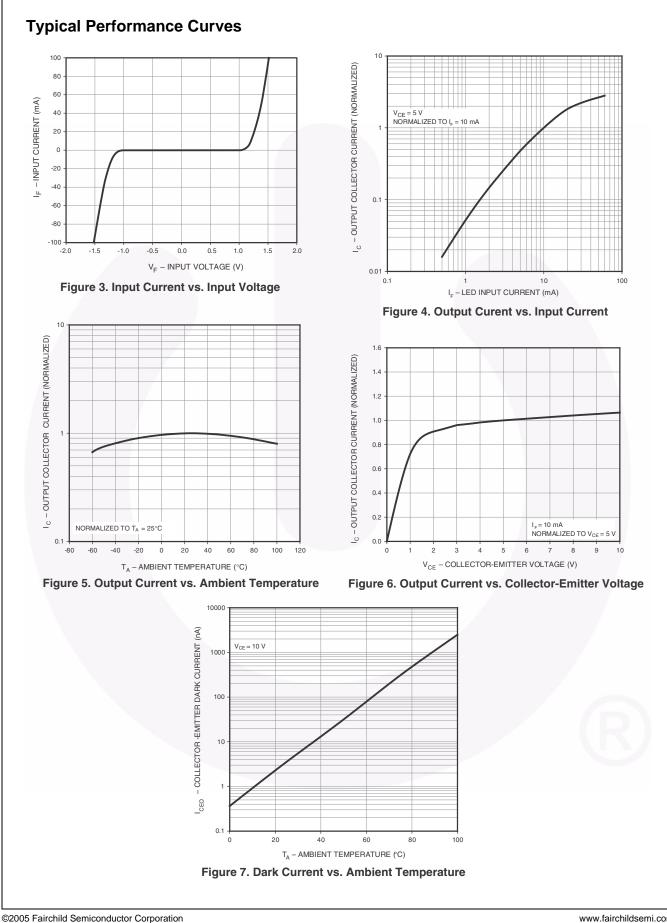
Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
EMITTER				L	1	
V _F	Input Forward Voltage	$I_F = \pm 10 \text{ mA}$		1.2	1.5	V
C _{IN}	Input Capacitance	V = 0 V, f = 1 MHz		20		pF
DETECTO	R					
I _{CEO1}	Collector-Emitter Dark Current	$V_{CE} = 10 \text{ V}, \text{ T}_{A} = 25^{\circ}\text{C}$		1.0	100	nA
I _{CEO2}		$V_{CE} = 10 \text{ V}, \text{ T}_{A} = 100^{\circ}\text{C}$		1.0		μA
I _{CBO}	Collector-Base Dark Current	V _{CB} = 10 V		0.2		nA
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 10 mA	30	100		nA
BV _{CBO}	Collector-Base Breakdown Voltage	I _C = 100 μA	70	120		V
BV _{ECO}	Emitter-Collector Breakdown Voltage	I _E = 100 μA	5	10		V
C _{CE}	Collector-Emitter Capacitance	f = 1.0 MHz, V _{CE} = 0		7		pF
C _{CB}	Collector-Base Capacitance	f = 1.0 MHz, V _{CB} = 0		20		pF
C _{EB}	Emitter-Base Capacitance	f = 1.0 MHz, V _{EB} = 0		10		pF
COUPLED)					
CTR	Current Transfer Ratio	$I_{F} = \pm 10 \text{ mA}, V_{CE} = 10 \text{ V}$	20	150		%
	Output-Collector Current Symmetry	$\left(\frac{I_{C} @ I_{F} = +10 \text{ mA}, V_{CE} = 10 \text{ V}}{I_{C} @ I_{F} = -10 \text{ mA}, V_{CE} = 10 \text{ V}}\right)$	0.5		2.0	
V _{CE (SAT)}	Collector-Emitter Saturation Voltage	$I_{\rm C} = 0.5 \text{ mA}, I_{\rm F} = \pm 10 \text{ mA}$		0.1	0.4	V

Isolation Characteristics

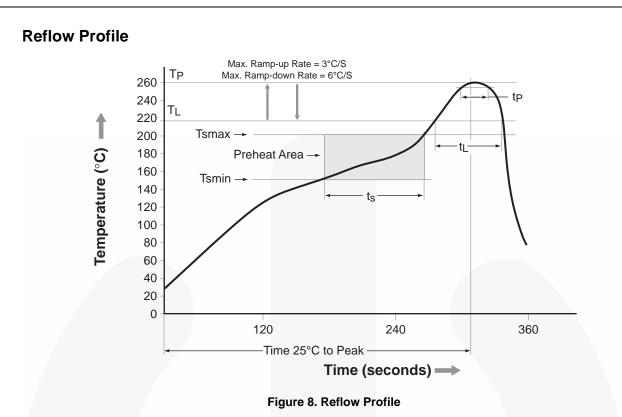
Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
V _{ISO}	Input-Output Isolation Voltage	t = 1 Minute	2500			VAC _{RMS}
C _{ISO}	Isolation Capacitance	V _{I-O} = 0 V, f = 1 MHz		0.2		pF
R _{ISO}	Isolation Resistance	$V_{I-O} = \pm 500 \text{ VDC}, T_A = 25^{\circ}C$	10 ¹¹			Ω



5

MOC256M Rev. 6.5

MOC256M — 8-pin SOIC AC Input Phototransistor Output Optocoupler



Profile Freature	Pb-Free Assembly Profile	
Temperature Minimum (Tsmin)	150°C	
Temperature Maximum (Tsmax)	200°C	
Time (t _S) from (Tsmin to Tsmax)	60–120 seconds	
Ramp-up Rate (t _L to t _P)	3°C/second maximum	
Liquidous Temperature (T _L)	217°C	
Time (t_L) Maintained Above (T_L)	60–150 seconds	
Peak Body Package Temperature	260°C +0°C / –5°C	
Time (t _P) within 5°C of 260°C	30 seconds	
Ramp-down Rate (T_P to T_L)	6°C/second maximum	
Time 25°C to Peak Temperature	8 minutes maximum	

Ordering Information

Part Number	Package	Packing Method
MOC256M	Small Outline 8-Pin	Tube (100 Units)
MOC256R2M	Small Outline 8-Pin	Tape and Reel (2500 Units)
MOC256VM	Small Outline 8-Pin, DIN EN/IEC60747-5-5 Option	Tube (100 Units)
MOC256R2VM	Small Outline 8-Pin, DIN EN/IEC60747-5-5 Option	Tape and Reel (2500 Units)

Marking Information

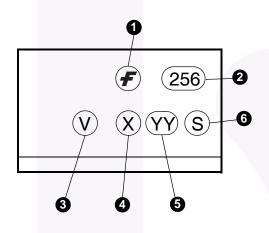
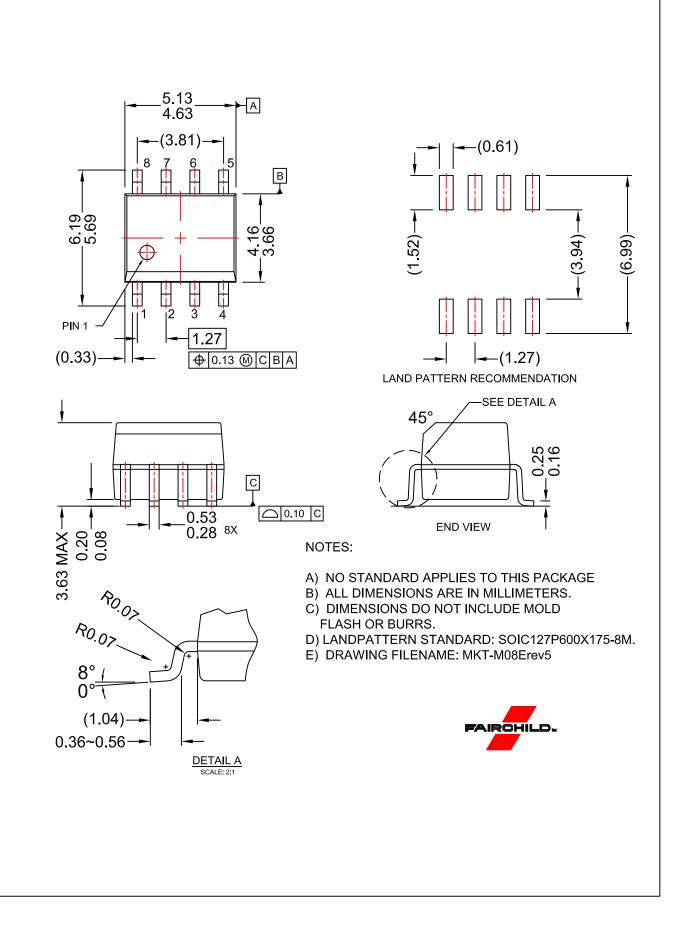


Figure 9. Top Mark

Table 1. Top Mark Definitions

1	Fairchild Logo
2	Device Number
3	DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)
4	One-Digit Year Code, e.g., "4"
5	Digit Work Week, Ranging from "01" to "53"
6	Assembly Package Code





* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. TO OBTAIN THE LATEST, MOST UP-TO-DATE DATASHEET AND PRODUCT INFORMATION, VISIT OUR WEBSITE AT <u>HTTP://WWW.FAIRCHILDSEMI.COM</u>, FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

AUTHORIZED USE

Unless otherwise specified in this data sheet, this product is a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability. This product may not be used in the following applications, unless specifically approved in writing by a Fairchild officer: (1) automotive or other transportation, (2) military/aerospace, (3) any safety critical application – including life critical medical equipment – where the failure of the Fairchild product reasonably would be expected to result in personal injury, death or property damage. Customer's use of this product is subject to agreement of this Authorized Use policy. In the event of an unauthorized use of Fairchild's product, Fairchild accepts no liability in the event of product failure. In other respects, this product shall be subject to Fairchild's Worldwide Terms and Conditions of Sale, unless a separate agreement has been signed by both Parties.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Terms of Use

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms		
Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 177