

# **Excellent Integrated System Limited**

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Vishay Semiconductor/Diodes Division SS26/54

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**Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite** Datasheet of SS26/54 - DIODE SCHOTTKY 60V 2A DO214AA Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



## SS22-M3, SS23-M3, SS24-M3, SS25-M3, SS26-M3

Vishay General Semiconductor

## Surface Mount Schottky Barrier Rectifier



DO-214AA (SMB)

PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub> 2.0 A						
V <sub>RRM</sub>	20 V, 30 V, 40 V, 50 V, 60 V					
I <sub>FSM</sub>	I <sub>FSM</sub> 75 A					
V <sub>F</sub>	0.50 V, 0.70 V					
T <sub>J</sub> max.	150 °C					
Package	DO-214AA (SMB)					
Diode variations	Single					

### **TYPICAL APPLICATIONS**

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

### FEATURES

- Low profile package
- Ideal for automated placement
- Guardring for overvoltage protection
- Low power losses, high efficiency
- Low forward voltage drop
- High surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### MECHANICAL DATA

Case: DO-214AA (SMB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)								
PARAMETER	SYMBOL	SS22	SS23	SS24	SS25	SS26	UNIT	
Device marking code		S2	S3	S4	S5	S6		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	20	30	40	50	60	V	
Maximum RMS voltage	V <sub>RMS</sub>	14 21 28 35 42				42	V	
Maximum DC blocking voltage	V <sub>DC</sub>	20 30 40 50 60				60	V	
Max. average forward rectified current at $T_L$ (fig. 1)	I <sub>F(AV)</sub>	2.0						
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	75					А	
Non-repetitive avalanche energy at $T_A = 25 \ ^\circ C$ , $I_{AS} = 2.0 \ A$ , L = 10 mH	E <sub>AS</sub>	20					mJ	
Electrostatic discharge capacitor voltage human body model: C = 100 pF, R = 1.5 k $\Omega$	V <sub>C</sub>	8.0					kV	
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	10 000					V/µs	
Operating junction temperature range	TJ	-65 to +150					°C	
Storage temperature range	T <sub>STG</sub>	-65 to +150					°C	

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS	SYMBOL	SS22	SS23	SS24	SS25	SS26	UNIT
Maximum instantaneous forward voltage (1)	2.0 A	V <sub>F</sub>	0.5		0.7		V	
Maximum DC reverse current at rated DC	T <sub>A</sub> = 25 °C	1	0.4					mA
blocking voltage <sup>(1)</sup>	T <sub>A</sub> = 100 °C	<sup>I</sup> R	10					

Note

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

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FREE





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### Vishay General Semiconductor

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
SYMBOL	YMBOL SS22 SS23 SS24 SS25 SS2				SS26	UNIT	
$R_{\theta JA}$	75						
$R_{\theta JL}$	17						
-	$\frac{\text{SYMBOL}}{R_{\theta JA}}$	SYMBOL         SS22           R <sub>0JA</sub>	SYMBOL     SS22     SS23       R <sub>0JA</sub>	SYMBOL         SS22         SS23         SS24           R <sub>0JA</sub> 75	SYMBOL         SS22         SS23         SS24         SS25           R <sub>0JA</sub> 75         75         75	SYMBOL         SS22         SS23         SS24         SS25         SS26           R <sub>0JA</sub>	

Note

<sup>(1)</sup> PCB mounted with 0.55" x 0.55" (14 mm x 14 mm) copper pad areas

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
SS24-M3/52T	0.096	52T	750	7" diameter plastic tape and reel			
SS24-M3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel			

### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

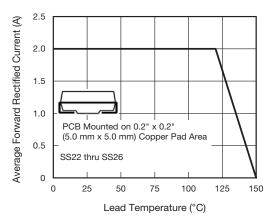


Fig. 1 - Forward Current Derating Curve

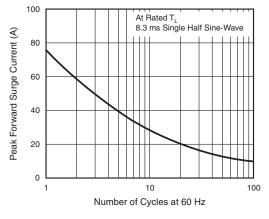
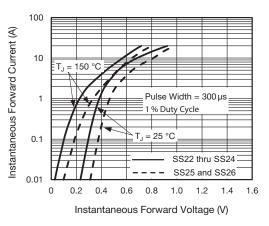
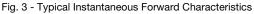


Fig. 2 - Maximum Non-Repetitive Surge Current





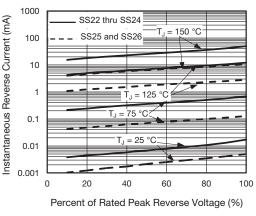


Fig. 4 - Typical Reverse Current Characteristics

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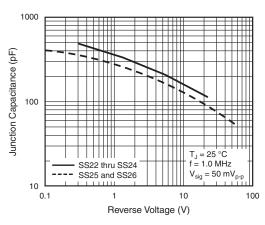
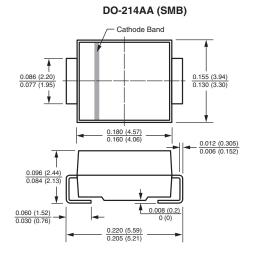
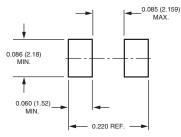


Fig. 5 - Typical Junction Capacitance

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





Mounting Pad Layout

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