

## Excellent Integrated System Limited

Stocking Distributor

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[Vishay/Siliconix](#)  
[SI7655DN-T1-GE3](#)

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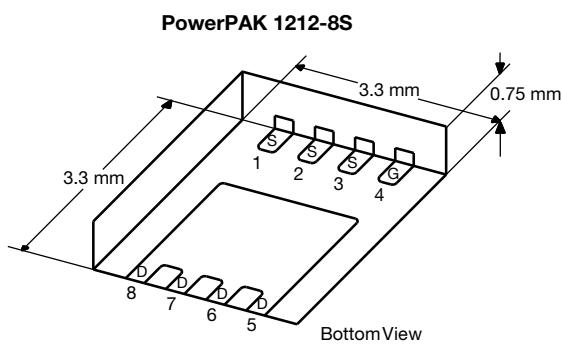
**New Product**

**Si7655DN**

Vishay Siliconix

**P-Channel 20 V (D-S) MOSFET**

<b>PRODUCT SUMMARY</b>			
<b>V<sub>DS</sub> (V)</b>	<b>R<sub>DS(on)</sub> (Ω) Max.</b>	<b>I<sub>D</sub> (A)</b>	<b>Q<sub>g</sub> (Typ.)</b>
- 20	0.0036 at V <sub>GS</sub> = - 10 V	- 40 <sup>e</sup>	72 nC
	0.0048 at V <sub>GS</sub> = - 4.5 V	- 40 <sup>e</sup>	
	0.0085 at V <sub>GS</sub> = - 2.5 V	- 40 <sup>e</sup>	



**Ordering Information:**  
SI7655DN-T1-GE3 (Lead (Pb)-free and Halogen-free)

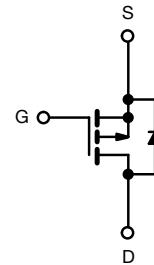


**FEATURES**

- TrenchFET® Power MOSFET
- Low Thermal Resistance PowerPAK® Package with Small Size and Low 0.75 mm Profile
- 100 % R<sub>g</sub> and UIS Tested
- Material categorization: For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

**APPLICATIONS**

- Smart Phones, Tablet PCs, Mobile Computing
  - Battery Switch
  - Load Switch



P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	- 20	V
Gate-Source Voltage	V <sub>GS</sub>	± 12	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C	- 40 <sup>e</sup>	A
	T <sub>C</sub> = 70 °C	- 40 <sup>e</sup>	
	T <sub>A</sub> = 25 °C	- 31 <sup>a, b</sup>	
	T <sub>A</sub> = 70 °C	- 25 <sup>a, b</sup>	
Pulsed Drain Current (t = 300 μs)	I <sub>DM</sub>	- 100	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	- 40 <sup>e</sup>	
	T <sub>A</sub> = 25 °C	- 4 <sup>a, b</sup>	
Avalanche Current	I <sub>AS</sub>	- 20	
Single-Pulse Avalanche Energy	E <sub>AS</sub>	20	mJ
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	57	W
	T <sub>C</sub> = 70 °C	36	
	T <sub>A</sub> = 25 °C	4.8 <sup>a, b</sup>	
	T <sub>A</sub> = 70 °C	3 <sup>a, b</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 50 to 150	
Soldering Recommendations (Peak Temperature) <sup>c, d</sup>		260	°C

Notes:

- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- See solder profile ([www.vishay.com/doc?73257](http://www.vishay.com/doc?73257)). The PowerPAK 1212-8S is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- Package limited.

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## THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a, b</sup>	$t \leq 10 \text{ s}$	$R_{thJA}$	21	26
Maximum Junction-to-Case (Drain)	Steady State	$R_{thJC}$	1.7	2.2

Notes:

a. Surface mounted on 1" x 1" FR4 board.  
 b. Maximum under steady state conditions is 63 °C/W.

## SPECIFICATIONS ( $T_J = 25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-20			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250 \mu\text{A}$		-12		$\text{mV}/^\circ\text{C}$
$V_{GS(\text{th})}$ Temperature Coefficient	$\Delta V_{GS(\text{th})}/T_J$			2.6		
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	-0.5		-1.1	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$			-1	$\mu\text{A}$
		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$			-10	
On-State Drain Current <sup>a</sup>	$I_{D(\text{on})}$	$V_{DS} \leq -5 \text{ V}, V_{GS} = -10 \text{ V}$	-20			A
		$V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$		0.0030	0.0036	
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(\text{on})}$	$V_{GS} = -4.5 \text{ V}, I_D = -15 \text{ A}$		0.0039	0.0048	$\Omega$
		$V_{GS} = -2.5 \text{ V}, I_D = -10 \text{ A}$		0.0062	0.0085	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -15 \text{ V}, I_D = -20 \text{ A}$		90		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		6600		pF
Output Capacitance	$C_{oss}$			890		
Reverse Transfer Capacitance	$C_{rss}$			930		
Total Gate Charge	$Q_g$	$V_{DS} = -10 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$		150	225	nC
				72	110	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -20 \text{ A}$		12		
Gate-Drain Charge	$Q_{gd}$			19		
Gate Resistance	$R_g$	$f = 1 \text{ MHz}$	0.5	2.6	5.2	$\Omega$
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -10 \text{ V}, R_L = 1 \Omega$ $I_D \equiv -10 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		45	90	ns
Rise Time	$t_r$			45	90	
Turn-Off Delay Time	$t_{d(\text{off})}$			100	200	
Fall Time	$t_f$			35	70	
Turn-On Delay Time	$t_{d(\text{on})}$			13	25	
Rise Time	$t_r$			10	20	
Turn-Off Delay Time	$t_{d(\text{off})}$			110	220	
Fall Time	$t_f$			25	50	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25^\circ\text{C}$			-40 <sup>c</sup>	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				-100	
Body Diode Voltage	$V_{SD}$	$I_F = -10 \text{ A}$		-0.75	-1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = -10 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}, T_J = 25^\circ\text{C}$		30	60	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			17	26	nC
Reverse Recovery Fall Time	$t_a$			15		ns
Reverse Recovery Rise Time	$t_b$			15		

Notes:

a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Package limited.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

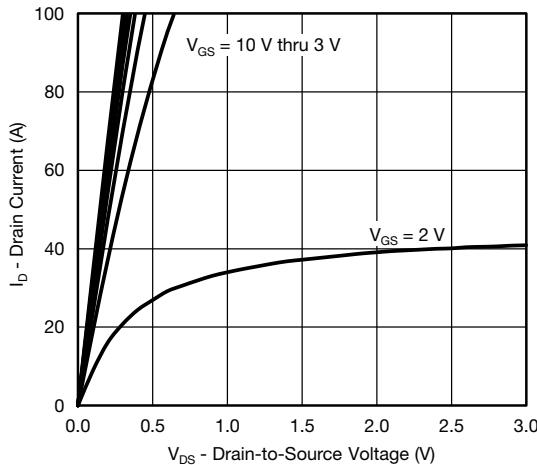


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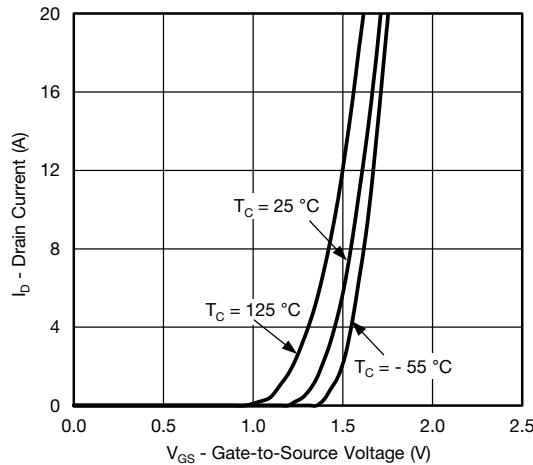
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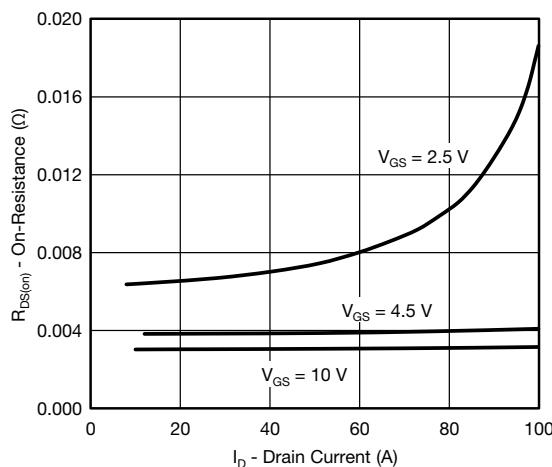
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



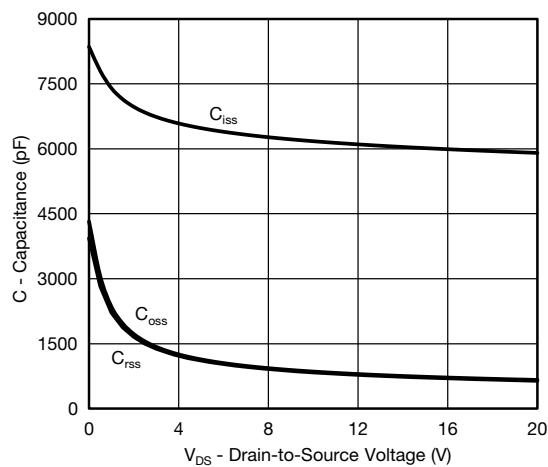
**Output Characteristics**



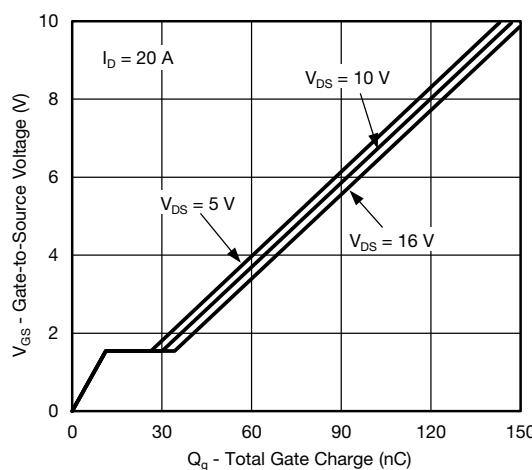
**Transfer Characteristics**



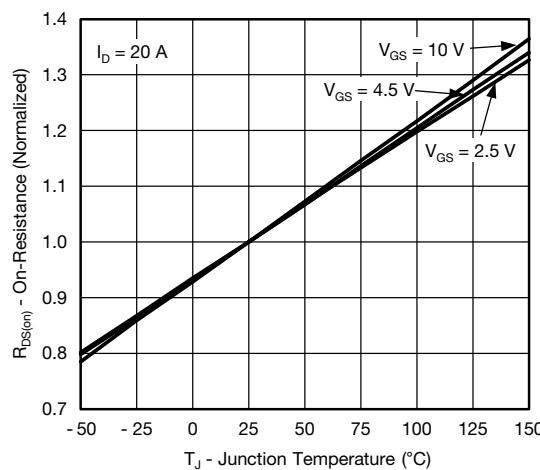
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**



**Gate Charge**



**On-Resistance vs. Junction Temperature**

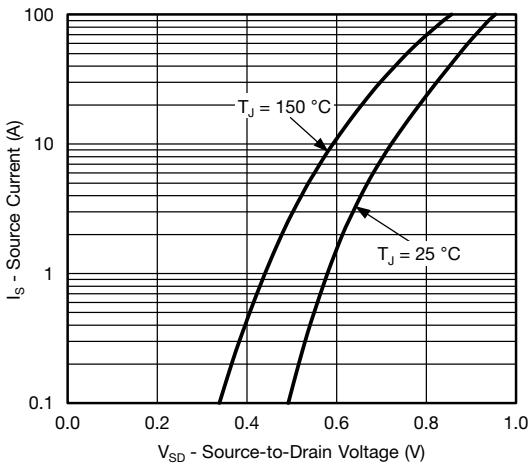
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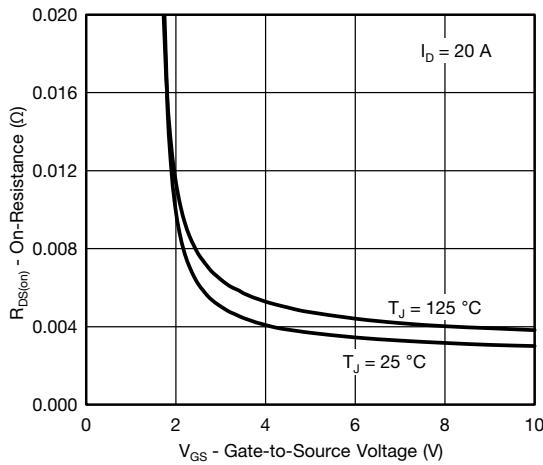
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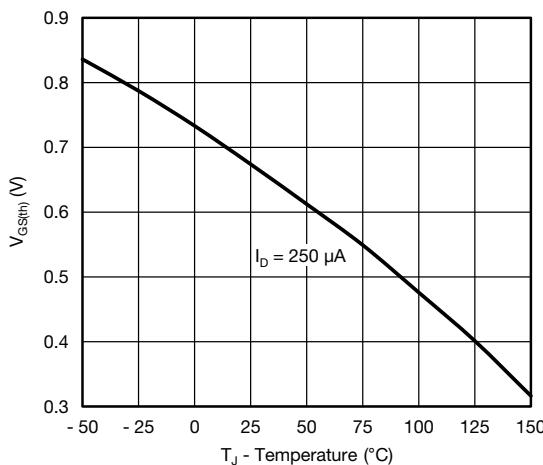
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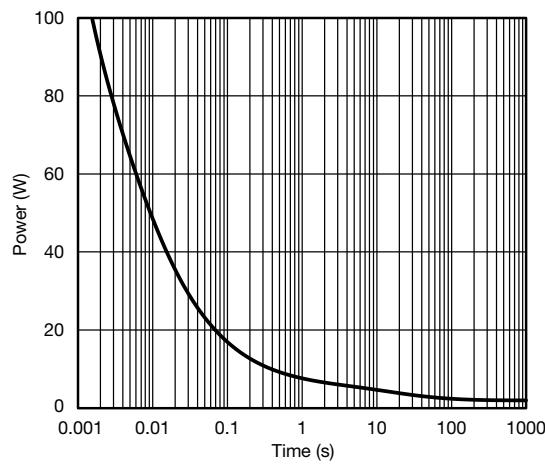
**Source-Drain Diode Forward Voltage**



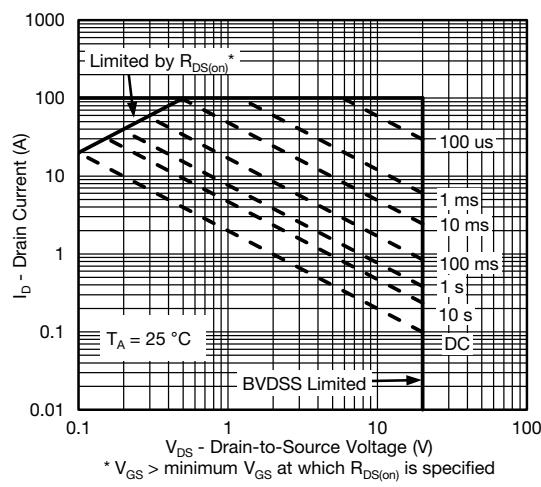
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**



**Single Pulse Power, Junction-to-Ambient**



**Safe Operating Area, Junction-to-Ambient**

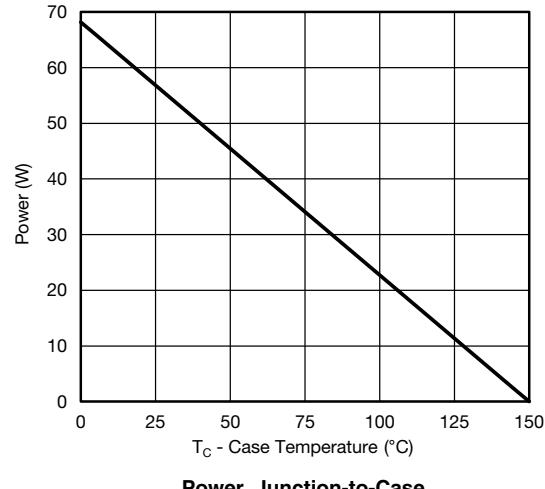
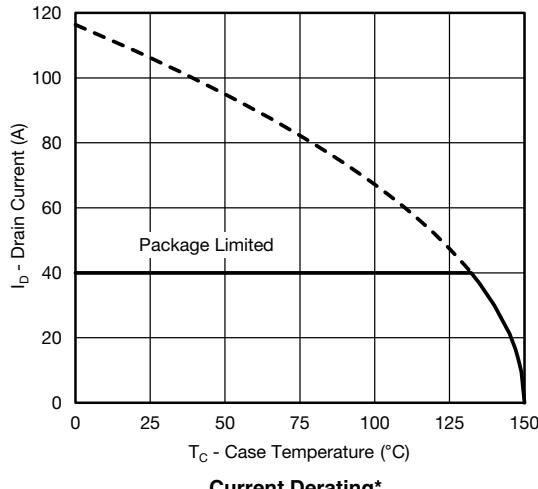


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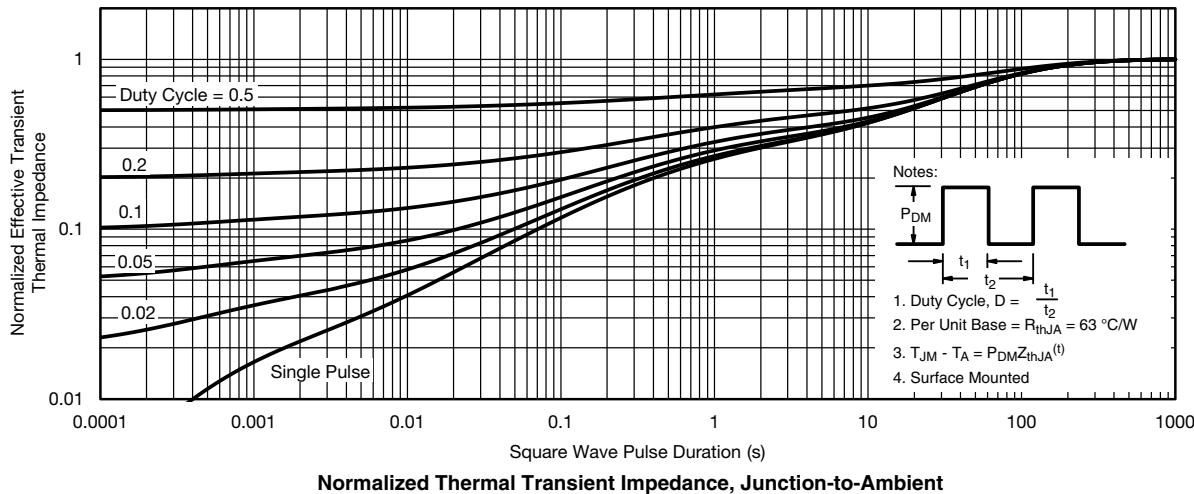
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\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



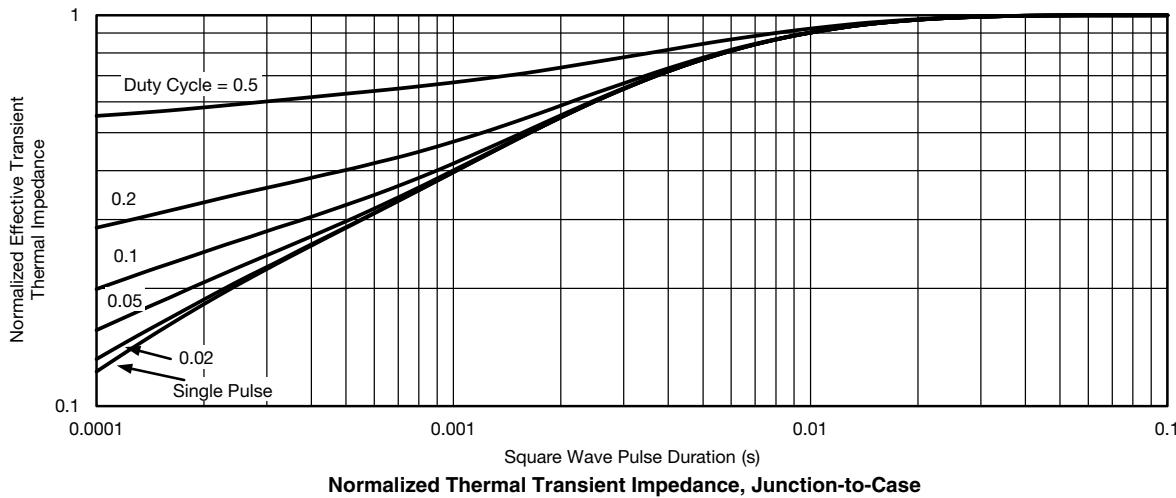
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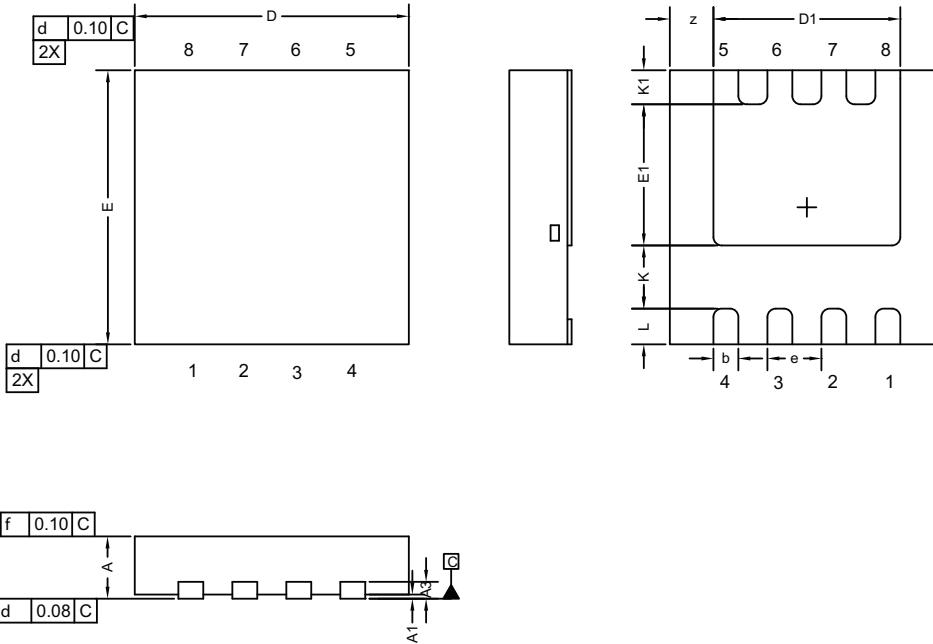


**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see [www.vishay.com/ppg?63617](http://www.vishay.com/ppg?63617).

## Case Outline for PowerPAK® 1212-8S

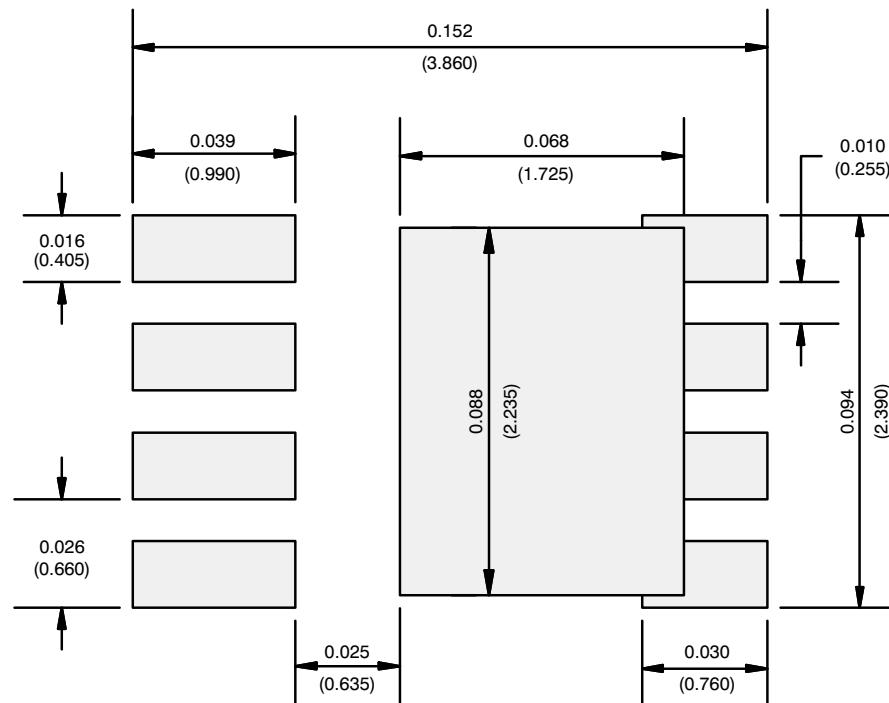


DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.67	0.75	0.83	0.027	0.030	0.033
A1	0	-	0.05	0	-	0.002
A3	0.20 REF			0.008 REF		
b	0.30 BSC			0.012 BSC		
D	3.30 BSC			0.130 BSC		
D1	2.15	2.25	2.35	0.084	0.088	0.092
E	3.30 BSC			0.130 BSC		
E1	1.60	1.70	1.80	0.063	0.067	0.071
e	0.65 BSC			0.026 BSC		
K	0.76 TYP			0.030 TYP		
K1	0.41 TYP			0.016 TYP		
L	0.43 BSC			0.017 BSC		
z	0.525 TYP			0.021 TYP		

ECN: C12-0200-Rev. A, 12-Mar-12  
DWG: 6008

**Note**

- Millimeters will govern.

**RECOMMENDED MINIMUM PADS FOR PowerPAK® 1212-8 Single**Recommended Minimum Pads  
Dimensions in Inches/(mm)[Return to Index](#)



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