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IPDiA 935133426610

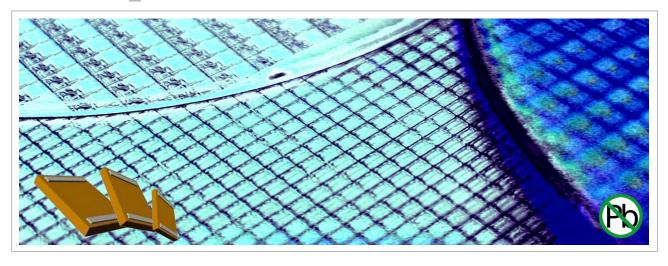
For any questions, you can email us directly: sales@integrated-circuit.com





XTSC426.xxx - 0805 Extreme Temperature Silicon Capacitor

Rev 3.1



Key features

- Ultra High temperature up to 250°C:
 - ◆ Temperature Coeff : <±1.5%(-55 °C to +250°C)</p>
 - Voltage <0.1 %/V
 - Negligible capacitance loss through aging
- Unique high capacitance in EIA/0805 package size, up to 100 nF
- High reliability (FIT <0.017 parts / billion hours)</p>
- Low leakage current down to 100 pA
- Low ESL and Low ESR
- Suitable for lead free reflow-soldering *Please refer to our assembly Application Note for further recommendations

Thanks to the unique IPDiA Silicon capacitor technology, most of the problems encountered in demanding applications can be solved.

EXtreme Temperature Silicon Capacitors are appropriate for applications used in extreme operating temperature range (up to 250°C).

XTSC industry leading performances allow to propose a **100nF** in **0805** with a **TC<±1.5%** over the full -55°C/+250°C temperature range.

This technology also offers a **negligible ageing** and a stable insulation resistance, even at very high temperature, as well as a stable capacitor value over the full operating.

Key applications

- 250°C requirements, High temperature applications, such as military, aerospace, automotive and downhole industries.
- High reliability applications
- Replacement of X8R and C0G dielectrics
- Decoupling / Filtering / Charge pump (i.e.: pressure sensor, motor management)
- Downsizing

The IPDiA technology features a capacitor integration capability (up to 250nF/mm²) which allows a capacitance value similar to X8R dielectric, but with better electrical performances than COG/NPO dielectrics.

This technology also offers **high reliability**, up to 10 times better than alternative capacitor technologies, such as Tantalum or MLCC, and eliminates cracking phenomena.

This Silicon based technology is RoHS compliant and compatible with lead free reflow soldering process.



Distributor of IPDiA: Excellent Integrated System Limited

Datasheet of 935133426610 - CAP SILICON 0.1UF 15% 11V 0805

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

ipdia

XTSC426.xxx

Electrical specification

		Capacitance value						
		10	15	22	33	47	68	
Unit	10 pF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	
	0.1 nF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	
	1 nF	10 nF: 935.133.426.510	Contact IPDIA Sales					
	10 nF	100 nF: 935.133.426.610						

^(*) Thinner thickness (as low as 100 µm thick) available, see Low Profile Silicon Capacitor product: LPSC

<u>Parameters</u>	<u>Value</u>	
Capacitance range	10nF to 100 nF ^(**)	
Capacitance tolerances	±15 % ^(**)	
Operating temperature range	-55 °C to 250 °C	
Storage temperatures	- 70 °C to 265 °C	
Temperature coefficient	<±1.5 %, from -55 °C to +250 °C	
Breakdown voltage (BV)	11 VDC ^(**)	
Capacitance variation versus RVDC	0.1 % /V (from 0 V to RVDC)	
Equivalent Serial Inductor (ESL)	Max 250 pH	
Equivalent Serial Resistor (ESR)	$Max 400mΩ^{(**)}$	
Insulation resistance	50GΩ min @ 3V,25°C 10GΩ min @ 3V,250°C	
Ageing	Negligible, < 0.001 % / 1000 h	
Reliability	FIT<0.017 parts / billion hours,	
Capacitor height	Max 400 μm ^(*)	

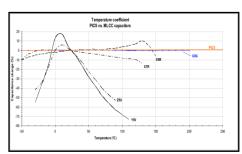


Fig.1 Capacitance change versus temperature variation compared with alternative dielectrics

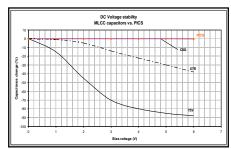


Fig.2 Capacitance change versus voltage variation compared with alternative dielectrics

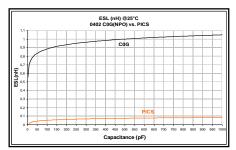
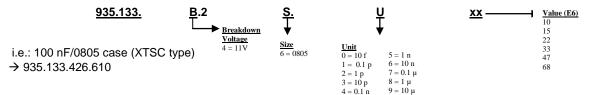


Fig.3 ESL versus capacitance value compared with alternative dielectrics

Part Number



Termination and Outline

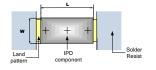
Termination

Lead-free nickel/solder coating compatible with automatic soldering technologies: reflow and manual

Typical dimensions, all dimensions in mm

Package outline

Тур.		0805
Comp.	П	2.20±0.05
size	W	1.40±0.05



(0805 PCB footprint)

Packaging

Tape and reel, tray, waffle pack or wafer delivery

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To contact us, email to: sales@ipdia.com

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^(**) Other values on request.