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Citizen Finedevice CSX750VCB4.096M-UT

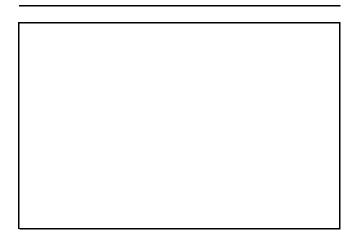
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Distributor of Citizen Finedevice: Excellent Integrated System Limited Datasheet of CSX750VCB4.096M-UT - OSC VCXO 4.096MHZ CMOS SMD Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

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SPECIFICATION

PRODUCT NAME: VOLTAGE CONTROLLED CRYSTAL OSCILLATOR

TYPE:

CSX-750V

FREQUENCY:

MHz

PARTS NO.:

CITIZEN WATCH CO., LTD. 1-12, Honcho 6-chome, Tanashi-shi, Tokyo 188-8511 Japan

Oscillator Technical section	APPROVED	CHECKED	PREPARED
Crystal Devices Div. Telephone: 0424-68-4572			
Fax : 0424-68-4666			
			1
PRODUCTS MARKETING GROUP Telephone: 0424-67-6214			
Telex:2822-471/ Fax: 0424-67-8503			



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. SCOPE

This specification relates to the voltage controlled crystal oscillator to be supplied by CITIZEN WATCH CO., LTD. (following as CITIZEN) .

NOTICE

- If something that is ambiguously defined or undefined in this specification happened, the customer and CITIZEN would discuss and take necessary steps by mutual consent.
 Product test data can't be attached to this specification.
- 3. This product is not authorized for use as critical component in life support devices or systems.

II. SPECIFICATION

1. ABSOLUTE MAXIMUM RATING

Parameter		CSX750VB/CSX750VC
Supply Voltage	Vmax	-0.5V to +7.0V
Storage Temperature	Tstg	-45°C to +90°C
Output Current	lout	10mA Max.
Input Control Voltage	Vc_m	-0.5V to Vdd +0.5V
Solder Heat Resistance	Tsol	Max.240°C x Max.10 seconds x 2times
Of The Outer Lead		Max.200°C x Max. 3 minutes

2. OPERATING RANGE

Parameter		CSX750VB	CSX750VC
Supply Voltage	Vdd	3.3V±5%	5.0V±10%
Operating Temperature	Topr	-10°C to 70°C or -40°C to 85°C	
Input Control Voltage	Vc	0.0V to Vdd	
Output Load	CL	30pF Max.	

3. FREQUENCY CHARACTERISTICS

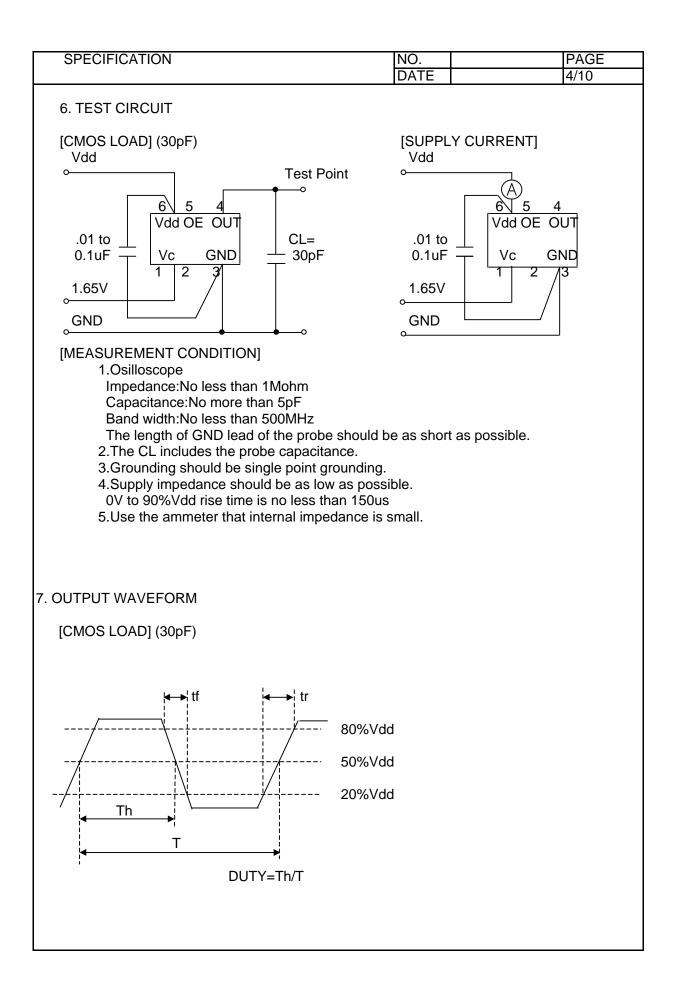
Parameter		CSX750VB	CSX750VC	
Stability (note1)	dF0	±50ppm Max.		
Pullability (note2)	Fpull	±90ppm Min.	±100ppm Min.	
Linearity	Ldev	±15% Max.	±10% Max.	
Modulation Band Width	Fmod	10kH:	10kHz Min.	

note1) Frequency Stability includes initial tolerance, temperature characteristics, input voltage characteristics, load characteristics, shock, vibration, reflow and 1st year aging. note2) Vc=1.65V±1.65V (CSX750VB) Vc=2.5V±2.0V (CSX750VC)



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4. ELECTRICAL CHARACTERISTICS (Ta=25°C load=30pF Vc=Vdd/2)					
Parameter		Conditions	CSX7	750VB	CSX750VC
Start Up Time (no	te) tosc			4msec Max.	
Power Supply Cu	rrent Idd	No Load	11mA	Max.	30mA Max.
Disable Current	linh	No Load	5mA	Max.	20mA Max.
Rise Time	tr	20% to 80%Vdo	ł	5ns	Max.
Fall Time	tf	80% to 20%Vdo	ł	5ns	Max.
Duty Cycle	duty	50%Vdd		45% t	o 55%
Output HIGH Volta	age Voh	loh = -0.8mA		Vdd-0.4	4V Min.
Output LOW Volta	age Vol	lol = 3.2mA		0.4V	Max.
OE Input HIGH Vo	oltage Vih			Vdd x (0.7 Min.
OE Input LOW Vo	ltage Vil			Vdd x 0).3 Max.
Output Disable Ti	me tpxz	See 5.		100ns Max.	
Output Enable Tir	ne tpzx			100ns	s Max.
OE Input HIGH or OPEN	Clock Outpu Active	ut enable			
LOW	High impedar	nce disable			
Propagation delay time between tpxz:OE(HIGH to LOW) and OUTPUT(active level to high impedance) Clock Output OE input					
NOTE: A disable clock output does not synchronize with OE, because internal quartz oscillator is continuous.					





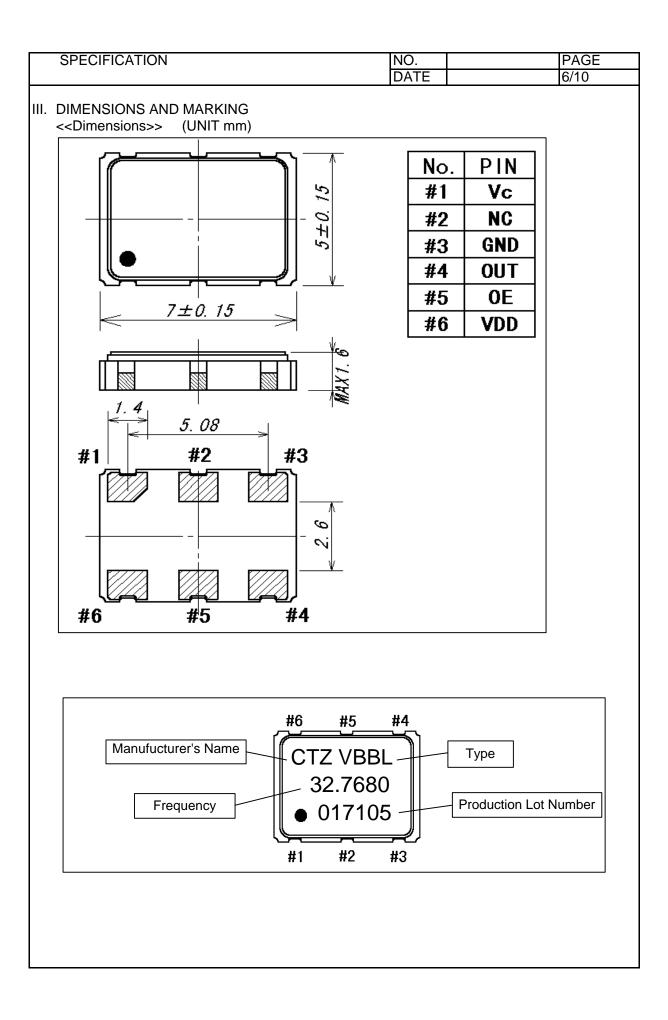


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8. ENVIRONMENTAL AND MECHANICAL CHARACTERISTICS The following are our reliability test conditions.				
Item	Conditions			
Shock	MIL-STD-883E 2	2002.3B		

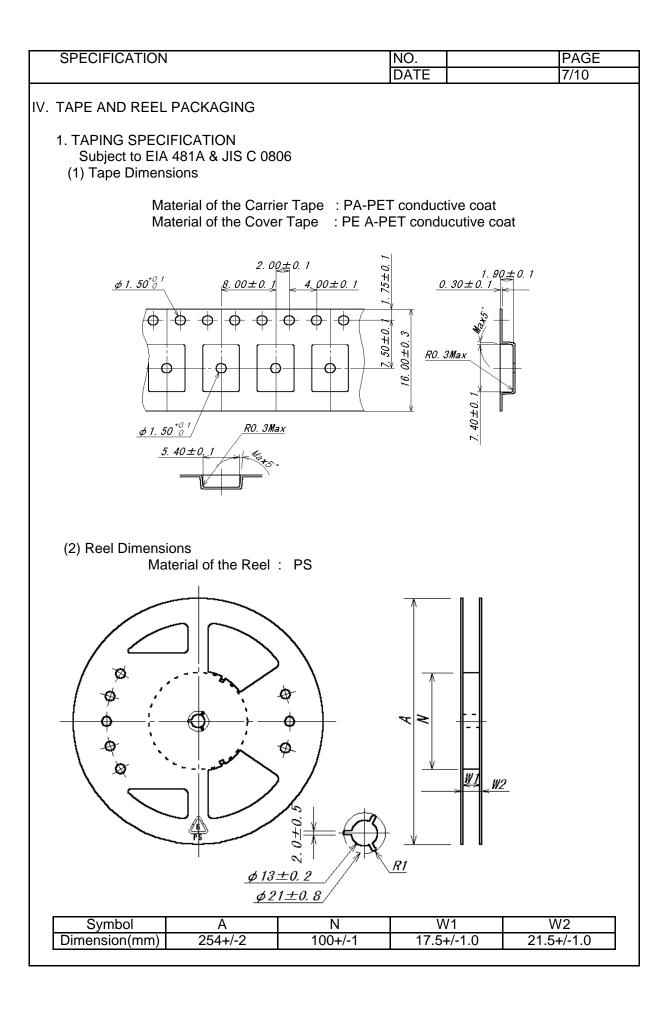
Onock	
Vibration	MIL-STD-883E 2007.2A
Gross Leak	Leak rate less than 50ppm atm cc /sec of Air
Fine Leak	Leak rate less than 0.01ppm atm cc /sec of Herium



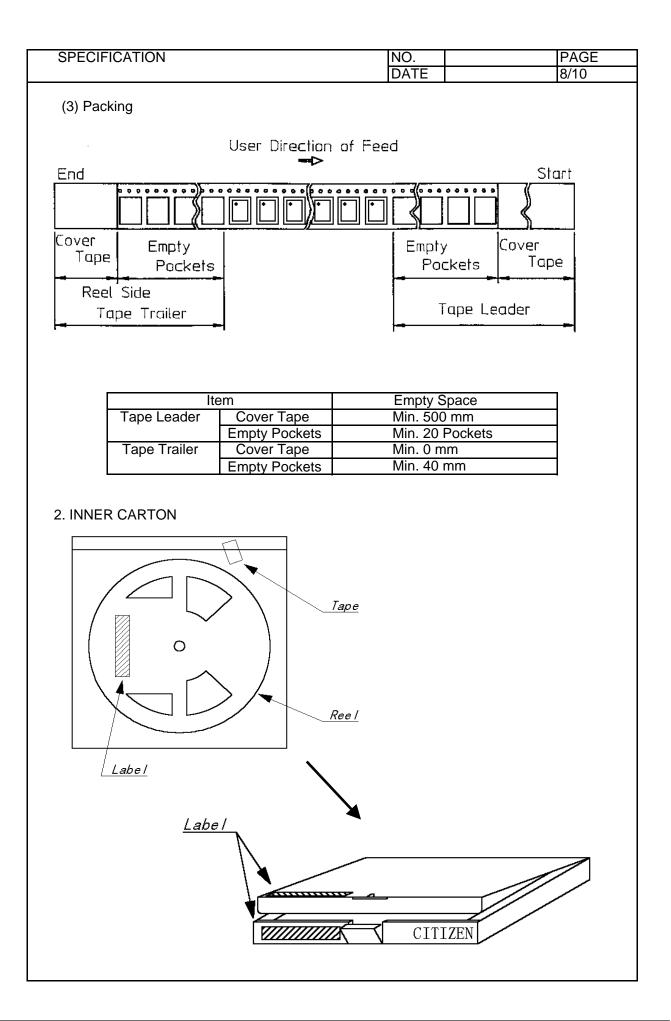
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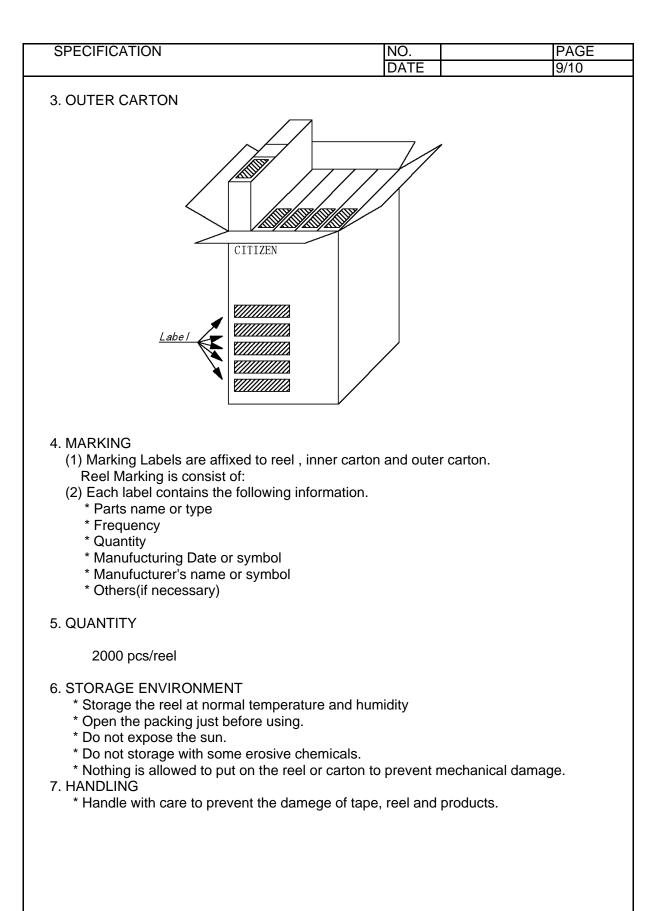














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V.	NOTES 1. HANDLING (ELECTROSTATIC DISCHARGES) This device is made with CMOS circuitry. Please take precautions to prevent damage due to electrical static discharge.				
	(SHOCK RELIABILITY) This device contains a quartz crystal, so please do not give too much shock or vibration. An automatic insersion is available, however, the internal quartz crystal might be damaged in case that too much shock or vibration is given by machine condition. Be sure to check your machine condition in advance.				
	(CLEANING) Since, depending on the cleaning conditions, there to the Crystal Osillator, do not fail to test and confir company's cleaning conditions.				
	(TEMPERATURE AND HUMIDITY) We recomend to store and use device under norm When this device is used in high humidity application condensation. As with other IC's, please take precautions to prev	ons, there is	s a potential probl	em with	
	 2. CIRCUIT DESIGNS (POWER LINES) We recomend placing a 0.01 to 0.1uF capacitor between VDD and GND to obtain stable operation and protect against power line ripple. VDD and GND pattern should be as wide as possible. 				
	(OE INPUT LINE) When OE pin is not used, please connect it to VDD.				
(OUTPUT LINE) As a long output line may cause irregular output, please take care to design that is as short as possible, and also keep high level signal source away from this dev					
	(STARTING UP) Vc must be kept ground level or left open when sta	arting up.			