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PMF87EN

30 V, single N-channel Trench MOSFET 1 August 2012

Product data sheet

1. Product profile

1.1 General description

N-channel enhancement mode Field-Effect Transistor (FET) in a SOT323 (SC-70) small Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Logic-level compatible
- Very fast switching
- Trench MOSFET technology

1.3 Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1. Quie	ck reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V	
V _{GS}	gate-source voltage	-		-20	-	20	V	
I _D	drain current	V_{GS} = 10 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	1.9	А	
Static characteristics								
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 1.7 A; T _j = 25 °C		-	67	80	mΩ	

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².







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2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain		G
			SC-70 (SOT323)	017aaa253

3. Ordering information

Table 3. Ordering information							
Type number Package							
	Name	Description	Version				
PMF87EN	SC-70	plastic surface-mounted package; 3 leads	SOT323				

4. Marking

Table 4. Marking codes						
Type number	Marking code					
	[1]					
PMF87EN	VA%					

[1] % = placeholder for manufacturing site code

5. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V_{GS} = 10 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	1.9	А
		V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	1.7	А
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	1.1	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	6.8	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	275	mW
			[1]	-	355	mW
		T _{sp} = 25 °C		-	1810	mW



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Symbol	Parameter	Conditions		Min	Max	Unit	
Tj	junction temperature			-55	150	°C	
T _{amb}	ambient temperature			-55	150	°C	
T _{stg}	storage temperature			-65	150	°C	
Source-drain diode							
I _S	source current	T _{amb} = 25 °C	[1]	-	0.7	А	

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

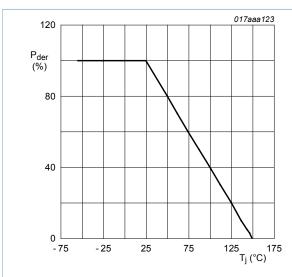
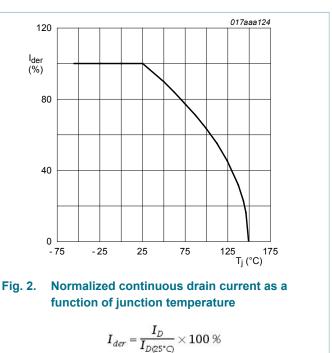


Fig. 1. Normalized total power dissipation as a function of junction temperature

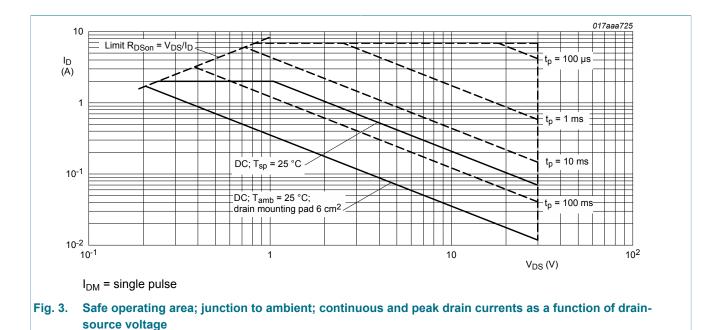
$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$





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6. Thermal characteristics

Table 6. Thermal characteristics								
Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	394	453	K/W	
			[2]	-	308	354	K/W	
			[3]	-	263	302	K/W	
R _{th(j-sp)}	thermal resistance from junction to solder point			-	60	70	K/W	

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

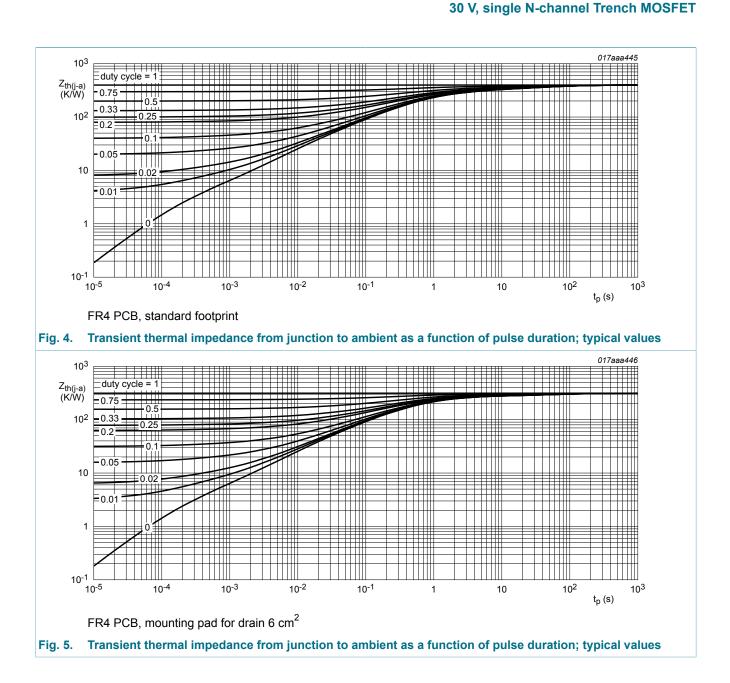
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm², $t \le 5$ s.



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7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics	· · ·	, i			-
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C	30	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} = V _{GS} ; T _j = 25 °C	1	1.5	2.5	V
I _{DSS}	drain leakage current	V_{DS} = 30 V; V_{GS} = 0 V; T_j = 25 °C	-	-	1	μA
		V _{DS} = 30 V; V _{GS} = 0 V; T _j = 150 °C	-	-	10	μA

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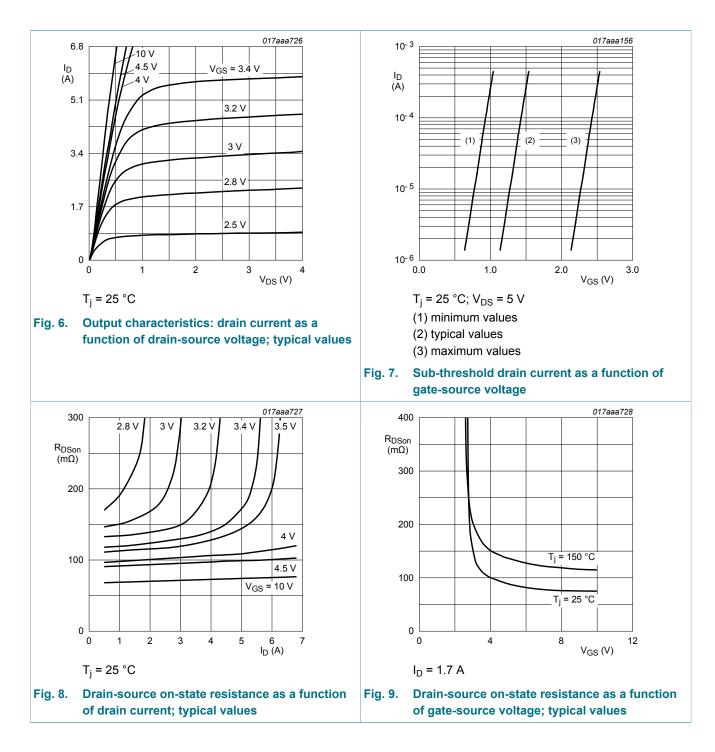
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{GSS}	gate leakage current	V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
		V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
R _{DSon}	drain-source on-state	V _{GS} = 10 V; I _D = 1.7 A; T _j = 25 °C	-	67	80	mΩ
	resistance	V _{GS} = 10 V; I _D = 1.7 A; T _j = 150 °C	-	101	120	mΩ
		V _{GS} = 4.5 V; I _D = 1.4 A; T _j = 25 °C	-	87	110	mΩ
9fs	forward transconductance	V _{DS} = 10 V; I _D = 1.7 A; T _j = 25 °C	-	5.8	-	S
Dynamic cl	haracteristics		1			
Q _{G(tot)}	total gate charge	V_{DS} = 15 V; I _D = 1.7 A; V _{GS} = 10 V; T _j = 25 °C	-	3.1	4.7	nC
Q _{GS}	gate-source charge		-	0.46	-	nC
Q _{GD}	gate-drain charge	_	-	0.42	-	nC
C _{iss}	input capacitance	V_{DS} = 15 V; f = 1 MHz; V_{GS} = 0 V;	-	135	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	33	-	pF
C _{rss}	reverse transfer capacitance		-	14	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 15 V; I_{D} = 1.7 A; V_{GS} = 10 V;	-	3	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	14	-	ns
t _{d(off)}	turn-off delay time		-	15	-	ns
t _f	fall time		-	6	-	ns
Source-dra	in diode		I			
V _{SD}	source-drain voltage	I _S = 0.7 A; V _{GS} = 0 V; T _j = 25 °C	-	0.8	1.2	V



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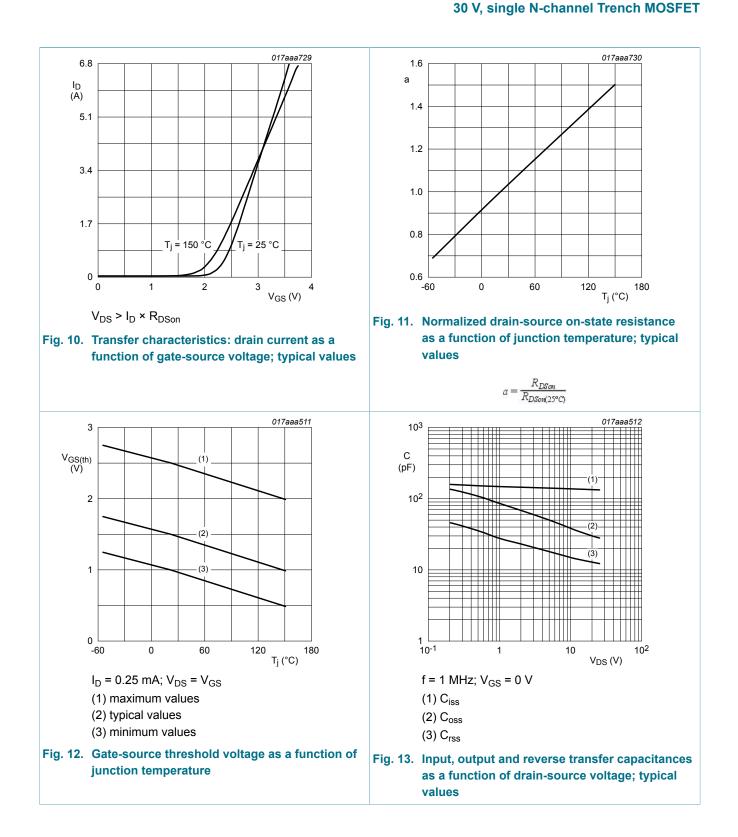
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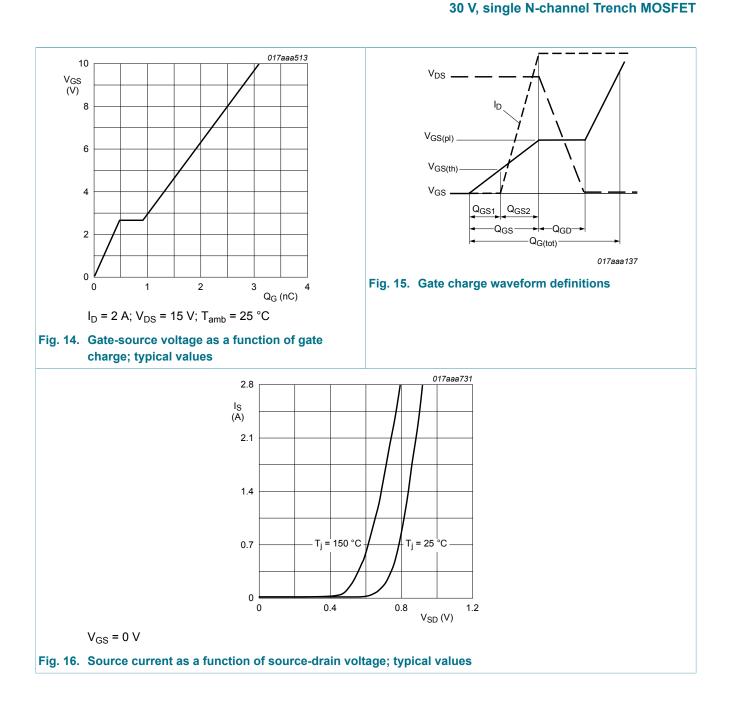
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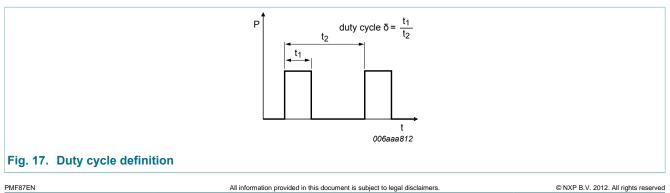


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Test information 8.



Product data sheet

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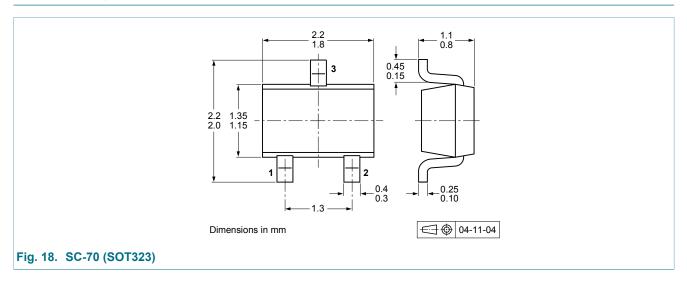


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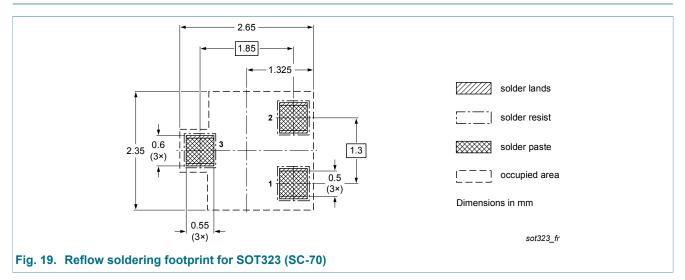
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9. Package outline



10. Soldering

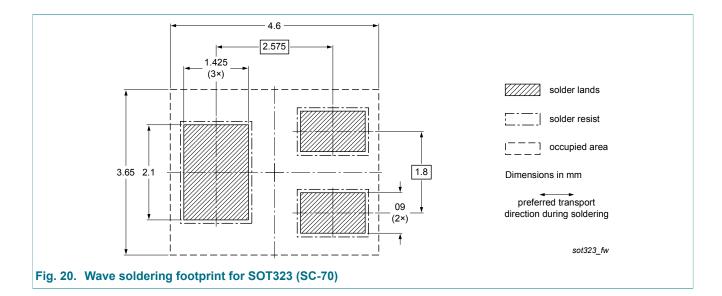




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11. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMF87EN v.1	20120801	Product data sheet	-	-



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12. Legal information

12.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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