

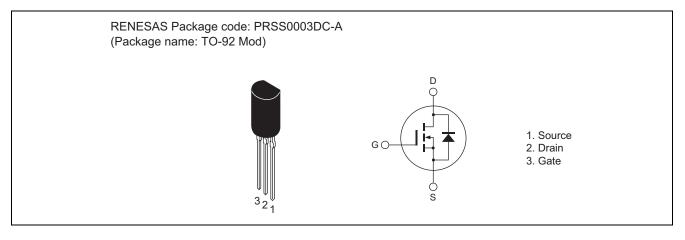
RJK6002DJE

600V - 2A - MOS FET High Speed Power Switching R07DS0845EJ0100 Rev.1.00 Jul 05, 2011

Features

- Low on-resistance
- $R_{DS(on)} = 5.7 \ \Omega \ typ.$ (at $I_D = 1 \ A$, $V_{GS} = 10 \ V$, $Ta = 25^{\circ}C$)
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

| Item | Symbol | Ratings | $\frac{(Ta = 25^{\circ}C)}{Unit}$ |
|---|----------------------------------|-------------|-----------------------------------|
| Drain to source voltage | V _{DSS} | 600 | V |
| Gate to source voltage | V _{GSS} | ±30 | V |
| Drain current | I _D ^{Note1} | 2 | А |
| Drain peak current | Note3 D (pulse) | 4 | А |
| Body-drain diode reverse drain current | I _{DR} ^{Note1} | 2 | А |
| Body-drain diode reverse drain peak current | Note3 IDR (pulse) | 4 | А |
| Channel dissipation | Pch Note2 | 0.9 | W |
| Channel to ambient thermal impedance | θch-a | 139 | °C/W |
| Channel temperature | Tch | 150 | °C |
| Storage temperature | Tstg | -55 to +150 | °C |

Notes: 1. Limited by Tch max.

2. Value at Tc = 25° C

3. Pulse width limited by safe operating area.



Electrical Characteristics

 $(Ta = 25^{\circ}C)$

| Item | Symbol | Min | Тур | Max | Unit | Test conditions |
|--|----------------------|-----|-----|------|------|---|
| Drain to source breakdown voltage | V _{(BR)DSS} | 600 | — | _ | V | $I_D = 10 \text{ mA}, V_{GS} = 0$ |
| Zero gate voltage drain current | I _{DSS} | _ | _ | 1 | μA | $V_{DS} = 600 \text{ V}, V_{GS} = 0$ |
| Gate to source leak current | I _{GSS} | _ | — | ±0.1 | μA | $V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$ |
| Gate to source cutoff voltage | V _{GS(off)} | 3.0 | — | 4.5 | V | $V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$ |
| Static drain to source on state resistance | R _{DS(on)} | _ | 5.7 | 6.8 | Ω | $I_D = 1 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$ |
| Input capacitance | Ciss | _ | 165 | | pF | V _{DS} = 25 V |
| Output capacitance | Coss | _ | 20 | _ | pF | V _{GS} = 0 f = 1 MHz |
| Reverse transfer capacitance | Crss | _ | 2.5 | _ | pF | |
| Turn-on delay time | t _{d(on)} | _ | 28 | _ | ns | $I_D = 1 A$ $V_{GS} = 10 V$ $R_L = 300 \Omega$ $Rg = 10 \Omega$ |
| Rise time | tr | _ | 17 | _ | ns | |
| Turn-off delay time | t _{d(off)} | _ | 47 | _ | ns | |
| Fall time | t _f | _ | 20 | _ | ns | |
| Body-drain diode forward voltage | V _{DF} | — | 0.9 | 1.5 | V | $I_F = 2 \text{ A}, V_{GS} = 0^{Note4}$ |
| Body-drain diode reverse recovery time | t _{rr} | _ | 260 | — | ns | $I_F = 2 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$ |

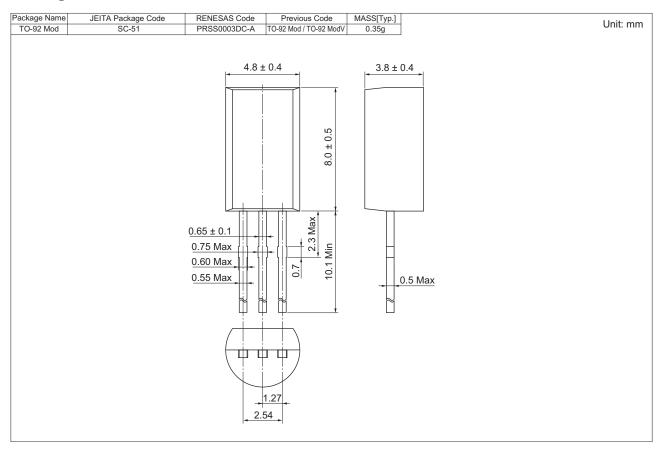
Notes: 4. Pulse test

5. Since this device is equipped with high voltage FET chip ($V_{DSS} \ge 600 \text{ V}$), high voltage may be supplied. Therefore, please be sure to confirm about electric discharge between drain terminal and other terminal.

This device is sensitive to electrostatic discharge.
 It is recommended to adopt appropriate cautions when handling this product.

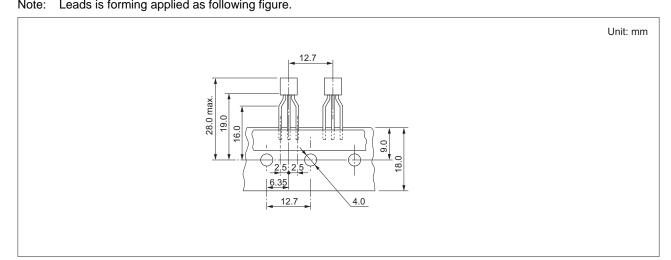


Package Dimensions



Ordering Information

| Quantity | Shipping Container |
|----------|-------------------------|
| | Hold Box, Radial Taping |
| | |



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