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Diodes Incorporated 74AHCU04S14-13

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Pin Assignments



74AHCU04

UNBUFFERED HEX INVERTERS

Description

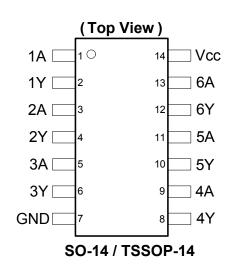
The 74AHCU04 provides provides six independent unbuffered inverters with standard push-pull outputs. The device is designed for operation with a power supply range of 2.0V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment.

The gates perform the Boolean function:

Y = A

Features

- Wide Supply Voltage Range from 2.0V to 5.5V
- Sinks or Sources 8mA at V_{CC} = 4.5V
- CMOS Low Power Consumption
- Schmitt Trigger Action at All Inputs
- Inputs can be driven by 3.3V or 5.5V allowing for voltage translation applications.
- ESD Protection Exceeds JESD 22
 - 200-V Machine Model (A115-A)
 - 2000-V Human Body Model (A114-A)
 - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 250mA per JESD 78, Class II
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)



Applications

- Suited for use as an inverter in a crystal oscillator
- General Purpose Logic
- Wide array of products such as:
 - PCs, Networking, Notebooks, Netbooks
 - Computer Peripherals, Hard Drives, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

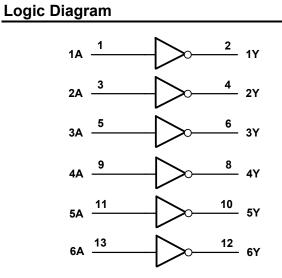
Click here for ordering information, located at the end of datasheet





Pin Descriptions

Pin Number	Pin Name	Function
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	V _{CC}	Supply Voltage



Function Table

Input	Output
Α	Y
L	Н
Н	L

Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM Human Body Model ESD Protection		2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range	-0.5 to +7.0	V
I _{IK}	Input Clamp Current VI < -0.5V	-20	mA
I _{OK}	Output Clamp Current V _O < -0.5V	-20	mA
I _{OK}	Output Clamp Current $V_O > V_{CC} + 0.5V$	25	mA
lo	Continuous Output Current -0.5V < V _O V _{CC} +0.5V	+/- 25	mA
Icc	Continuous Current Through V _{CC}	75	mA
I _{GND}	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG} Storage Temperature		-65 to +150	°C
Ртот	Total Power Dissipation	500	mW

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.





Recommended Operating Conditions (Note 5) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage		2.0	5.5	V
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	Vcc	V
A 4/ A \ /		V _{CC} = 3.0V to 3.6V		100	
Δt/ΔV	Input Transition Rise or Fall Rate	V_{CC} = 4.5V to 5.5V		20	ns/V
TA	Operating Free-Air Temperature		-40	+125	°C

Note: 5. Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

0	Descrite	Test Oscilla		T _A = -40°0	C to +85°C	T _A = -40°C	to +125°C	11
Symbol Parameter	Test Conditions	Vcc	Min	Max	Min	Max	Unit	
			2.0V	1.7		1.7		
VIH	High-Level Input		3.0V	2.4		2.4		V
	Voltage		5.5V	4.4		4.4		
			2.0V		0.3		0.3	
VIL	Low-Level input		3.0 V		0.6		0.6	V
	Voltage		5.5V		1.1		1.1	
	High-Level Output	I _{OH} = -50μA	2.0V	1.8		1.8		v
		I _{OH} = -50μA	3.0V	2.7		2.7		
V _{OH}		I _{OH} = -50μA	4.5V	4.0		4.0		
	Voltage	I _{OH} = -4mA	3.0V	2.48		2.40		
		I _{OH} = -8mA	4.5V	3.80		3.70		
		I _{OL} = 50μA	2.0V		0.2		0.2	
		I _{OL} = 50μA	3.0V		0.3		0.3	
V _{OL}	Low-Level Output Voltage	I _{OL} = 50μA	4.5V		0.5		0.5	V
		I _{OL} = 4mA	3.0V		0.44		0.55	1
		I _{OL} = 8mA	4.5V		0.44		0.55]
h	Input Current	V_1 = GND to 5.5V	3.6V		±1		±2	μA
Icc	Supply Current	$V_{I} = GND \text{ or } V_{CC, I_{O}} = 0$	3.6V		20		40	μA

Operating Characteristics

Parameter		Test Conditions	V _{CC} = 2.0V Typ	V _{CC} = 3.3V Typ	V _{CC} = 5V Typ	Unit
C _{pd}	Power Dissipation Capacitance per Gate	f = 1MHz	7.9	8.3	9.1	pF
Ci	Input Capacitance	$V_i = V_{CC} - or GND$	4.0	4.0	4.0	pF

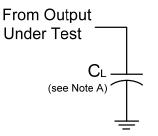




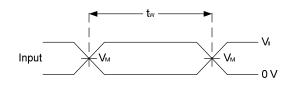
Switching Characteristics

Symbol	Devementer	Test	Mar	-	Γ _A = +25°C	•	-40°C to	o +85°C	-40°C to	+125°C	11
Symbol	Parameter	Conditions	Vcc	Min	Тур	Max	Min	Мах	Min	Max	Unit
		Figure 1	3.0V to 3.6V	0.5	3.0	7.1	0.5	8.0	0.5	9.0	
	Propagation	C _L = 15pF	4.5V to 5.5V	0.5	2.4	5.5	0.5	6.5	0.5	7.0	
t _{PD}	Delay A_N to Y_N	Figure 1	3.0V to 3.6V	0.5	3.4	10.6	0.5	12.0	0.5	13.5	ns
		C _L = 50pF	4.5V to 5.5V	0.5	3.5	7.0	0.5	8.0	0.5	9.0	

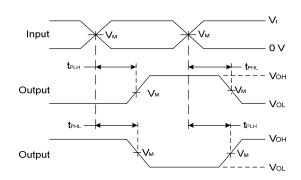
Parameter Measurement Information



Mar	Inputs		N.	0
Vcc	VI	t _r /t _f	V _M	
3.3V to -3.6V	V _{CC}	3ns	V _{CC} /2	15pF, 50pF
4.5V to 5.5V	V _{CC}	3ns	V _{CC} /2	15pF, 50pF



Voltage Waveform Pulse Duration



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1 Load Circuit and Voltage Waveforms

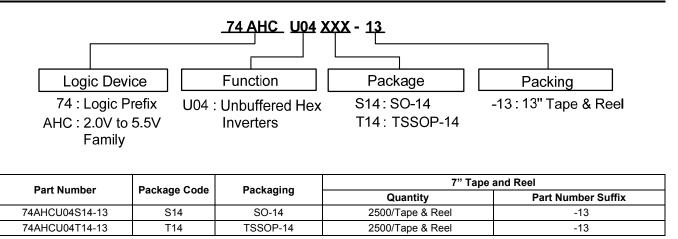
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as $t_{\text{PD}}.$



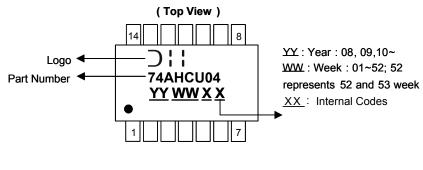


Ordering Information



Marking Information

(1) SO-14, TSSOP-14



Part Num	ber	Package
74AHCU04	S14	SO-14
74AHCU04	T14	TSSOP-14

Pb,

Pb,

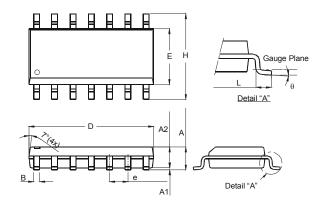




Package Outline Dimensions (All dimensions in mm.)

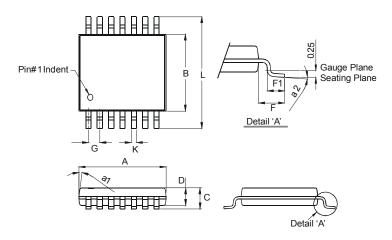
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14				
Dim	Min	Max			
Α	1.47	1.73			
A1	0.10	0.25			
A2	1.45 Typ				
В	0.33	0.51			
D	8.53	8.74			
E	3.80	3.99			
е	1.27	Тур			
Н	5.80	6.20			
L	0.38	1.27			
θ	0°	8°			
All Di	mensions	s in mm			

Package Type: TSSOP-14



	TSSOP-1	14			
Dim	Min	Max			
a1	7° (4X)				
a2	0°	8°			
Α	4.9	5.10			
в	4.30	4.50			
С	_	1.2			
D	0.8	1.05			
F	1.00	Тур			
F1	0.45	0.75			
G	0.65	5 Тур			
Κ	0.19	0.30			
L	L 6.40 Typ				
All Dir	mension	s in mm			

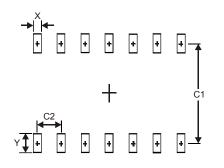




Suggested Pad Layout

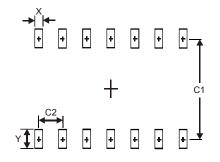
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65





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