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Stocking Distributor

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[Diodes Incorporated](#)
[SBR15U30SP5-13](#)

For any questions, you can email us directly:

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SBR15U30SP5

15A SBR[®]
SUPER BARRIER RECTIFIER[®]
POWERDI[®]

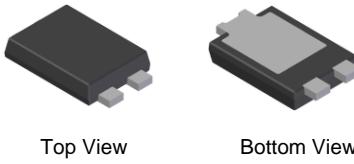
Features

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for +200°C Maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier Technology
- High Forward Surge Capability
- Ultra Low Forward Voltage Drop
- Excellent High Temperature Stability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: POWERDI[®]5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208(e3)
- Weight: 0.093 grams (Approximate)

POWERDI[®]5



LEFT PIN o → BOTTOMSIDE
 RIGHT PIN o → o HEAT SINK

Note: Pins Left & Right must
 be electrically connected
 at the printed circuit board.

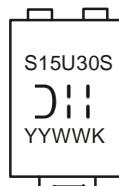
Ordering Information (Note 4)

| Part Number | Case | Packaging |
|--------------------------|------------------------|-------------------|
| SBR15U30SP5-13 | POWERDI [®] 5 | 5,000/Tape & Reel |
| SBR15U30SP5-13D (Note 5) | POWERDI [®] 5 | 5,000/Tape & Reel |

Notes:

- EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.
- "D" suffix designate for the 12mm Tape and Reel option.

Marking Information



S15U30S = Product Type Marking Code

Д11 = Manufacturers' Code Marking

K = Factory Designator

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 15 for 2015)

WW = Week Code (01 - 53)



SBR15U30SP5

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|---|---------------------|-------|------|
| Peak Repetitive Reverse Voltage | V_{RRM} | | |
| Working Peak Reverse Voltage | V_{RWM} | 30 | V |
| DC Blocking Voltage | V_{RM} | | |
| RMS Reverse Voltage | $V_{R(\text{RMS})}$ | 21 | V |
| Average Rectified Output Current | I_O | 15 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I_{FSM} | 280 | A |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|-------------------------|-------------|---------------------------|
| Maximum Thermal Resistance Thermal Resistance Junction to Ambient (Note 6) | $R_{\theta JA}$ | 26 | $^\circ\text{C}/\text{W}$ |
| Operating Temperature Range | $V_R \leq 80\% V_{RRM}$ | -65 to +150 | $^\circ\text{C}$ |
| | $V_R \leq 50\% V_{RRM}$ | ≤ 180 | |
| | DC Forward Mode | ≤ 200 | |
| Storage Temperature Range | T_{STG} | -65 to +175 | $^\circ\text{C}$ |

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

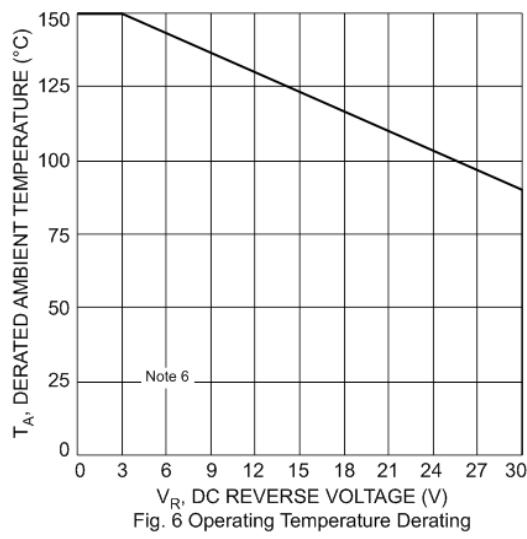
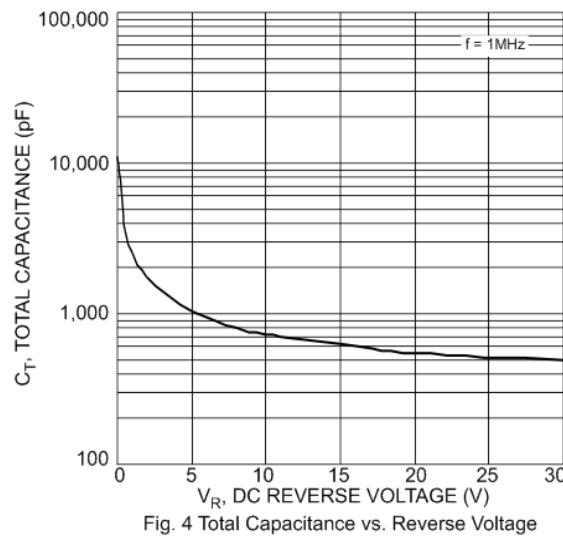
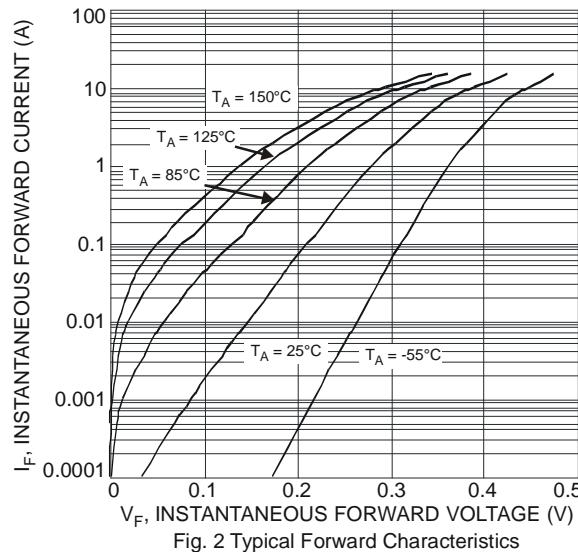
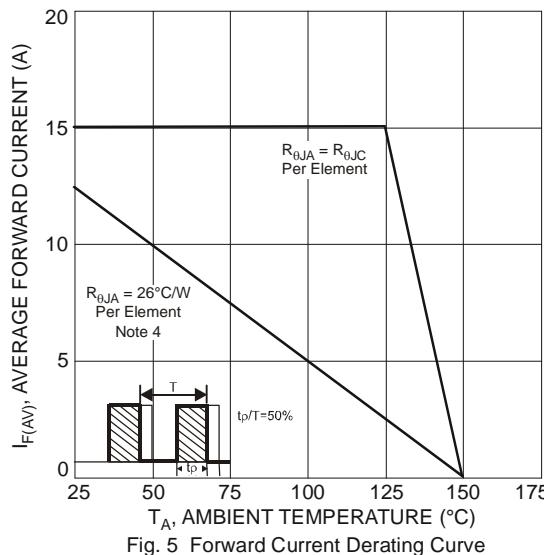
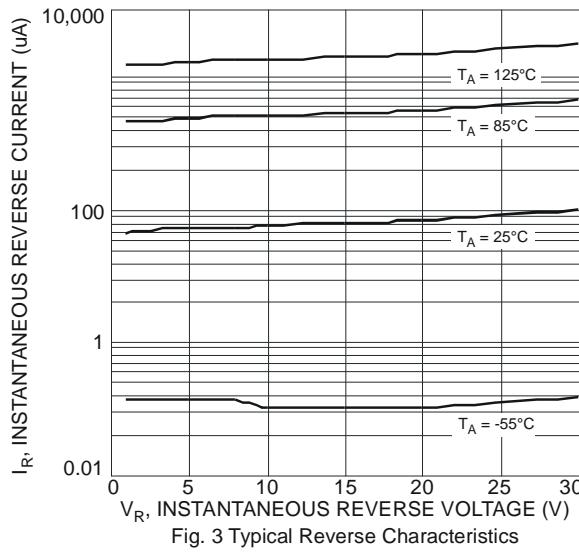
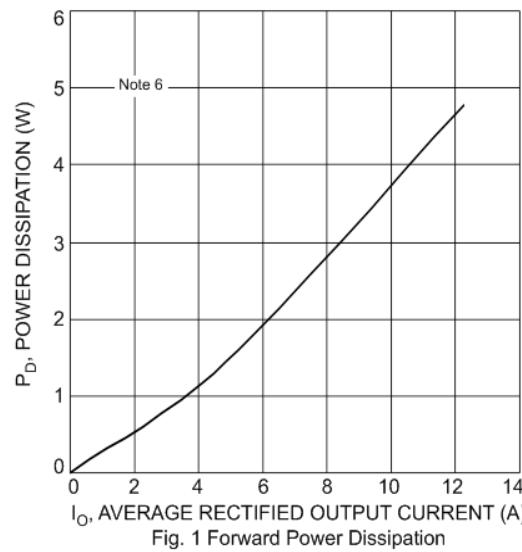
| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--------------------------------|--------|-----|-----|--------------|------|---|
| Forward Voltage Drop (Per Leg) | V_F | — | — | 0.49 0.42 | V | $I_F = 15\text{A}, T_J = +25^\circ\text{C}$ $I_F = 15\text{A}, T_J = +125^\circ\text{C}$ |
| Leakage Current (Note 7) | I_R | — | — | 0.5 100 | mA | $V_R = 30\text{V}, T_J = +25^\circ\text{C}$ $V_R = 30\text{V}, T_J = +125^\circ\text{C}$ |

Notes: 6. Polymide, 2oz. Copper 16x minimum recommended pad layout per <http://www.diodes.com>

7. Short duration pulse test used to minimize self-heating effect.



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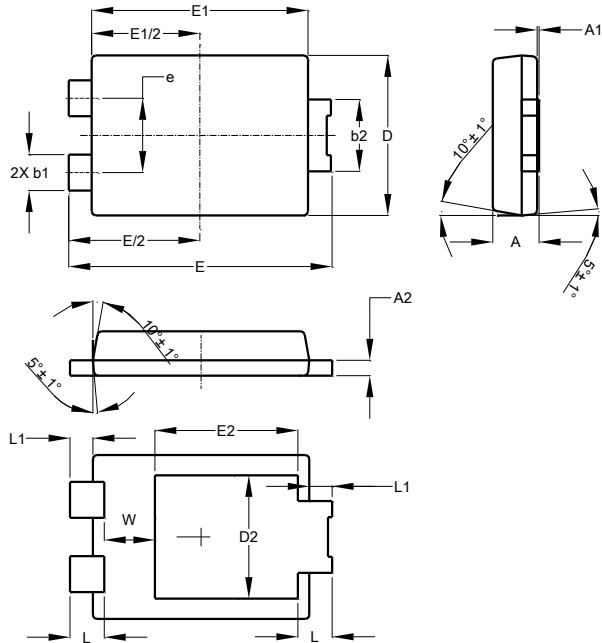




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Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

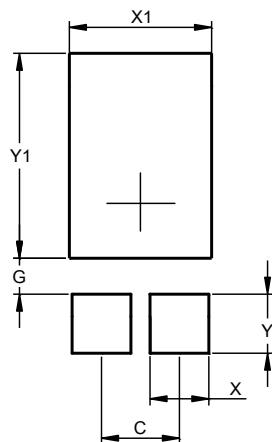


| POWERDI [®] 5 | | | |
|------------------------|------|------|-------|
| Dim | Min | Max | Typ |
| A | 1.05 | 1.15 | 1.10 |
| A1 | 0.00 | 0.05 | -- |
| A2 | 0.33 | 0.43 | 0.381 |
| b1 | 0.80 | 0.99 | 0.89 |
| b2 | 1.70 | 1.88 | 1.78 |
| D | 3.90 | 4.05 | 3.966 |
| D2 | -- | -- | 3.054 |
| E | 6.40 | 6.60 | 6.504 |
| e | -- | -- | 1.84 |
| E1 | 5.30 | 5.45 | 5.37 |
| E2 | -- | -- | 3.549 |
| L | 0.75 | 0.95 | 0.85 |
| L1 | 0.50 | 0.65 | 0.57 |
| W | 1.10 | 1.41 | 1.255 |

All Dimensions in mm

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 1.840 |
| G | 0.852 |
| X | 1.390 |
| X1 | 3.360 |
| Y | 1.400 |
| Y1 | 4.860 |

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