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**DMP3085LSS**

**P-CHANNEL ENHANCEMENT MODE MOSFET**

**Product Summary**

$V_{(BR)DSS}$	$R_{DS(ON) MAX}$	Package	$I_D$ $T_A = +25^\circ C$
-30V	70m $\Omega$ @ $V_{GS} = -10V$	SO-8	-3.8A
	95m $\Omega$ @ $V_{GS} = -4.5V$		-3.2A

**Description**

This MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

**Applications**

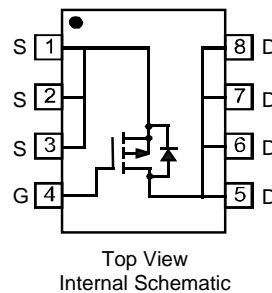
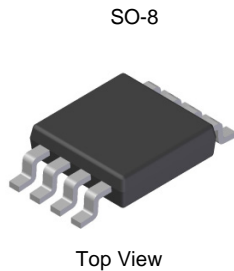
- Backlighting
- Power Management Functions
- DC-DC Converters

**Features**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 standards for High Reliability**

**Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish — Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 **e3**
- Weight: 0.008 grams (approximate)

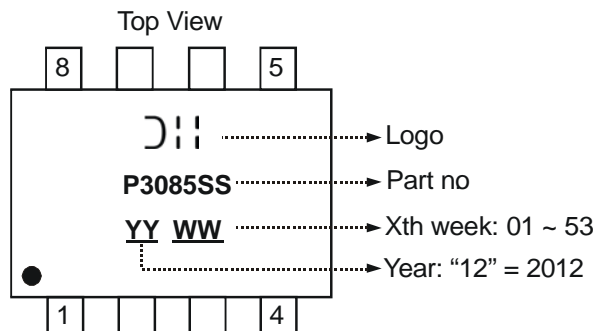


**Ordering Information**

Part Number	Case	Packaging
DMP3085LSS-13	SO-8	2500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>

**Marking Information**



**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V <sub>DSS</sub>	-30	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	I <sub>D</sub>	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	-3.8 -3	A
		t < 10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	-5.3 -4.2
Maximum Continuous Body Diode Forward Current (Note 6)	I <sub>S</sub>	-2.5	A	
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	20	A	

**Thermal Characteristics**

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	P <sub>D</sub>	T <sub>A</sub> = +25°C	1.3	W
		T <sub>A</sub> = +70°C	0.8	
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	Steady State	96	°C/W
		t < 10s	48	
Total Power Dissipation (Note 6)	P <sub>D</sub>	T <sub>A</sub> = +25°C	1.6	W
		T <sub>A</sub> = +70°C	1	
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	Steady State	78	°C/W
		t < 10s	39	
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	18		
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C	

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1	µA	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1	—	-3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	50	70	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -5.3A
		—	75	95		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.2A
Forward Transfer Admittance	Y <sub>fs</sub>	—	5.8	—	S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -5.3A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.7	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	C <sub>ISS</sub>	—	563	—	pF	V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>OSS</sub>	—	48	—		
Reverse Transfer Capacitance	C <sub>RSS</sub>	—	41	—		
Gate Resistance	R <sub>G</sub>	—	10.3	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	5.2	—	nC	V <sub>DS</sub> = -15V, I <sub>D</sub> = -3.8A
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	11	—		
Gate-Source Charge	Q <sub>gs</sub>	—	1.7	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	1.9	—		
Turn-On Delay Time	t <sub>D(on)</sub>	—	4.8	—	nS	V <sub>DS</sub> = -15V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -1A, R <sub>G</sub> = 6.0Ω
Turn-On Rise Time	t <sub>r</sub>	—	5	—		
Turn-Off Delay Time	t <sub>D(off)</sub>	—	31	—		
Turn-Off Fall Time	t <sub>f</sub>	—	14.6	—		

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
  - I<sub>AR</sub> and E<sub>AR</sub> rating are based on low frequency and duty cycles to keep T<sub>J</sub> = 25°C
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.



**DMP3085LSS**

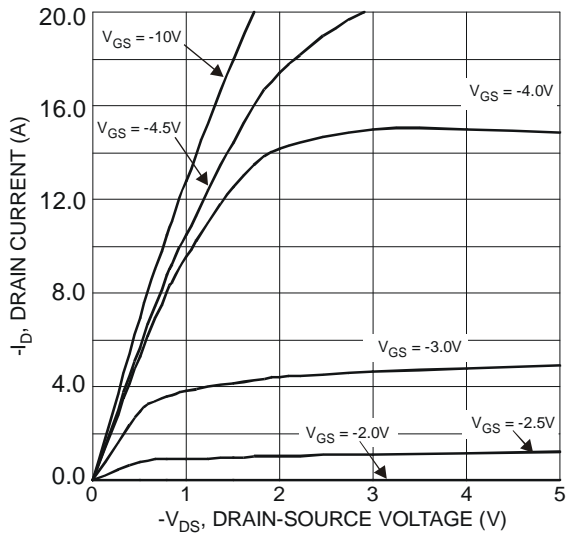


Figure 1 Typical Output Characteristics

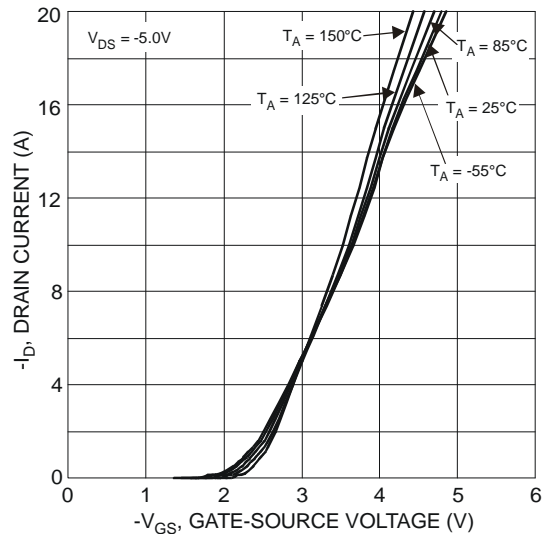


Figure 2 Typical Transfer Characteristics

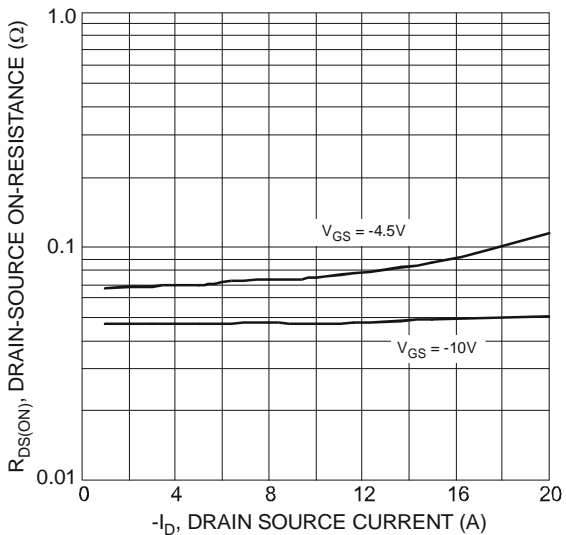


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

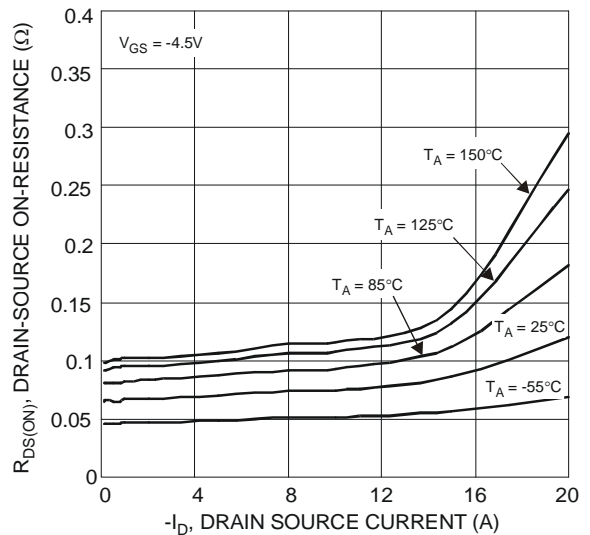


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

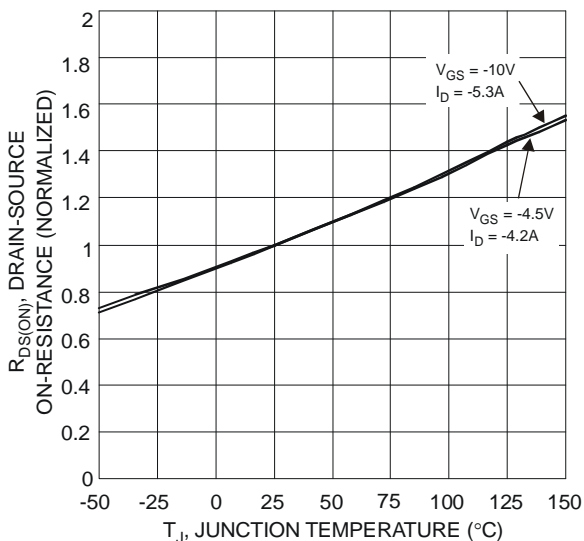


Figure 5 On-Resistance Variation with Temperature

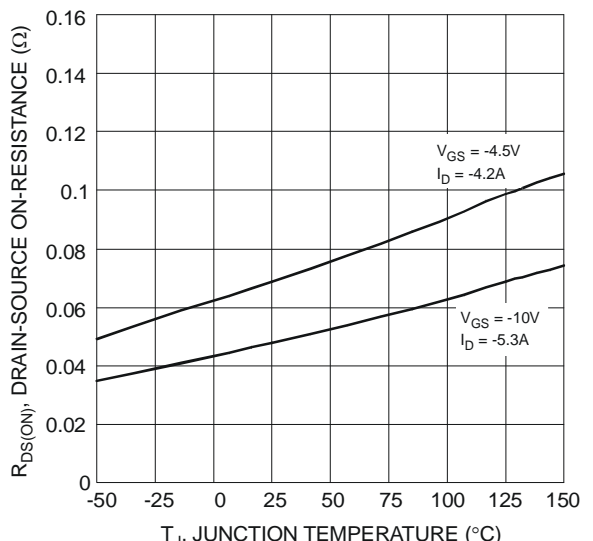


Figure 6 On-Resistance Variation with Temperature



**DMP3085LSS**

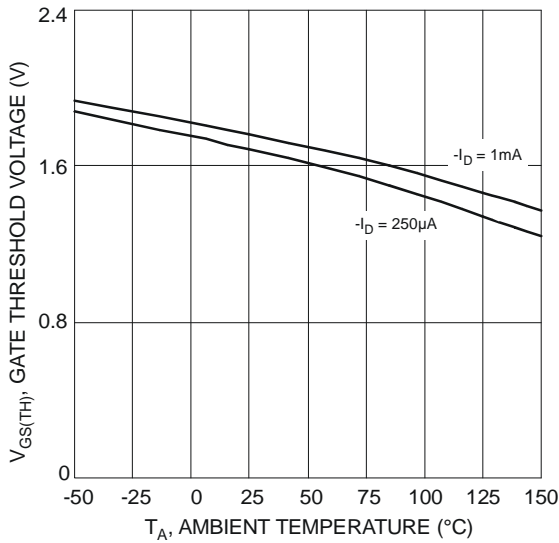


Figure 7 Gate Threshold Variation vs. Ambient Temperature

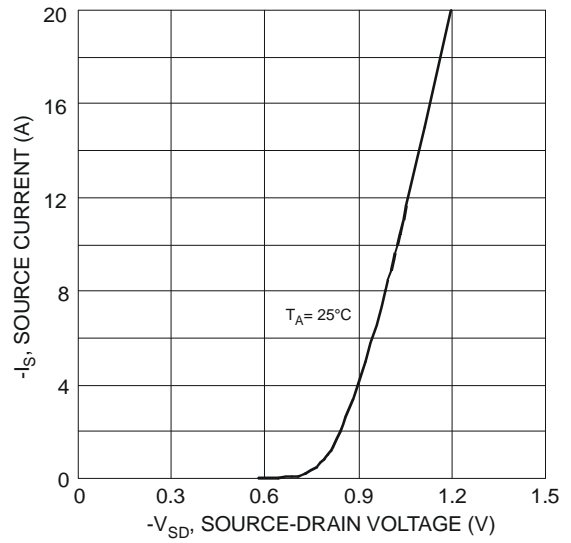


Figure 8 Diode Forward Voltage vs. Current

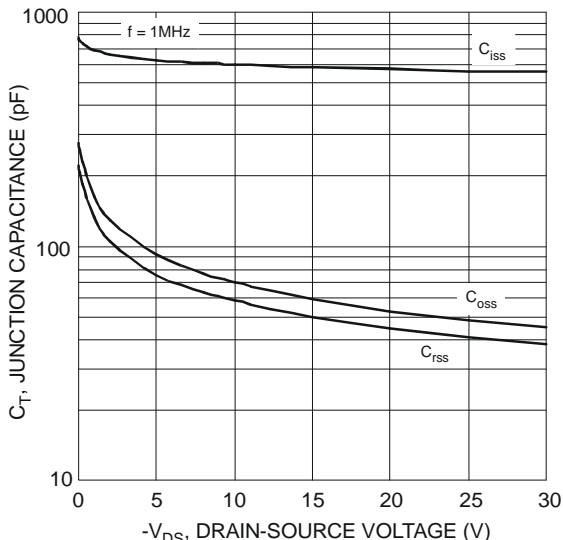


Figure 9 Typical Junction Capacitance

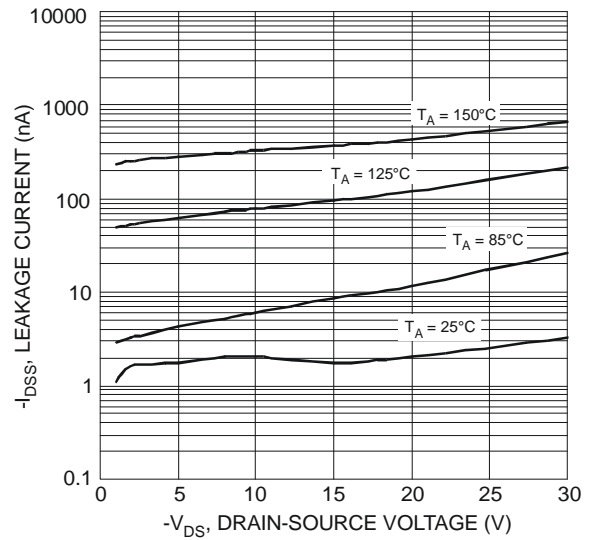


Figure 10 Typical Drain-Source Leakage Current vs. Voltage

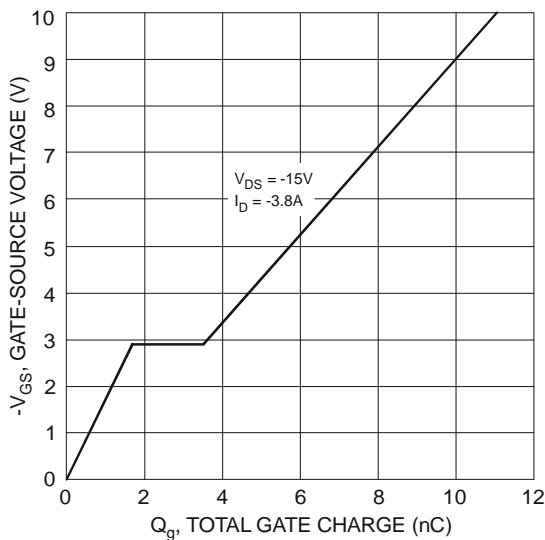
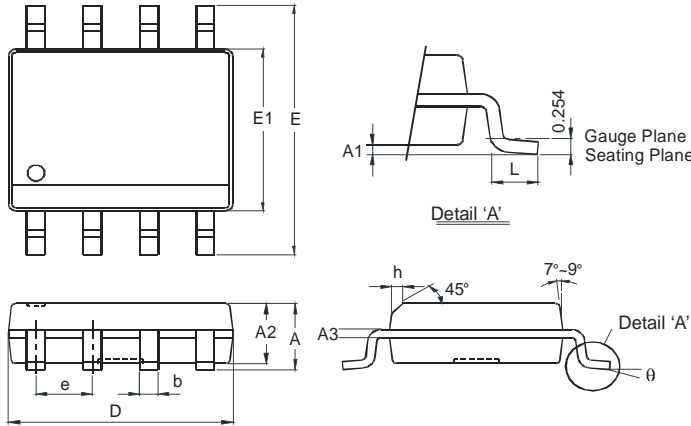


Figure 11 Gate-Charge Characteristics

**Package Outline Dimensions**

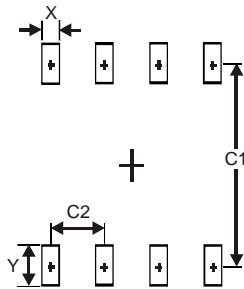
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SO-8		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

**Suggested Pad Layout**

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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