

Excellent Integrated System Limited

Stocking Distributor

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Integrated Device Technology (IDT) 4MA161133Z4AACTGI

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



Distributor of Integrated Device Technology (IDT): Excellent Integrated System Limited Datasheet of 4MA161133Z4AACTGI - OSC MEMS 161.133MHZ LVDS SMD Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



161.133 MHz LVDS Oscillator

High Performance Differential Oscillator

4MA161133Z4

Features

- Frequency:
- Output Type:

Supply Voltage:

• RMS phase jitter:

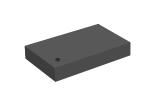
- Frequency Stability:
- ± 50ppm

LVDS

- 2.5V & 3.3V • Standard Packages:
 - 5.0 x 3.2 mm; 7.0 x 5.0 mm
 - 0.7ps typical (12k to 20MHz)

161.133 MHz

• Operating Temperature: - 40 to 85 °C ADVANCE DATASHEET



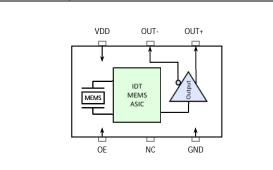
This product is rated "Green", please contact IDT for environmental compliancy information

Specification

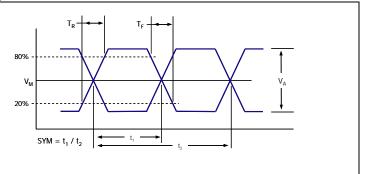
Parameter	2.5 V Specifications			3.3 V Specifications			Units	Conditions
	Min	Тур	Max	Min	Тур	Max	01110	
Supply Voltage (V _{DD})		2.50			3.30		V	
Output Frequency		161.133			161.133		MHz	
Frequency Stability			± 50			± 50	ppm	-40 to 85°C
Supply Current		95			95		mA	No load
Input LOW level			$0.3V_{\text{DD}}$			$0.3V_{\text{DD}}$	V	At OE pin
Input HIGH level	$0.\;7V_{\text{DD}}$			$0.\ 7V_{\text{DD}}$			V	At OE pin
Output LOW level		1.075			1.075		V	
Output HIGH level		1.425			1.425		V	
Amplitude (V _A)		0.35			0.35		V	Single Ended output swing (Pk-Pk)
Mid Level (V _M)		1.25			1.25		V	
Rise Time (T _R)		400			400		ps	Maximum; 20/80% of V _A ; Output load (CL) = 2pF
Fall Time (T _F)		400			400		ps	Maximum; 20/80% of V_A ; Output load (CL) = 2pF
Symmetry (SYM)	45		55	45		55	%	Worst case; measured at 50% of waveform
Period Jitter		5			5		ps	Measured over 10k cycles, rms
Phase Jitter			1			1	ps	12k to 20MHz, rms
Aging			± 5			± 5	ppm	25°C, 10 years
Note: Above specifications are typical at room temperature (25°C) unless otherwise specified. Frequency stability includes initial frequency tolerance, temperature variation, supply								

voltage variation, reflow drift, and aging (+25 °C, 10 years).

Block Diagram

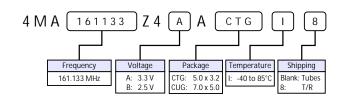


Output Waveform



Part Ordering Information

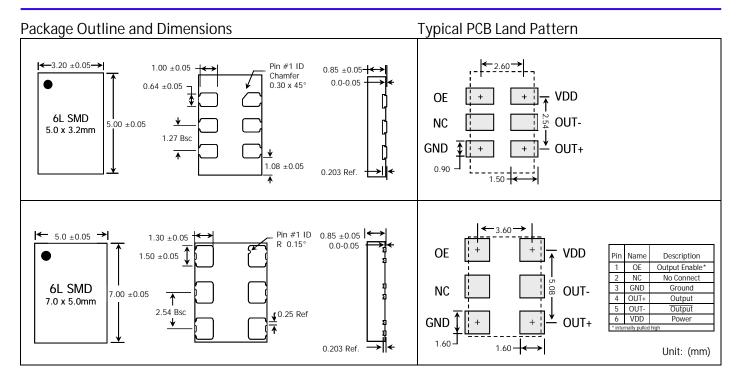
Package Size	Voltage	Ordering Code
5.0 x 3.2 mm	3.3 V	4MA161133Z4AACTGI
5.0 X 5.2 IIIII	2.5 V	4MA161133Z4BACTGI
7.0 x 5.0 mm	3.3 V	4MA161133Z4AACUGI
7.0 X 5.0 Mim	2.5 V	4MA161133Z4BACUGI





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Datasheet 161.13 MHz LVDS Oscillator





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