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STMicroelectronics STP80N6F6

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STP80N6F6

Automotive-grade N-channel 60 V, 4.4 mΩ typ., 80 A STripFET™ VI DeepGATE™ Power MOSFET in a TO-220 package

Datasheet - production data

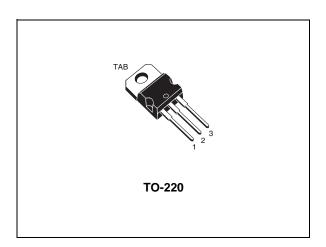
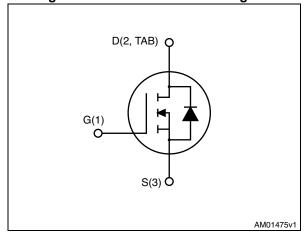


Figure 1. Internal schematic diagram



Features

Order code	V_{DS}	R _{DS(on)} max.	I _D
STP80N6F6	60 V	5 mΩ	80 A ⁽¹⁾

- 1. Current limited by package
- Designed for automotive applications and AEC-Q101 qualified
- · Low gate charge
- · Very low on-resistance
- · High avalanche ruggedness

Applications

· Switching applications

Description

This device is an N-channel Power MOSFET developed using the 6^{th} generation of STripFET™ DeepGATE™ technology, with a new gate structure. The resulting Power MOSFET exhibits the lowest $R_{DS(on)}$ in all packages.

Table 1. Device summary

Order code	Marking	Packages	Packaging
STP80N6F6	80N6F6	TO-220	Tube



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STP80N6F6 Electrical ratings

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{DS}	Drain-source voltage	60	V
V_{GS}	Gate-source voltage	± 20	V
I _D ⁽¹⁾	Drain current (continuous) at T _C = 25 °C	80	Α
I _D ⁽¹⁾	Drain current (continuous) at T _C = 100 °C	80	Α
I _{DM} ⁽¹⁾	Drain current (pulsed)	320	Α
P _{TOT}	Total dissipation at T _C = 25 °C	120	W
	Derating factor	0.8	W/°C
T _{stg}	Storage temperature	- 55 to 175	°C
T _j	Operating junction temperature	- 55 10 175	

^{1.} Current limited by package

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max	1.25	°C/W
R _{thj-a}	Thermal resistance junction-ambient max	62.5	°C/W

Electrical characteristics

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2 Electrical characteristics

(T_{CASE} = 25 °C unless otherwise specified)

Table 4. On/off states

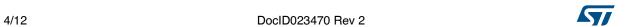
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage (V _{GS} = 0)	I _D = 250 μA	60			٧
	Zero gate voltage	V _{DS} = 60 V			1	μΑ
Drain current (V _{GS} =	Drain current (V _{GS} = 0)	V _{DS} = 60 V, T _C =125 °C			100	μΑ
I _{GSS}	Gate-body leakage current (V _{DS} = 0)	V _{GS} = ± 20 V			± 100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3		4.5	٧
R _{DS(on)}	Static drain-source on-resistance	V _{GS} = 10 V, I _D = 40 A		4.4	5	mΩ

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance		-	8325	-	pF
C _{oss}	Output capacitance	$V_{DS} = 25 \text{ V, f} = 1 \text{ MHz,}$	-	500	-	pF
C _{rss}	Reverse transfer capacitance	V _{GS} = 0	-	400	-	pF
Q_g	Total gate charge		-	147	-	nC
Q _{gs}	Gate-source charge	$V_{DD} = 30 \text{ V}, I_{D} = 80 \text{ A},$ $V_{GS} = 10 \text{ V}$	-	44	-	nC
Q _{gd}	Gate-drain charge	1.03	-	46	-	nC

Table 6. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time		-	40	-	ns
t _r	Rise time	$V_{DD} = 30 \text{ V}, I_{D} = 40 \text{ A}$ $R_{G} = 4.7 \Omega V_{GS} = 10 \text{ V}$		71		ns
t _{d(off)}	Turn-off-delay time	$R_{G} = 4.7 \Omega V_{GS} = 10 V$	-	132	-	ns
t _f	Fall time		-	40	-	ns



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Electrical characteristics

Table 7. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max	Unit
I _{SD}	Source-drain current		-		80	Α
I _{SDM} ⁽¹⁾	Source-drain current (pulsed)		-		320	Α
V _{SD} ⁽²⁾	Forward on voltage	I _{SD} = 80 A, V _{GS} = 0	-		1.3	٧
t _{rr}	Reverse recovery time	I _{SD} = 80 A, V _{DD} = 48 V	-	46		ns
Q _{rr}	Reverse recovery charge	$di/dt = 100 A/\mu s$,	-	65		nC
I _{RRM}	Reverse recovery current	T _j = 150 °C	-	2.8		Α

^{1.} Current limited by package.

^{2.} Pulsed: pulse duration = 300 μ s, duty cycle 1.5%



Electrical characteristics STP80N6F6

2.1 **Electrical characteristics (curves)**

Figure 2. Safe operating area AM17950v1 (A) 100 100µs 10 1ms 10ms Tj=175°C Single pulse VDS(V) 10

Figure 3. Thermal impedance

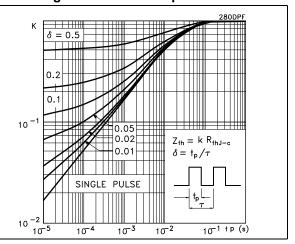
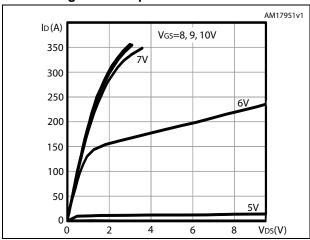


Figure 4. Output characteristics

Figure 5. Transfer characteristics



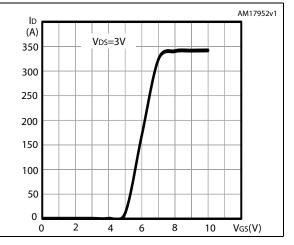
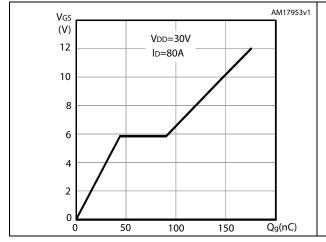
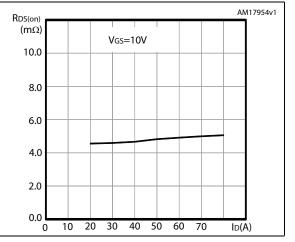


Figure 6. Gate charge vs gate-source voltage

Figure 7. Static drain-source on-resistance





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STP80N6F6 **Electrical characteristics**

Figure 8. Capacitance variations

Figure 9. Normalized gate threshold voltage vs temperature

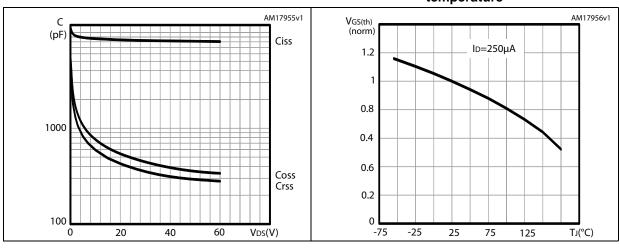


Figure 10. Normalized on-resistance vs temperature

Figure 11. Normalized V_{DS} vs temperature

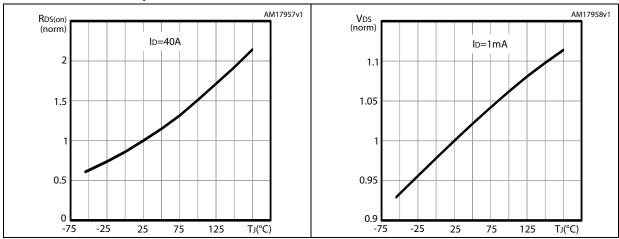
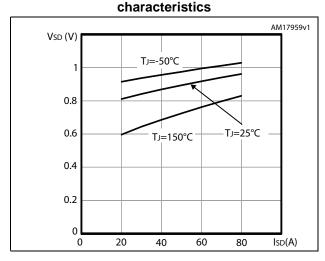


Figure 12. Source-drain diode forward







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Package mechanical data

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3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.



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Package mechanical data

Figure 13. TO-220 type A drawing øΡ H1 D1 L20 L30 b1(X3) b (X3) 0015988_typeA_Rev_T



Package mechanical data

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Table 8. TO-220 type A mechanical data

Dim.		mm	
Dim.	Min.	Тур.	Max.
А	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
С	0.48		0.70
D	15.25		15.75
D1		1.27	
Е	10		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØP	3.75		3.85
Q	2.65		2.95



STP80N6F6 Revision history

4 Revision history

Table 9. Document revision history

Date	Revision	Changes
08-Aug-2012	1	Initial release.
21-Jan-2014	2	 Document status promoted from preliminary to production data Modified: title Modified: Features Added: note 1 in cover page Modified: R_{DS(on)max} and I_D values in cover page Modified: I_D (at TC = 25 °C and at TC = 100 °C) values, I_D, I_{DM} values and added note 1 in Table 2 Modified: R_{thj-case} value in Table 3 Modified: R_{DS(on)} values in Table 4 Modified: I_D and the entire typical values in Table 5, 6 and 7 Added: Section 2.1: Electrical characteristics (curves) Updated: Section 3: Package mechanical data Minor text changes





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