

Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

<u>Vishay Semiconductor/Diodes Division</u> 10TTS08S

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Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of 10TTS08S - SCR PHASE CONTROL 800V 10A D2PAK

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



10TTS08S High Voltage Series

Vishay High Power Products

Surface Mountable Phase Control SCR, 10 A



1 (K) (G) 3

PRODUCT SUMMARY				
V _T at 6.5 A	< 1.15 V			
I _{TSM}	140 A			
V_{RRM}	800 V			

DESCRIPTION/FEATURES

The 10TTS08S High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level.

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE UNITS				
NEMA FR-4 or G-10 glass fabric-based epoxy with 4 oz. (140 μm) copper	2.5	3.5			
Aluminum IMS, R _{thCA} = 15 °C/W	6.3	9.5	Α		
Aluminum IMS with heatsink, R _{thCA} = 5 °C/W	14.0	18.5			

Note

• $T_A = 55$ °C, $T_J = 125$ °C, footprint 300 mm²

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I _{T(AV)}	Sinusoidal waveform	6.5	۸	
I _{RMS}		10	Α	
V _{RRM} /V _{DRM}		800	V	
I _{TSM}		140	А	
V _T	6.5 A, T _J = 25 °C	1.15	V	
dV/dt		150	V/µs	
dl/dt		100	A/µs	
T _J	Range	- 40 to 125	°C	

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA		
10TTS08S	800	800	1.0		

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T 440.00 4000	ation half sing	6.5	
Maximum RMS on-state current	I _{T(RMS)}	$T_{\rm C} = 112 {}^{\circ}{\rm C}, 180 {}^{\circ}{\rm condu}$	iction half sine wave	10	
Maximum peak, one-cycle,	1	10 ms sine pulse, rated	V _{RRM} applied, T _J = 125 °C	120	Α
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no volta	age reapplied, T _J = 125 °C	140	
Maniana 124 for fraince	l ² t	10 ms sine pulse, rated	V _{RRM} applied, T _J = 125 °C	72	A2-
Maximum I ² t for fusing		10 ms sine pulse, no volta	age reapplied, T _J = 125 °C	100	A ² s
Maximum I ² √t for fusing	I ² √t	$t = 0.1$ to 10 ms, no voltage reapplied, $T_J = 125$ °C		1000	A²√s
Maximum on-state voltage drop	V_{TM}	6.5 A, T _J = 25 °C		1.15	V
On-state slope resistance	r _t	T _J = 125 °C		17.3	mΩ
Threshold voltage	V _{T(TO)}			0.85	V
M. C.	1 /1	T _J = 25 °C	V Data IV W	0.05	
Maximum reverse and direct leakage current	I_{RM}/I_{DM}	T _J = 125 °C	V_R = Rated V_{RRM}/V_{DRM}	1.0	A
Typical holding current	I _H	Anode supply = 6 V, resistive load, initial I _T = 1 A		30	mA
Maximum latching current	ΙL	Anode supply = 6 V, resistive load		50	
Maximum rate of rise of off-state voltage	dV/dt	T _J = 25 °C 150		150	V/µs
Maximum rate of rise of turned-on current	dI/dt			100	A/μs

TRIGGERING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P_{GM}		8.0	W
Maximum average gate power	$P_{G(AV)}$		2.0	
Maximum peak positive gate current	+I _{GM}		1.5	Α
Maximum peak negative gate voltage	-V _{GM}		10	V
Maximum required DC gate current to trigger	I _{GT}	Anode supply = 6 V, resistive load, T _J = - 65 °C	20	mA
		Anode supply = 6 V, resistive load, T _J = 25 °C	15	
		Anode supply = 6 V, resistive load, T _J = 125 °C	10	
Maximum required DC gate voltage to trigger		Anode supply = 6 V, resistive load, T _J = - 65 °C	1.2	.,
	V _{GT}	Anode supply = 6 V, resistive load, T _J = 25 °C	1	
		Anode supply = 6 V, resistive load, T _J = 125 °C	0.7	V
Maximum DC gate voltage not to trigger	V_{GD}	T _J = 125 °C, V _{DRM} = Rated value 0.2 0.1		
Maximum DC gate current not to trigger	I _{GD}			mA

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.8	
Typical reverse recovery time	t _{rr}	T ₁ = 125 °C	3	μs
Typical turn-off time	t _q	1J = 125 C	100	

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THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 125	°C
Soldering temperature	T _S	For 10 s (1.6 mm from case)	240	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	1.5	°C/W
Typical thermal resistance, junction to ambient (PCB mount)	R _{thJA} (1)		40	C/VV
Approximate weight			2	g
Approximate weight			0.07	OZ.
Marking device		Case style D ² PAK (SMD-220)	10TTS	08S

Note

 $^{^{(1)}}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μ m) copper 40 °C/W For recommended footprint and soldering techniques refer to application note #AN-994

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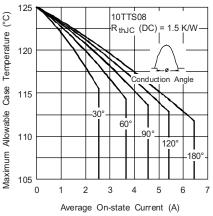


Fig. 1 - Current Rating Characteristics

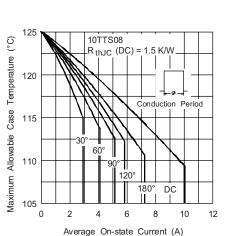


Fig. 2 - Current Rating Characteristics

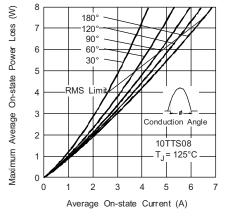


Fig. 3 - On-State Power Loss Characteristics

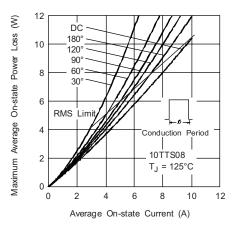


Fig. 4 - On-State Power Loss Characteristics

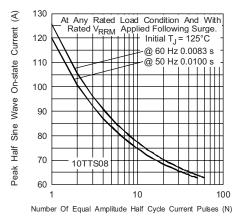


Fig. 5 - Maximum Non-Repetitive Surge Current

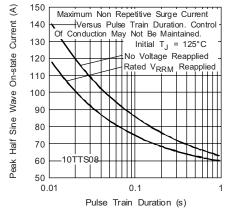


Fig. 6 - Maximum Non-Repetitive Surge Current

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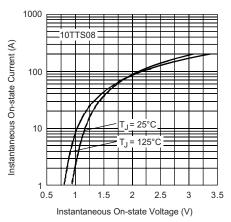


Fig. 7 - On-State Voltage Drop Characteristics

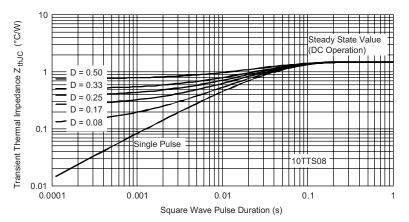


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

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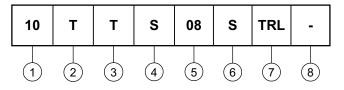
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ORDERING INFORMATION TABLE





- 1 Current rating, RMS value
- 2 Circuit configuration:

T = Single thyristor

3 - Package:

T = TO-220AC

4 - Type of silicon:

S = Converter grade

- 5 Voltage code x 100 = V_{RRM}
- 6 S = TO-220 D²PAK (SMD-220) version
- 7 Tape and reel option:
 - TRL = Left reel
 - TRR = Right orientation reel
- 8 • None = Standard production
 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95046				
Part marking information http://www.vishay.com/doc?95054				
Packaging information http://www.vishay.com/doc?95032				

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