

# **Excellent Integrated System Limited**

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Diodes Incorporated AZV358MMTR-G1

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## DUAL LOW VOLTAGE RAIL-TO-RAIL OUTPUT OPERATIONAL AMPLIFIERS

# Description

The AZV358 is dual low voltage (2.7V to 5.5V) operational amplifiers which have rail-to-rail output swing capability. The input commonmode voltage range includes ground. The chip exhibits excellent speed-power ratio, achieving 1MHz of bandwidth and  $1V/\mu s$  of slew rate with low supply current.

The AZV358 is built with BiCMOS process. It has bipolar input and output stages for improved noise performance, low input offset voltage and higher output current drive.

AZV358 is available in the package of TSSOP-8 and MSOP-8. The small packages save space on pc boards, and enable the design of small portable electronic devices. It also allows the designer to place the device closer to the signal source to reduce noise pickup and increase signal integrity.

AZV358 is also available in standard SOIC-8 package.

### **Features**

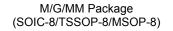
(For V<sub>CC</sub>=5V and V<sub>EE</sub>=0V, typical unless otherwise noted)

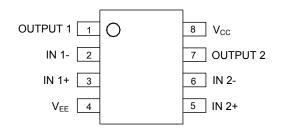
- Guaranteed 2.7V to 5.5V Performance
- No Crossover Distortion
- Gain-Bandwidth Product 1MHz
- Industrial Temperature Range: -40°C to +85°C
- Low Supply Current: 210µA
- Rail-to-Rail Output Swing under 10kΩ Load:

 $V_{OH}$  up to  $V_{CC}$  -10mV

- V<sub>OL</sub> near to V<sub>EE</sub> +65mV
- V<sub>CM</sub>: -0.1V to V<sub>CC</sub>-0.8V

# Pin Assignments

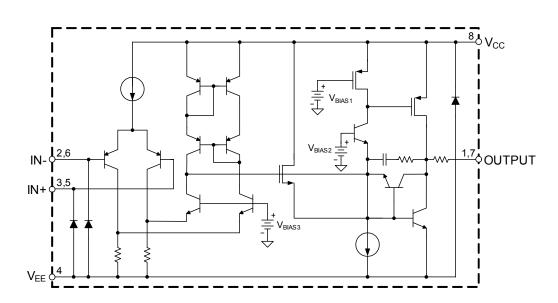




# Applications

- Active Filters
- Low Power, Low Voltage Applications
- General Purpose Portable Devices
- Cellular Phone, Cordless Phone
- Battery-Powered Systems

# **Functional Block Diagram**







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# Absolute Maximum Ratings (@T<sub>A</sub>=25°C, unless otherwise specified. Note 1)

Symbol	Parameter	Rating	Unit
V <sub>CC</sub>	Power Supply Voltage	6	V
TJ	Operation Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature Range	-65 to 150	°C
T <sub>LEAD</sub>	Lead Temperature (Soldering, 10 seconds)	260	°C
	ESD (Machine Model)	200	V
—	ESD (Human Body Model)	2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

# **Recommended Operating Conditions**

Symbol	Parameter	Min	Max	Unit
Vcc	Supply Voltage	2.7	5.5	V
T <sub>A</sub>	Ambient Operating Temperature Range	-40	85	°C

# **2.7V Electrical Characteristics** (@T<sub>A</sub>=25°C, **bold** typeface applies over T<sub>A</sub>=-40°C to 85°C, V<sub>CC</sub>=2.7V, V<sub>EE</sub>=0V, V<sub>CM</sub>=1.0V, V<sub>O</sub>=V<sub>CC</sub>/2 and R<sub>L</sub>>1M $\Omega$ , unless otherwise specified. Note 2)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
		—	_	1.7	7		
V <sub>IO</sub>	Input Offset Voltage	_	_	_	9	mV	
		_	_	11	250		
IB	Input Bias Current	-	-	—	500	nA	
		—	_	5	50		
I <sub>IO</sub>	Input Offset Current	_	_	—	150	nA	
V <sub>CM</sub>	Input Common Mode Voltage Range	for CMRR≥50dB	-0.1	_	1.9	V	
	Supply Current	V <sub>O</sub> =V <sub>CC</sub> /2, A <sub>VCL</sub> =1, No load	_	140	340		
Icc			_	_	420	μA	
CMRR	Common Mode Rejection Ratio	0≤V <sub>CM</sub> ≤1.7V	50	63	_	dB	
PSRR	Power Supply Rejection Ratio	2.7V≤V <sub>CC</sub> ≤5V, V <sub>O</sub> =1V	50	60	—	dB	
ISOURCE	Output Chart Circuit Current	V <sub>O</sub> =0V	5	20	—	mA	
ISINK	Output Short Circuit Current	V <sub>0</sub> =2.7V	10	30	—	mA	
V <sub>OH</sub>	Output Maltage Output		2.60	2.69	_	V	
V <sub>OL</sub>	Output Voltage Swing	$R_L=10k\Omega$ to 1.35V	_	60	180	mV	
GBWP	Gain Bandwidth Product	C <sub>L</sub> =200pF	_	1	—	MHz	
фм	Phase Margin	-	_	60	_	deg	
G <sub>M</sub>	Gain Margin	_	_	10	—	dB	

Note 2: Limits over the full temperature are guaranteed by design, but not tested in production.





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**5V Electrical Characteristics** (@T<sub>A</sub>=25°C, **bold** typeface applies over T<sub>A</sub>=-40°C to 85°C, V<sub>CC</sub>=5V, V<sub>EE</sub>=0V, V<sub>CM</sub>=2.0V, V<sub>O</sub>=V<sub>CC</sub>/2 and R<sub>L</sub>>1M $\Omega$ , unless otherwise specified. Note 2)

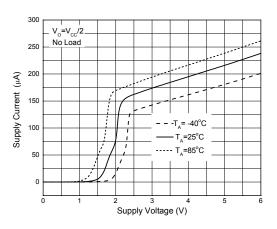
Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
		_	_	1.7	7		
V <sub>IO</sub>	Input Offset Voltage	_	_	_	9	mV	
		_	—	15	250		
IB	Input Bias Current	_	_	_	500	nA	
		_	_	5	50		
l <sub>io</sub>	Input Offset Current	_	_	_	150	nA	
V <sub>CM</sub>	Input Common Mode Voltage Range	for CMRR≥50dB	-0.1	_	4.2	V	
		V <sub>O</sub> =V <sub>CC</sub> /2, A <sub>VCL</sub> =1, No	—	210	440		
Icc	Supply Current	load	_	—	615	μA	
<u>_</u>	Large Signal Voltage Gain		84	100	—	dB	
Gv	Large Signal Voltage Galfi	R <sub>L</sub> =2kΩ	80	—	—		
CMRR	Common Mode Rejection Ratio	0≤V <sub>CM</sub> ≤4V	50	63	—	dB	
PSRR	Power Supply Rejection Ratio	2.7V≤V <sub>CC</sub> ≤5V, V <sub>O</sub> =1V, V <sub>CM</sub> =1V	50	60	_	dB	
ISOURCE	Output Short Circuit Current	V <sub>O</sub> =0V	5	60	—	mA	
Isink		V <sub>O</sub> =5V	10	160	—	mA	
		$R_L=2k\Omega$ to 2.5V	4.7	4.96	—	- V	
Maria			4.6	_	—		
V <sub>OH</sub>		$R_L$ =10k $\Omega$ to 2.5V	4.9	4.99	—		
	Output Voltage Swing		4.8	_	—		
	Output voltage Swing	R <sub>L</sub> =2kΩ to 2.5V R <sub>L</sub> =10kΩ to 2.5V	—	120	300	mV	
Mar			—	_	400		
V <sub>OL</sub>			—	65	180		
			—	_	280		
SR	Slew Rate		—	1	—	V/µs	
GBWP	Gain Bandwidth Product	C <sub>L</sub> =200pF	—	1	—	MHz	
фм	Phase Margin	_	—	60	—	deg	
G <sub>M</sub>	Gain Margin	—	_	10	—	dB	

Note 2: Limits over the full temperature are guaranteed by design, but not tested in production.



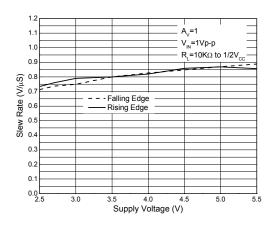


## **Performance Characteristics**

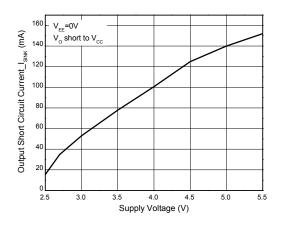


Supply Current vs. Supply Voltage

### Slew Rate vs. Supply Voltage



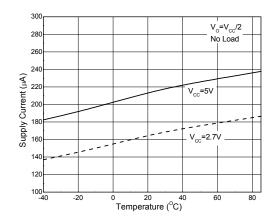
### **Output Short Circuit Current vs. Supply Voltage**



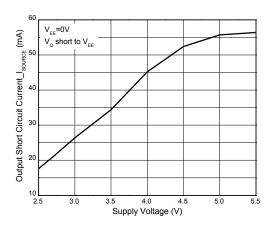
Supply Current vs. Temperature

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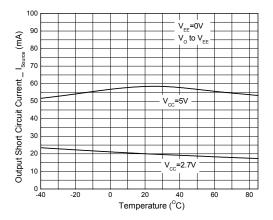
**AZV358** 



### **Output Short Circuit Current vs. Supply Voltage**



### **Output Short Circuit Current vs. Temperature**

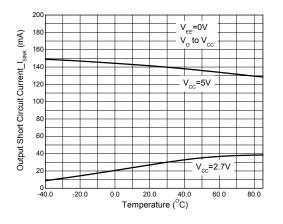


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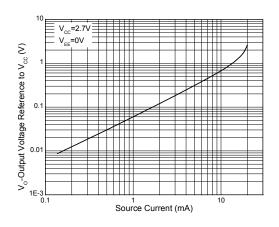


# Performance Characteristics (Cont.)

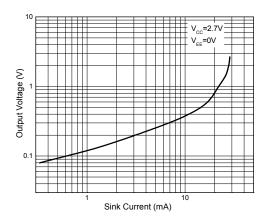


**Output Short Circuit Current vs. Temperature** 

### **Output Voltage vs. Output Source Current**



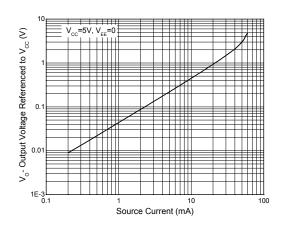
### Output Voltage vs. Output Sink Current



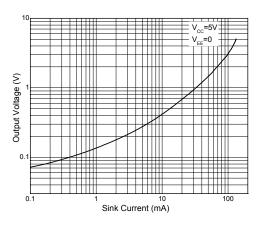
**Output Voltage vs. Output Source Current** 

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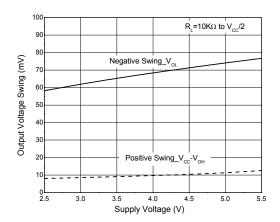
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### Output Voltage vs. Output Sink Current



### Output Voltage Swing vs. Supply Voltage

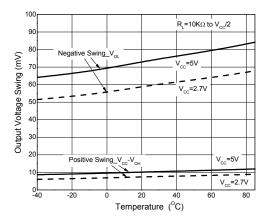


### AZV358 Document number: DS36536 Rev. 1 - 4

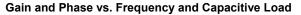


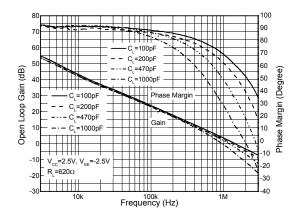


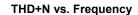
Performance Characteristics (Cont.)

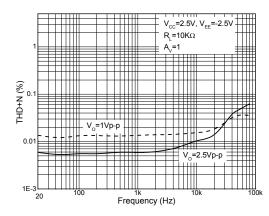


# Output Voltage Swing vs. Temperature





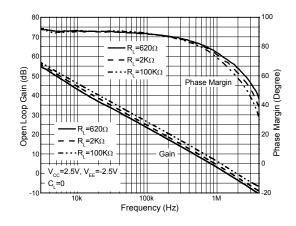




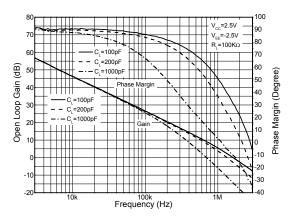
### Gain and Phase vs. Frequency and Resistive Load

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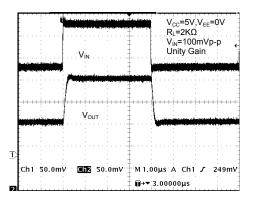
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### Gain and Phase vs. Frequency and Capacitive Load



### Non-Inverting Input Small Signal Pulse Response



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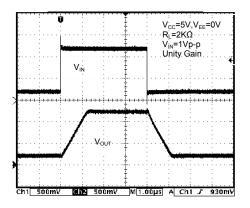


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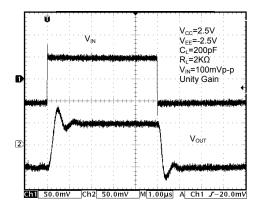


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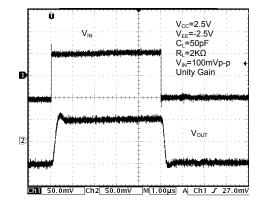
## Non-Inverting Input Large Signal Pulse Response



### Non-Inverting Input Small Signal Response



### Non-Inverting Input Small Signal Response



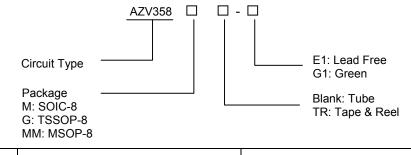




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# Ordering Information



Package Temperature Range		Part Number		Marki	Decking Tune	
		Lead Free	Green	Lead Free	Green	Packing Type
0010.0	40 10 05%0	AZV358M-E1	AZV358M-G1	AZV358M-E1	AZV358M-G1	Tube
SOIC-8	SOIC-8 -40 to 85°C	AZV358MTR-E1	AZV358MTR-G1	AZV358M-E1	AZV358M-G1	Tape & Reel
70000.0		AZV358G-E1	AZV358G-G1	EG3E	GG3E	Tube
TSSOP-8 -40 to 85°C		AZV358GTR-E1	AZV358GTR-G1	EG3E	GG3E	Tape & Reel
		AZV358MM-E1	AZV358MM-G1	AZV358MM-E1	AZV358MM-G1	Tube
MSOP-8 -40 to 85°C	AZV358MMTR-E1	AZV358MMTR-G1	AZV358MM-E1	AZV358MM-G1	Tape & Reel	

BCD Semiconductor's Pb-free products, as designated with "E1" suffix in the part number, are RoHS compliant. Products with "G1" suffix are available in green packages.



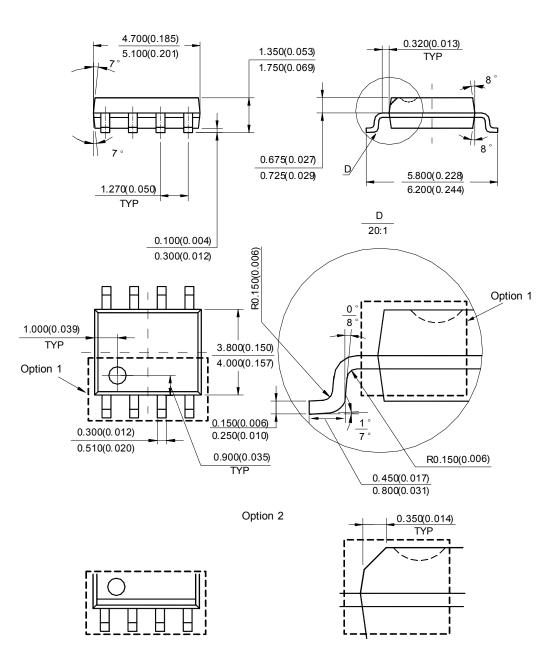


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# Package Outline Dimensions (All dimensions in mm(inch).)

DATA SHEET



SOIC-8

Note: Eject hole, oriented hole and mold mark is optional .



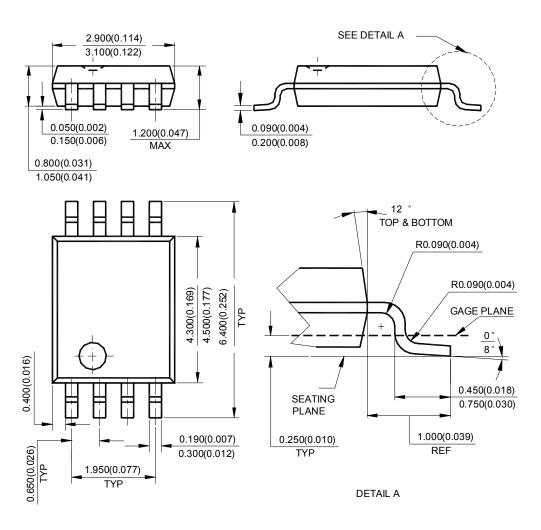


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# Package Outline Dimensions (Cont.) (All dimensions in mm(inch).)

**TSSOP-8** 



Note: Eject hole, oriented hole and mold mark is optional



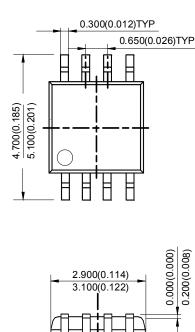


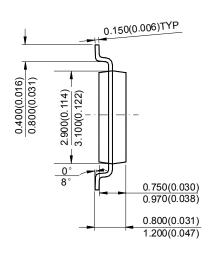
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# Package Outline Dimensions (Cont.) (All dimensions in mm(inch).)

MSOP-8





Note: Eject hole, oriented hole and mold mark is optional



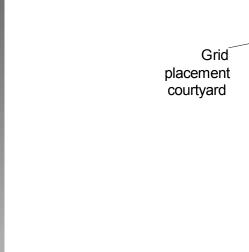


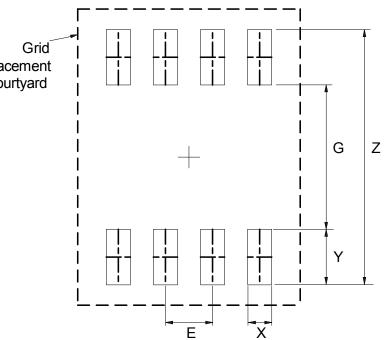
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# Suggested Pad Layout

SOIC-8





Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050

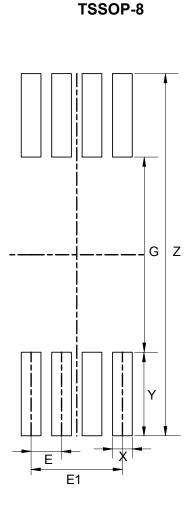




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# Suggested Pad Layout (Cont.)



Dimensions	Z	G	X	Y	E	E1
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	7.720/0.304	4.160/0.164	0.420/0.017	1.780/0.070	0.650/0.026	1.950/0.077



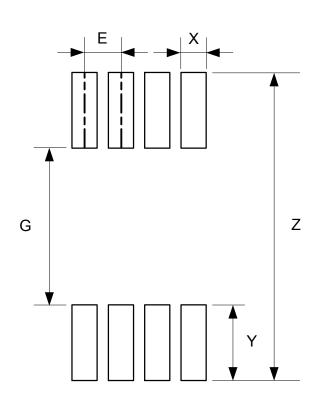


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# Suggested Pad Layout (Cont.)





Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	5.500/0.217	2.800/0.110	0.450/0.018	1.350/0.053	0.650/0.026





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AZV358

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- 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
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