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Stocking Distributor

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[Vishay Semiconductor/Opto Division](#)
[VSMY14940](#)

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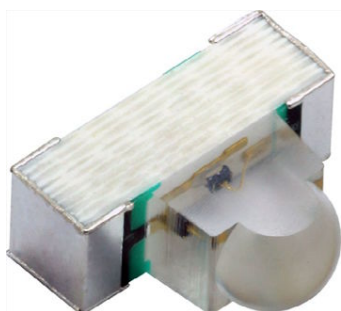


www.vishay.com

VSMY14940

Vishay Semiconductors

High Speed Infrared Emitting Diodes, 940 nm, Surface Emitter Technology



DESCRIPTION

As part of the [SurfLight™](#) portfolio, the VSMY14940 is an infrared, 940 nm, side looking emitting diode based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted PCB based package (with lens) for surface mounting (SMD).

APPLICATIONS

- Emitter for remote control
- IR touch panels
- Photointerrupters
- Optical switch

FEATURES

- Package type: surface mount
- Package form: side view
- Dimensions (L x W x H in mm): 3.2 x 2.51 x 1.2
- Peak wavelength: $\lambda_p = 940$ nm
- High reliability
- High radiant power
- Very high radiant intensity
- Angle of half intensity: $\phi = \pm 9^\circ$
- Suitable for high pulse current operation
- Floor life: 168 h, MSL 3, according to J-STD-020
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

PRODUCT SUMMARY

COMPONENT	I_e (mW/sr)	ϕ (deg)	λ_p (nm)	t_r (ns)
VSMY14940	82	± 9	940	10

Note

- Test condition see table "Basic Characteristics"

ORDERING INFORMATION

ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY14940	Tape and reel	MOQ: 1500 pcs, 1500 pcs/reel	Side view

Note

- MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	5	V
Forward current		I_F	70	mA
Surge forward current	$t_p = 100 \mu\text{s}$	I_{FSM}	1	A
Power dissipation		P_V	119	mW
Junction temperature		T_j	100	$^\circ\text{C}$
Operating temperature range		T_{amb}	-40 to +85	$^\circ\text{C}$
Storage temperature range		T_{stg}	-40 to +100	$^\circ\text{C}$
Soldering temperature	According fig. 10, J-STD-020	T_{sd}	260	$^\circ\text{C}$
Thermal resistance junction / ambient	J-STD-051, soldered on PCB	R_{thJA}	390	K/W



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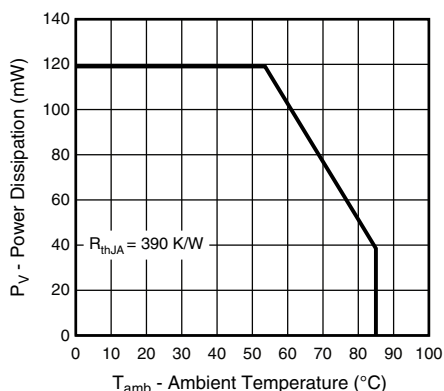


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

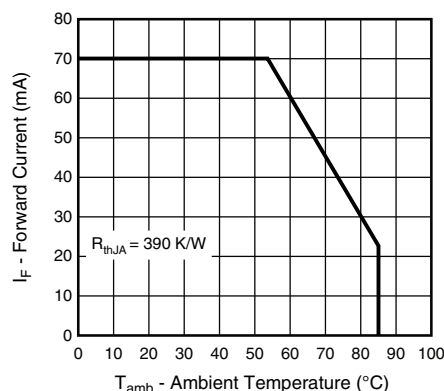


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 20\text{ mA}$, $t_p = 20\text{ ms}$	V_F	1.1	1.32	1.6	V
	$I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$	V_F	-	1.48	1.7	V
	$I_F = 1\text{ A}$, $t_p = 100\text{ }\mu\text{s}$	V_F	-	2.5	-	V
Temperature coefficient of V_F	$I_F = 20\text{ mA}$	TK_{V_F}	-	-1.7	-	mV/K
Reverse current		I_R	not designed for reverse operation			μA
Junction capacitance	$V_R = 0\text{ V}$, $f = 1\text{ MHz}$, $E = 0\text{ mW/cm}^2$	C_J	-	5	-	pF
Radiant intensity	$I_F = 20\text{ mA}$, $t_p = 20\text{ ms}$	I_e	15	24	35	mW/sr
	$I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$	I_e	50	82	120	mW/sr
	$I_F = 1\text{ A}$, $t_p = 100\text{ }\mu\text{s}$	I_e	-	660	-	mW/sr
Radiant power	$I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$	ϕ_e	-	40	-	mW
Temperature coefficient of radiant power	$I_F = 20\text{ mA}$	$TK\phi_e$	-	-0.21	-	%/K
Angle of half intensity		φ	-	± 9	-	deg
Peak wavelength	$I_F = 20\text{ mA}$	λ_p	920	940	960	nm
Spectral bandwidth	$I_F = 20\text{ mA}$	$\Delta\lambda$	-	35	-	nm
Temperature coefficient of λ_p	$I_F = 20\text{ mA}$	$TK\lambda_p$	-	0.25	-	nm/K
Rise time	$I_F = 100\text{ mA}$, 20 % to 80 %	t_r	-	10	-	ns
Fall time	$I_F = 100\text{ mA}$, 20 % to 80 %	t_f	-	10	-	ns

BASIC CHARACTERISTICS ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)

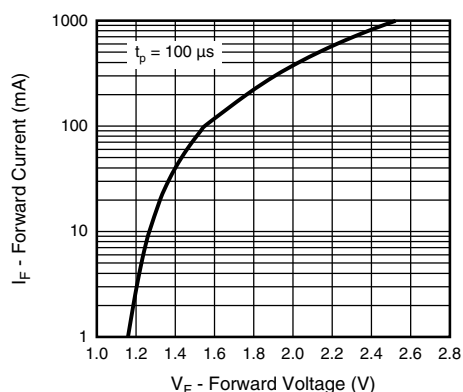


Fig. 3 - Forward Current vs. Forward Voltage

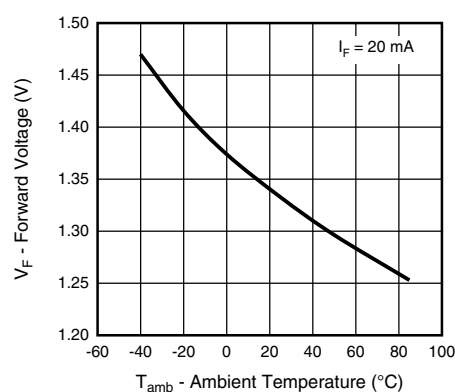


Fig. 4 - Forward Voltage vs. Ambient Temperature



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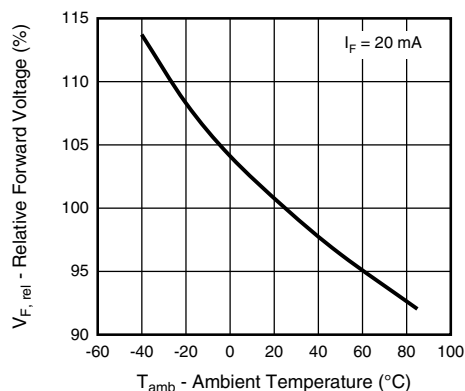


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

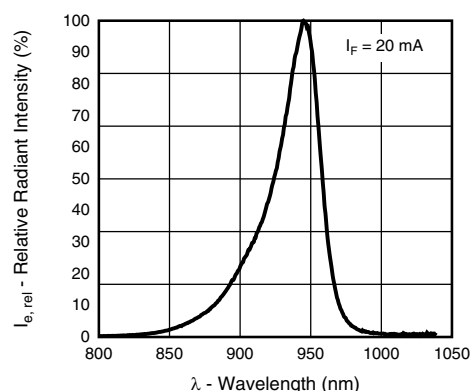


Fig. 8 - Relative Radiant Intensity vs. Wavelength

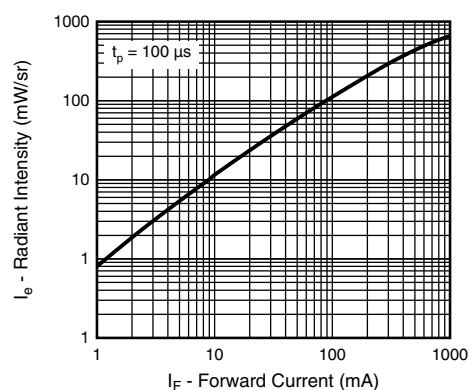


Fig. 6 - Radiant Intensity vs. Forward Current

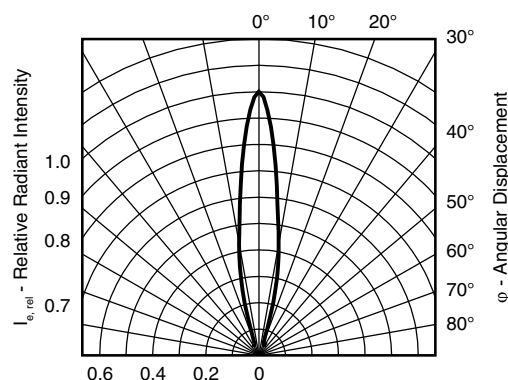


Fig. 9 - Relative Radiant Intensity vs. Angular Displacement

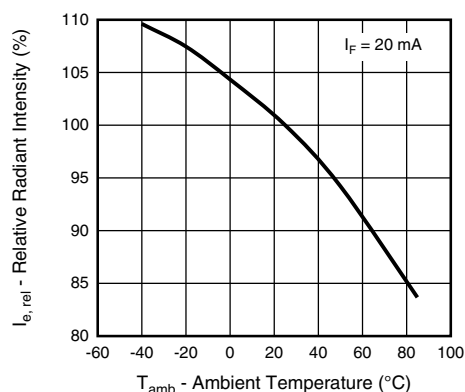


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature



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SOLDER PROFILE

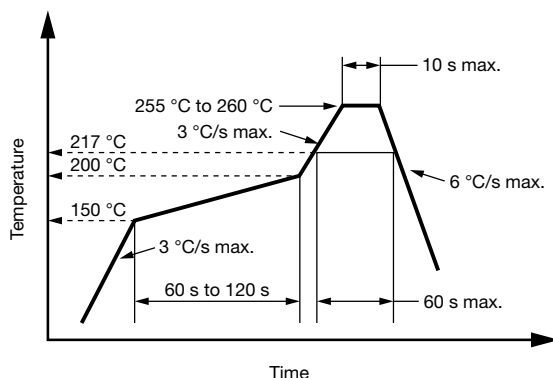


Fig. 10 - Lead (Pb)-free Reflow Solder Profile
According to J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

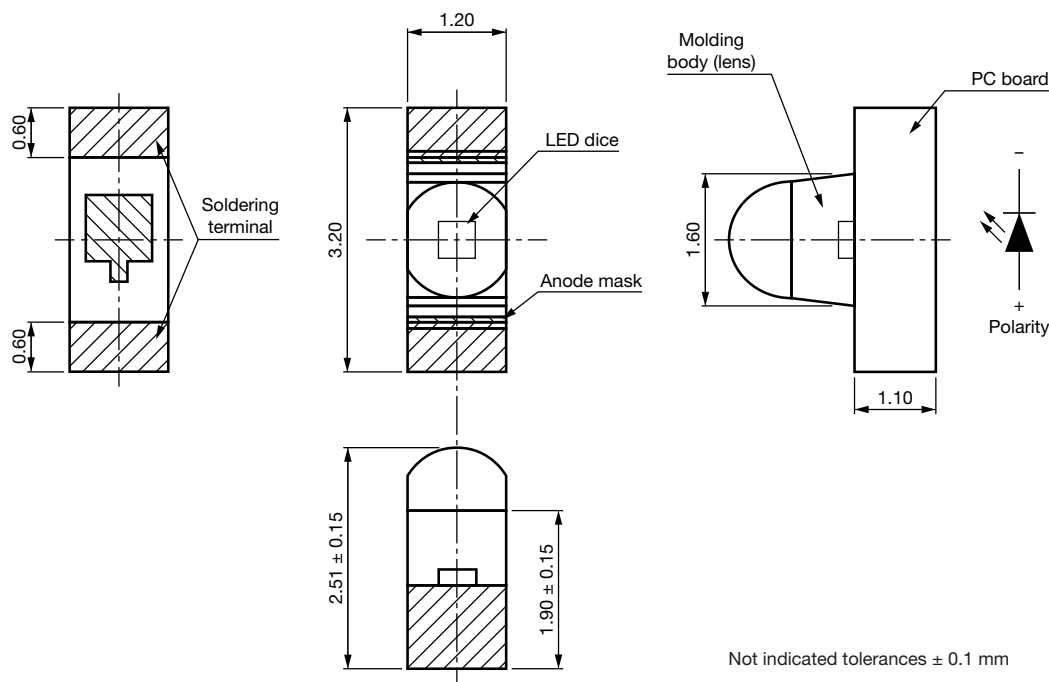
Conditions: $T_{amb} < 30\text{ °C}$, $RH < 60\%$

Moisture sensitivity level 3, according to J-STD-020.

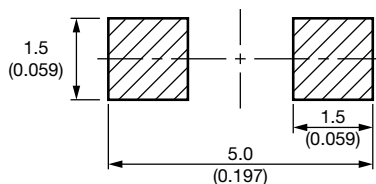
DRYING

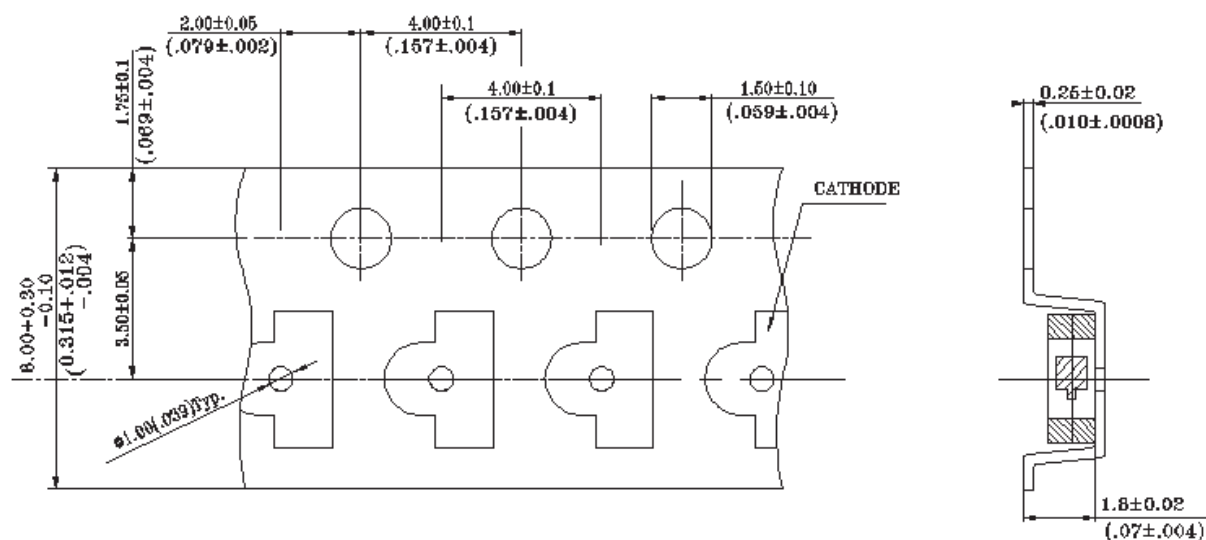
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), $RH < 5\%$.

PACKAGE DIMENSIONS in millimeters: VSMY14940



Recommended Solder Pad







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