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Stocking Distributor

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Sharp Microelectronics PC81410NSZ

For any questions, you can email us directly: sales@integrated-circuit.com



PC8141xNSZ Series

PC8141xNSZ Series

DIP 4pin High CMR, AC Input, Low Input Current Photocoupler



■ Description

PC8141xNSZ Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-pin DIP, available in SMT gullwing lead-form option.

Input-output isolation voltage(rms) is 5.0kV.

Collector-emitter voltage is $80V^{(*)}$, CTR is 50% to 600% at input current of ± 0.5 mA and CMR is MIN. $10kV/\mu s$.

■ Features

- 1. 4-pin DIP package
- Double transfer mold package (Ideal for Flow Soldering)
- 3. AC input type
- 4. Low input current type (I_F=±0.5mA)
- 5. High collector-emitter voltage (V_{CEO}: 80V(*))
- High noise immunity due to high common rejection voltage (CMR: MIN. 10kV/μs)
- 7. High isolation voltage between input and output $(V_{iso(rms)}: 5.0 \text{ kV})$

(*)Up to Date code "P8"(August 2002)VcEo:70V.

■ Agency approvals/Compliance

- Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. PC8141)
- 2. Package resin: UL flammability grade (94V-0)

■ Applications

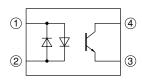
- 1. Programmable controllers
- 2. Facsimiles
- 3. Telephones

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SHADD

PC8141xNSZ Series

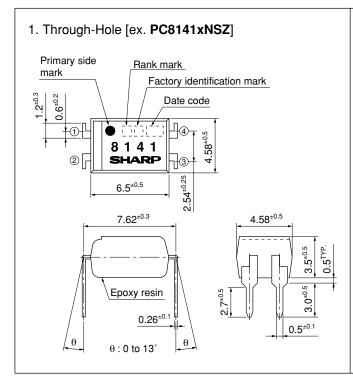
■ Internal Connection Diagram

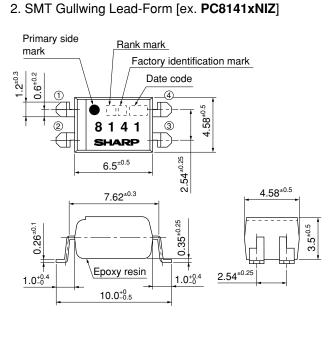


- 1) Anode/Cathode
- ② Cathode/Anode
- ③ Emitter
- 4 Collector

■ Outline Dimensions

(Unit:mm)





Product mass: approx. 0.25g

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PC8141xNSZ Series

Date code (2 digit)

	1st o	digit		2nd digit		
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	÷	÷	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin	
no mark	- Japan	
	Indonesia	
$\overline{\hspace{1cm}}$	Philippines	
_	China	

^{*} This factory making is for identification purpose only.

Please contact the local SHARP sales representative to see the actual status of the production.

Rank mark

Refer to the Model Line-up table

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Datasheet of PC81410NSZ - OPTOISOLATOR 5KV TRANS 4DIP

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Absolute Maximum Ratings (T _a =25°C							
	Parameter	Symbol	Rating	Unit			
	Forward current	I_{F}	±10	mA			
Input	*1 Peak forward current	I_{FM}	±200	mA			
I	Power dissipation	P	15	mW			
	Collector-emitter voltage	V_{CEO}	*4 80	V			
→	Emitter-collector voltage	V_{ECO}	6	V			
Oui	Collector current	I_{C}	50	mA			
	Collector power dissipation	P_{C}	150	mW			
-	Γotal power dissipation	P _{tot}	170	mW			
*2 Isolation voltage		V _{iso (rms)}	5.0	kV			
Operating temperature		T_{opr}	-30 to +100	°C			
Storage temperature		T_{stg}	-55 to +125	°C			
*3	Soldering temperature	T_{sol}	260	°C			

^{*1} Pulse width≤100µs, Duty ratio : 0.001

■ Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

	Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Forward voltage		V_F	$I_F=\pm 10 mA$	_	1.2	1.4	V
Input	Terminal capacitance		C_{t}	V=0, f=1kHz	-	30	250	pF
	Collector dark current		I_{CEO}	V_{CE} =50V, I_F =0	-	-	100	nA
	Collector-emitter breakdown voltage		$\mathrm{BV}_{\mathrm{CEO}}$	$I_{C}=0.1 \text{mA}, I_{F}=0$	*5 80	ı	İ	V
Output	Emitter-collector breakdown voltage		$\mathrm{BV}_{\mathrm{ECO}}$	$I_{E}=10\mu A, I_{F}=0$	6	-	-	V
	Collector current		I_{C}	$I_F = \pm 0.5 \text{mA}, V_{CE} = 5 \text{V}$	0.25	-	2.0	mA
	Collector-emitter saturation voltage		V _{CE (sat)}	$I_F=\pm 10$ mA, $I_C=1$ mA	_	-	0.2	V
	Isolation resistance		R_{ISO}	DC500V, 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	-	Ω
Transfer	Floating capacitance		$C_{\rm f}$	V=0, f=1MHz	_	0.6	1.0	pF
charac-	Response time	Rise time	t_r	$V_{CF}=2V$, $I_{C}=2mA$, $R_{L}=100\Omega$	-	4	18	μs
teristics		Fall time	$t_{\rm f}$	V _{CE} =2 V, I _C =2IIIA, R _L =100\$2	_	3	18	μs
	Common mode rejection voltage		CMR	$T_{a}\!\!=\!\!25^{\circ}C,R_{L}\!\!=\!\!470\Omega,V_{CM}\!\!=\!\!1.5kV(peak)$ $I_{F}\!\!=\!\!0,V_{CC}\!\!=\!\!9V,V_{np}\!\!=\!\!100mV$	10	_	_	kV/μs

^{*5} Up to Date code "P8"(August 2002)BVceo≥70V.

^{*2 40} to 60%RH, AC for 1 minute, f=60Hz

^{*3} For 10s

^{*4} Up to Date code "P8"(August 2002)VcEo:70V.



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■ Model Line-up

Lead Form	Through-Hole	SMT Gullwing		Y F A3		
Package	Sle	eve	Rank mark	I_{C} [mA] $(I_{F}=\pm 0.5$ mA, $V_{CE}=5$ V, $T_{a}=25$ °C)		
	100pcs	/sleeve		$(I_F=\pm 0.5IIIA, V_{CE}=3V, I_a=23C)$		
Model No.	PC81410NSZ	PC81410NIZ	with or without	0.25 to 2.0		
	PC81411NSZ	PC81411NIZ	A	0.5 to 1.5		

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.

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Fig.1 Test Circuit for Common Mode Rejection Voltage

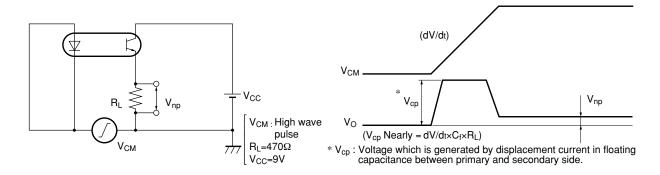


Fig.2 Forward Current vs. Ambient Temperature

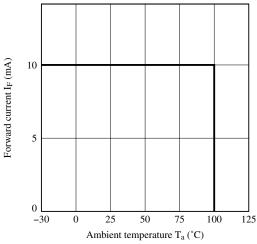


Fig.4 Collector Power Dissipation vs.

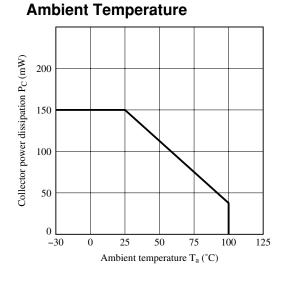


Fig.3 Diode Power Dissipation vs. Ambient Temperature

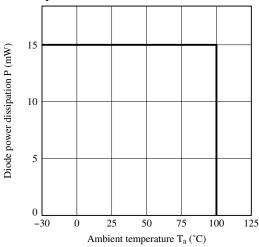


Fig.5 Total Power Dissipation vs. Ambient Temperature

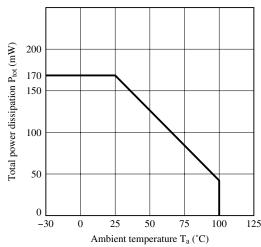




Fig.6 Peak Forward Current vs. Duty Ratio

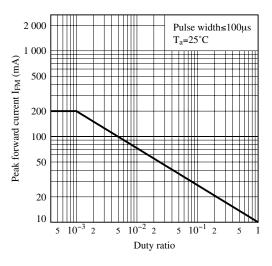


Fig.8 Current Transfer Ratio vs. Forward Current

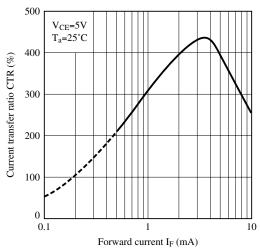


Fig.10 Relative Current Transfer Ratio vs.
Ambient Temperature

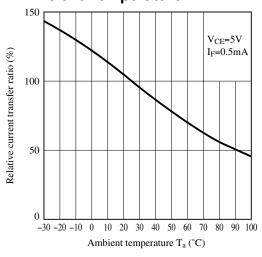


Fig.7 Forward Current vs. Forward Voltage

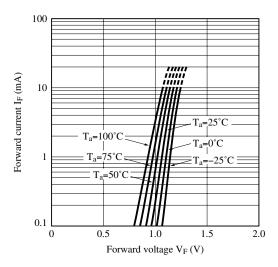


Fig.9 Collector Current vs. Collector-emitter Voltage

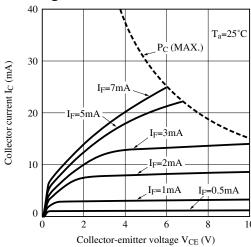
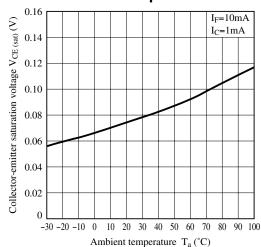


Fig.11 Collector - emitter Saturation Voltage vs. Ambient Temperature





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Fig.12 Collector Dark Current vs. Ambient Temperature

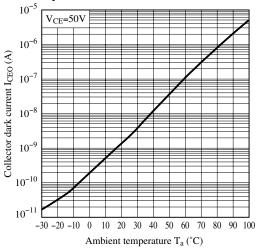


Fig.14 Response Time vs. Load Resistance (saturation region)

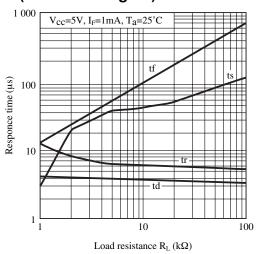


Fig.16 Frequency Response

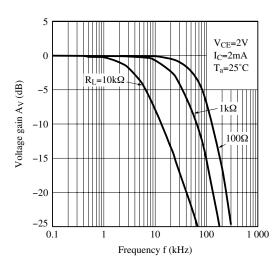


Fig.13 Response Time vs. Load Resistance (active region)

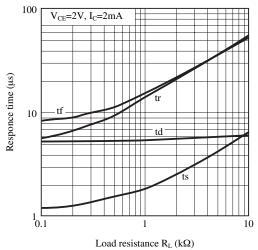
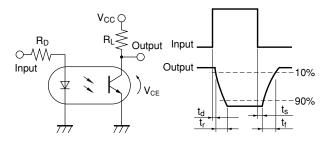
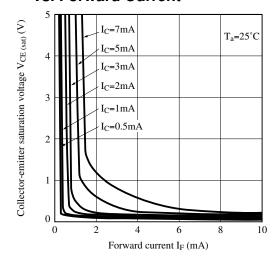


Fig.15 Test Circuit for Response Time



Please refer to the conditions in Fig.13 and Fig.14.

Fig.17 Collector-emitter Saturation Voltage vs. Forward Current



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.

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■ Design Considerations

Design guide

While operating at I_F<0.5mA, CTR variation may increase.

Please make design considering this fact.

In case that some sudden big noise caused by voltage variation is provided between primary and secondary terminals of photocoupler some current caused by it is floating capacitance may be generated and result in false operation since current may go through IRED or current may change.

If the photocoupler may be used under the circumstances where noise will be generated we recommend to use the bypass capacitors at the both ends of IRED.

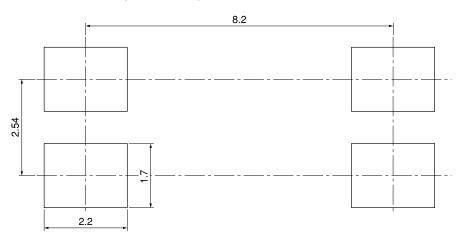
This product is not designed against irradiation and incorporates non-coherent IRED.

Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

Recommended Foot Print (reference)



(Unit: mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.

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■ Manufacturing Guidelines

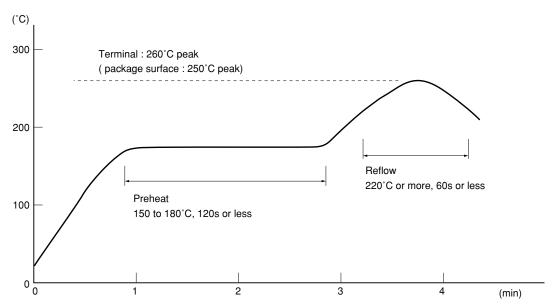
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.

Sheet No.: D2-A03701EN

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Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances:CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

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■ Package specification

Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

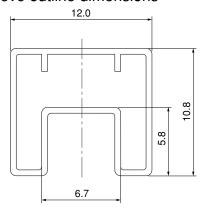
MAX. 100pcs of products shall be packaged in a sleeve.

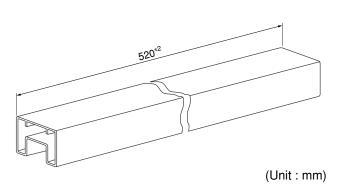
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions





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 - --- Office automation equipment
 - --- Telecommunication equipment [terminal]
 - --- Test and measurement equipment
 - --- Industrial control
 - --- Audio visual equipment
 - --- Consumer electronics
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- --- Gas leakage sensor breakers
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- --- Various safety devices, etc.
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