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Sharp Microelectronics PC844IJ1

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Distributor of Sharp Microelectronics: Excellent Integrated System Limited Datasheet of PC844IJ1 - OPTOISOLTR 5KV 4CH TRANS 16-SMD Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

PC844XJ0000F Series

PC844XJ0000F Series

DIP 16pin (4-channel) AC Input Photocoupler

*1-channel package type is also available. (model No. **PC814XJ0000F Series**)

Description

PC844XJ0000F Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-channel 16pin DIP.

Input-output isolation voltage(rms) is 5.0kV.

Collector-emitter voltage is 80V and CTR is 20% to 300% at input current of ± 1 mA.

Features

- 1. 16pin DIP 4-channel package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. AC input type
- 4. High collector-emitter voltage (V_{CEO} : 80V)
- 5. Current transfer ratio (CTR : MIN. 20% at I_F=±1mA, V_{CE}=5V)
- 6. High isolation voltage between input and output $(V_{iso(rms)}: 5.0 kV)$
- 7. Lead-free and RoHS directive compliant

■ Agency approvals/Compliance

- 1. Recognized by UL1577, file No. E64380 (as model No. **PC844**)
- 2. Package resin : UL flammability grade (94V-0)

Applications

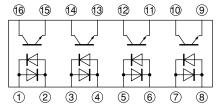
- 1. Programmable controllers
- 2. Telephone sets, telephone exchangers
- 3. System appliances
- 4. Signal tranmission between circuits of different potentials and impedances

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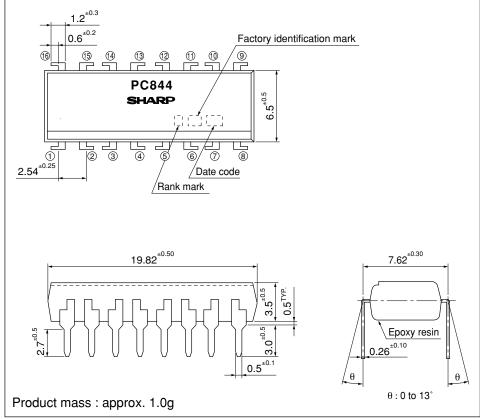
■ Internal Connection Diagram



1357 Anode/Cathode 2468 Cathode/Anode 911315 Emitter 10121416 Collector

Outline Dimensions





Plating material : SnCu (Cu : TYP. 2%)



Date code (2 digit)

	1st o	digit		2nd digit		
Year of production				Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	Α	2002	Р	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	Т	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	Α	September	9	
1999	L	2011	В	October	0	
2000	М	2012	С	November	Ν	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin	
no mark	- Japan	
	Indonesia	
$\overline{}$	Philippines	
	China	

* This factory marking is for identification purpose only. Please contact the local SHARP sales representative to see the actual status of the production.

Rank mark

Refer to the Model Line-up table



■ Absolute Maximum Ratings (T _a =25°C)							
	Parameter	Symbol	Rating	Unit			
t	Forward current	I_F	±50	mA			
Input	*1 Peak forward current	I _{FM}	±1	A			
Ĥ	Power dissipation	Р	70	mW			
	Collector-emitter voltage	V _{CEO}	80	V			
Output	Emitter-collector voltage	V _{ECO}	6	V			
Out	Collector current	I _C	50	mA			
	Collector power dissipation	P _C	150	mW			
Total power dissipation		P _{tot}	200	mW			
*2 Isolation voltage		V _{iso (rms)}	5.0	kV			
Operating temperature		T _{opr}	-30 to +100	°C			
S	Storage temperature	T _{stg}	-55 to +125	°C			
*3 Soldering temperature		T _{sol}	260	°C			

*1 Pulse width≤100µs, Duty ratio : 0.001

*2 40 to 60%RH, AC for 1 minute, f = 60Hz

*3 For 10s

Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

								$(1_a = 25 \text{ C})$
	Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
ut	Forward voltage V ₁		V _F	I _F =±20mA	-	1.2	1.4	V
Inpu	Peak forward volta	age	V _{FM}	$I_{FM}=\pm0.5V$	-	-	3.0	V
II	Terminal capacitar	nce	Ct	V=0, f=1kHz	-	50	250	pF
ut	Collector dark cur	rent	I _{CEO}	V _{CE} =50V, I _F =0	-	_	100	nA
utp	Collector-emitter breakd	own voltage	BV _{CEO}	$I_{C}=0.1 \text{mA}, I_{F}=0$	80	_	_	V
ō	Emitter-collector breakdown voltage		BV _{ECO}	$I_{E}=10\mu A, I_{F}=0$	6	-	-	V
er characteristics	Collector current		I _C	$I_F=\pm 1$ mA, $V_{CE}=5V$	0.2	-	3.0	mA
	Collector-emitter saturation voltage		V _{CE (sat)}	$I_F=\pm 20 \text{mA}, I_C=1 \text{mA}$	-	0.1	0.2	V
	Isolation resistance		R _{ISO}	DC500V, 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	_	Ω
	Floating capacitance		C _f	V=0, f=1MHz	-	0.6	1.0	pF
	Cut-off frequency		f _c	$V_{CE}=5V, I_{C}=2mA, R_{L}=100\Omega, -3dB$	15	80	_	kHz
Transfer	Response time	Rise time	t _r	$V_{CE}=2V, I_{C}=2mA, R_{L}=100\Omega$	-	4	18	μs
Tr		Fall time	t _f		_	3	18	μs



■ Model Line-up

Package	Sleeve	Rank mark	I _C [mA]
	25 pcs/sleeve	Kalik Illark	$(I_{F}=\pm 1.0 \text{mA}, V_{CE}=5 \text{V}, T_{a}=25^{\circ}\text{C})$
Model No.	PC844XJ0000F	with or without	0.2 to 3.0
Widdel No.	PC844X1J000F	А	0.5 to 1.5

Please contact a local SHARP sales representative to inquire about production status.

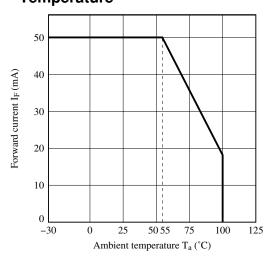
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Fig.1 Forward Current vs. Ambient Temperature





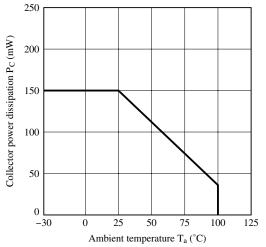


Fig.5 Peak Forward Current vs. Duty Ratio

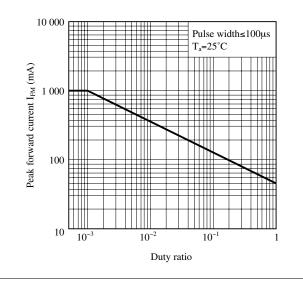


Fig.2 Diode Power Dissipation vs. Ambient Temperature

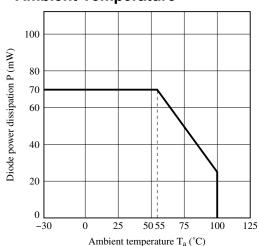


Fig.4 Total Power Dissipation vs. Ambient Temperature

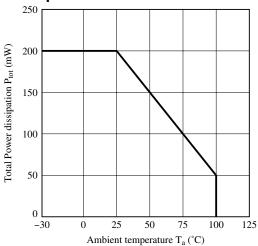
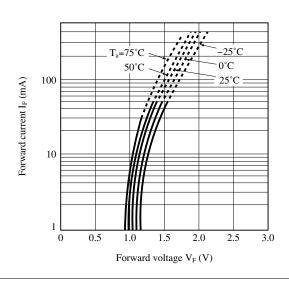


Fig.6 Forward Current vs. Forward Voltage



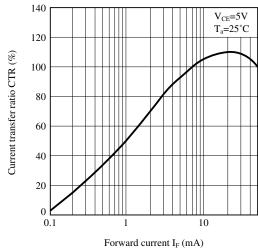
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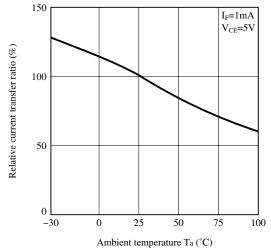
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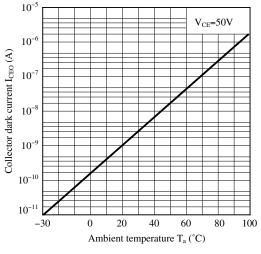
Fig.7 Current Transfer Ratio vs. Forward Current

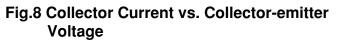












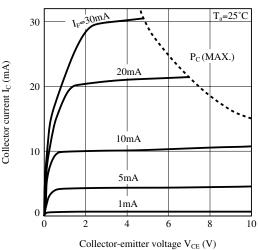
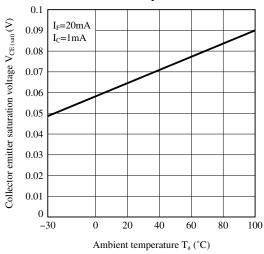


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature





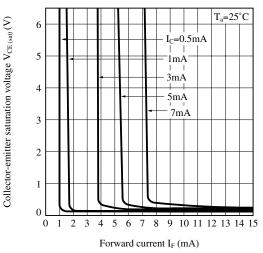




Fig.13 Response Time vs. Load Resistance

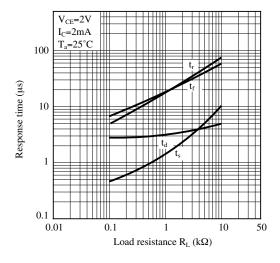
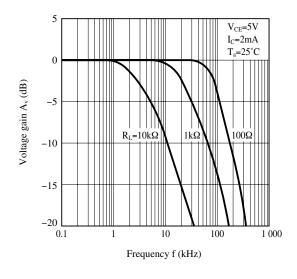


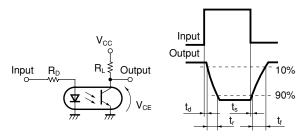
Fig.15 Frequency Response



Remarks : Please be aware that all data in the graph are just for reference and not for guarantee.

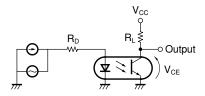
Fig.14 Test Circuit for Response Time

PC844XJ0000F Series



Please refer to the conditions in Fig.13.

Fig.16 Test Circuit for Frequency Response



Please refer to the conditions in Fig.15.



Design Considerations

Design guide

While operating at $I_F < 1.0 \text{mA}$, CTR variation may increase. Please make design considering this fact.

This product is not designed against irradiation and incorporates non-coherent IRED.

Degradation

In general, the emission of the IRED used in photocouplers will degrade over time. In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

☆ For additional design assistance, please review our corresponding Optoelectronic Application Notes.



Manufacturing Guidelines

• Soldering Method

Flow Soldering :

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s. Preheating is within the bounds of 100 to 150°C and 30 to 80s. Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C. Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.

Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances : CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBB and PBDE are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive.

 Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).



■ Package specification

• Sleeve package

Package materials Sleeve : HIPS (with anti-static material) Stopper : Styrene-Elastomer

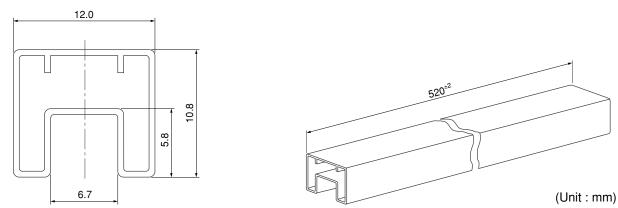
Package method

MAX. 25pcs of products shall be packaged in a sleeve. Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side. MAX. 20 sleeves in one case.

Sleeve outline dimensions

Through-Hole





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• The circuit application examples in this publication are provided to explain representative applications of SHARP devices and are not intended to guarantee any circuit design or license any intellectual property rights. SHARP takes no responsibility for any problems related to any intellectual property right of a third party resulting from the use of SHARP's devices.

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(i) The devices in this publication are designed for use in general electronic equipment designs such as:

- --- Personal computers
- --- Office automation equipment
- --- Telecommunication equipment [terminal]
- --- Test and measurement equipment
- --- Industrial control
- --- Audio visual equipment
- --- Consumer electronics

(ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

PC844XJ0000F Series

with equipment that requires higher reliability such as:

- --- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.

(iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:

- --- Space applications
- --- Telecommunication equipment [trunk lines]
- --- Nuclear power control equipment
- --- Medical and other life support equipment (e.g., scuba).

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