

Excellent Integrated System Limited

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<u>Vishay Semiconductor/Diodes Division</u> <u>SSC53L-M3/9AT</u>

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Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite Datasheet of SSC53L-M3/9AT - DIODE SCHOTTKY 5A 30V DO-214AB

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

SSC53L-M3, SSC54-M3

HALOGEN

FREE

www.vishay.com

Vishay General Semiconductor

High-Current Density Surface Mount Schottky Rectifier



DO-214AB (SMC)

PRIMARY CHARACTERISTICS					
Package	DO-214AB (SMC)				
I _{F(AV)}	5.0 A				
V _{RRM}	30 V, 40 V				
I _{FSM}	175 A				
V _F	0.38 V, 0.42 V				
T _J max.	150 °C				
Diode variations	Single die				

FEATURES

- · Low profile package
- Ideal for automated placement
- Guardring for overvoltage protection
- Low power losses, high efficiency
- Very low forward voltage drop
- High surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: DO-214AB (SMC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test **Polarity:** Color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SSC53L	SSC54	UNIT	
Device marking code		53L	S54		
Maximum repetitive peak reverse voltage	V _{RRM}	30	40	V	
Maximum RMS voltage	V _{RMS}	21	28	V	
Maximum DC blocking voltage	V _{DC}	30 40		V	
Maximum average forward rectified current at T _L (fig. 1)	I _{F(AV)}	5.0		А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	175		А	
Voltage rate of change (rated V _R)	dV/dt	10 000		V/µs	
Operating junction temperature range	TJ	-65 to +150		°C	
Storage temperature range	T _{STG}	-65 to +150			

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	SSC53L		SSC54		LINUT
	IESIC	TEST CONDITIONS		TYP.	MAX.	TYP.	MAX.	UNIT
Maximum instantaneous forward voltage (1)	5.0 A	T _J = 25 °C		0.42	0.45	0.45	0.49	V
	5.0 A	T _J = 125 °C		0.33	0.38	0.36	0.42	
Maximum reverse current at rated V _R ⁽²⁾		T _J = 25 °C	-	0.7	-	0.5	т Л	
		T _J = 125 °C	IR	45	65	40	60	mA

Notes

 $^{^{(2)}}$ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SSC53L SSC54		UNIT		
Typical thermal resistance (1)	$R_{\theta JA}$	60		°C/W		
Typical thermal resistance (*)	$R_{\theta JL}$	2	0	C/VV		

Note

⁽¹⁾ Aluminum substrate mounted

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
SSC53L-M3/57T	0.235	57T	850	7" diameter plastic tape and reel			
SSC53L-M3/9AT	0.235	9AT	3500	13" diameter plastic tape and reel			

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

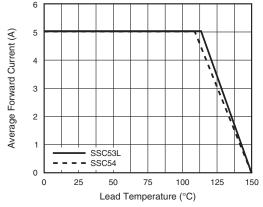


Fig. 1 - Forward Current Derating Curve

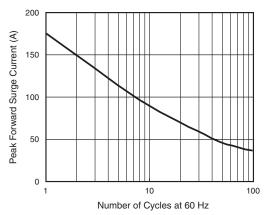


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

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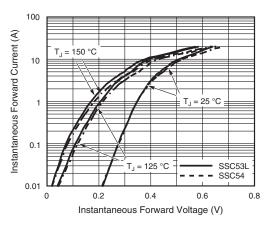


Fig. 3 - Typical Instantaneous Forward Characteristics

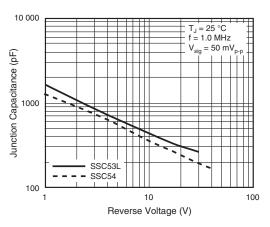


Fig. 5 - Typical Junction Capacitance

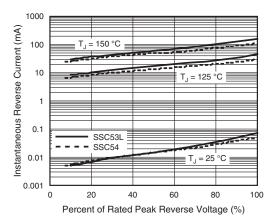
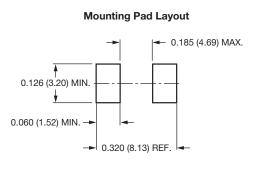


Fig. 4 - Typical Reverse Characteristics

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

DO-214AB (SMC) Cathode Band 0.246 (6.22) 0.126 (3.20) 0.220 (5.59) 0.114 (2.90) 0.280 (7.11) 0.260 (6.60) 0.012 (0.305) 0.006 (0.152) 0.103 (2.62) 0.079 (2.06) 0.008 (0.2) 0.060 (1.52) 0.030 (0.76) 0 (0) 0.320 (8.13) 0.305 (7.75)



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